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Numerical optimization and performance evaluation of ZnPC:PC70BM based dye-sensitized solar cell

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The increase in global energy consumption and the related ecological problems have generated a constant demand for alternative energy sources superior to traditional ones. This is why unlimited photon-energy harnessing is important. A notable focus to address this concern is on advancing and producing cost-effective low-loss solar cells. For efficient light energy capture and conversion, we fabricated a ZnPC:PC70BM-based dye-sensitized solar cell (DSSC) and estimated its performance using a solar cell capacitance simulator (SCAPS-1D). We evaluated the output parameters of the ZnPC:PC70BM-based DSSC with different photoactive layer thicknesses, series and shunt resistances, and back-metal work function. Our analyses show that moderate thickness, minimum series resistance, high shunt resistance, and high metal-work function are favorable for better device performance due to low recombination losses, electrical losses, and better transport of charge carriers. In addition, in-depth research for clarifying the impact of factors, such as thickness variation, defect density, and doping density of charge transport layers, has been conducted. The best efficiency value found was 10.30% after tweaking the parameters. It also provides a realistic strategy for efficiently utilizing DSSC cells by altering features that are highly dependent on DSSC performance and output.

Dye-sensitized solar cells (DSSCs) are a type of photovoltaic device that converts sunlight into electrical energy. Unlike traditional silicon-based solar cells, DSSCs are fabricated through a flexible manufacturing process using a sensitizing dye in order to capture light and generate electrons. These solar cells' main benefits are their cost-effectiveness, lightweight, and feasibility for efficient light harvesting even at relatively low temperatures¹⁻⁶. One of the key advantages of DSSCs is that they can be made from low-cost materials, making them an attractive option for large-scale solar energy generation. Abundant materials worldwide are readily available for manufacturing DSSCs, whereas traditional solar cells often rely on rare and expensive materials. This makes DSSCs a potentially feasible substitute for other types of solar cells with wide applicability: particularly, they are applicable in areas with limited resources or access to high-end technology⁷⁻⁹. Despite their potential, DSSCs still face several challenges that must be overcome before they can compete with traditional solar cells. These include improving their efficiency, stability, and durability, as well as reducing the cost of production. However, ongoing research and development in this area are making significant strides, and DSSCs continue to hold promise as a prospective alternative to traditional solar cells.

Theoretical modeling studies have evolved toward sustainable energy substitutes, energy security, and reducing pollution in constructing solar cells of practical significance. Using theoretical simulations in cell design and production can enhance experimental data and conserve time and money. SCAPS-1D is a unique, user-friendly, and intelligent software employed for modeling and analyzing third-generation solar cells¹⁰⁻¹⁵. Korir

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and colleagues¹⁶ developed a DSSC comprised of solid-state layers via numerical simulation using SCAPS-1D: they optimized numerous factors of the performance of PC61BM as the electron acceptor and reported a power conversion efficiency (PCE) of 5.38%. Jahantigh and Safikhani¹⁷ also employed SCAPS-1D to assess the effectiveness of DSSC under various high-temperature situations: their findings show that the CuI as hole transport layer (HTL) performs better than the other two HTLs in terms of performance and result. By using the SCAPS-1D simulation software, Noorasid et al.¹⁸ modeled and analyzed solid-state DSSC (ss-DSSC) paired with CuI as a hole transport material and found it compelling: after adjusting various parameters, the researchers' findings demonstrate that their designed model with back contact performs better than without back contact, owing to the lower HTL thickness. Ojotu and Babaji¹⁹ used SCAPS for modeling and analyzing ss-DSSC with Poly 3-Hexylthiophene (P3HT) as the HTM: after optimizing their device settings, their device got a PCE of 4.90% and a fill factor (FF) of 56.45%. Nithya and Sudheer²⁰ used the SCAPS-1D program to develop an NFA-OSC. They claimed CuI was a more effective HTL than conventional arrangements. Their method obtains a PCE of 15.68% under ideal circumstances. Abdelaziz and coworkers²¹ found that the productivity of the graded bulk heterojunction (GBHJ) photovoltaic (PV) cell was higher than that of the bulk heterojunction (BHJ) PV cell: they got a PCE of 12.39% using non-fullerene acceptors. CuSCN serves as the HTL in the non-fullerene OSC that was created by Sharma and colleagues²² using SCAPS-1D: they adjusted the settings and got a 20.36% power conversion efficiency.

Since the very first DSSC released by O'regan and Grätzel² in the early 1990s, DSSCs have sparked considerable attention. The device performance of a DSSC can be effectively improved by employing its layers optimally and consistently. The dye gets oxidized by releasing electrons, and the electrolyte compensates for the dye's oxygen loss by supplying electrons to the dye and preventing oxidation. Typically, electrolytes in the liquid state comprise a redox system (R/R) to help them conduct electricity²³. Among these, the iodide/triiodide (I/I₃) redox couple reigns supreme as the most prevalent choice for electrolytes in DSSCs. There are several challenges in preparing liquid electrolytes: some of them are solvent evaporation, low heat stability, and challenging sealability. To avoid this problem in the future, employing a solid-state electrolyte, such as that used in ss-DSSCs^{9,24,25}, is preferable. HTLs have been used widely in DSSCs due to their remarkable features like high stability, no toxicity and no leakage of charge carriers. Also, this helps to transport holes efficiently, yielding increased productivity²⁶. Typical organic and inorganic HTLs have been investigated to address the issues with liquid electrolytes, but their efficiency is still relatively low, in addition to the problems with sealing²⁷. However, several organic and inorganic-based HTLs have the potential of achieving noteworthy results in light harvesting²⁸. A DSSC with titania as an electron extraction layer and N719 as a photo-harvesting layer realized a PCE of 8.5%²⁹. Organic hole acceptor materials such as P3HT and POT were initially used in ss-DSSCs. They displayed PCE of 1% due to inadequate polymer HTL pore filling into the mesoporous TiO₂ film, which causes ineffective charge separation and low charge extraction efficiency³⁰. The PC61BM is a well-known electron acceptor layer employed in photovoltaic cells. ITO/PEDOT:PSS/CH₃NH₃PbI₃/Al cell setup with PC61BM as the electron transport layer (ETL) produces PCE of 3.33%³¹. PC61BM has a higher electron affinity as well as a suitable electron acceptor. The P3HT/PC61BM heterojunction layer transmits carriers and captures solar photons, enhancing DSSCs' photoelectric response³². There are lots of other studies that were reported to lift the efficacy of DSSCs³³. Recent studies have found that the efficiency of these solar cells can approach 12% or more^{34–37}.

The main aim of our study is to assess the potential of the ZnPC:PC70BM structure as a replacement for conventional dyes in DSSCs, offering favorable optoelectronic properties that could potentially improve their efficiency and durability. By analyzing the performance of this new structure, we determine its feasibility for use in commercial applications and its potential to address some of the challenges faced by conventional dyes in DSSCs. Herein, we also explored the performance of ZnPC:PC70BM-based DSSC by varying different parameters, primarily series/shunt resistances and metal contacts that enhance its effectiveness. This study offers valuable insights into the potential of the ZnPC:PC70BM structure and provides useful information for the development of more efficient and durable DSSCs. Additionally, its results were evaluated against actual and simulated data from other pieces of literature.

Device design and simulation

Design and method. The simulated cell in several segments was designed and analyzed using SCAPS (version 3.3.07)³⁵. Figure 1a displays the SCAPS program's step-by-step simulation approach. SCAPS's principal role is to solve one-dimensional semiconductor equations alongside Gummel-type iteration and Newton-Raphson differentiation methods to compute performance characteristics of designed solar cells effectively^{38,39}. The necessary equations are:

$$\frac{\partial}{\partial x} \left(\epsilon_0 \epsilon \frac{\partial \Psi}{\partial x} \right) = -q \left(p - n + N_D^+ - N_A^- + \frac{\rho_{def}}{q} \right) \quad (1)$$

$$-\frac{\partial J_n}{\partial x} - U_n + G = \frac{\partial n}{\partial t} \quad (2)$$

$$-\frac{\partial J_p}{\partial x} - U_p + G = \frac{\partial p}{\partial t} \quad (3)$$

$$J_n = -\frac{\mu_n n}{q} \frac{\partial E_{Fn}}{\partial x} \quad (4)$$

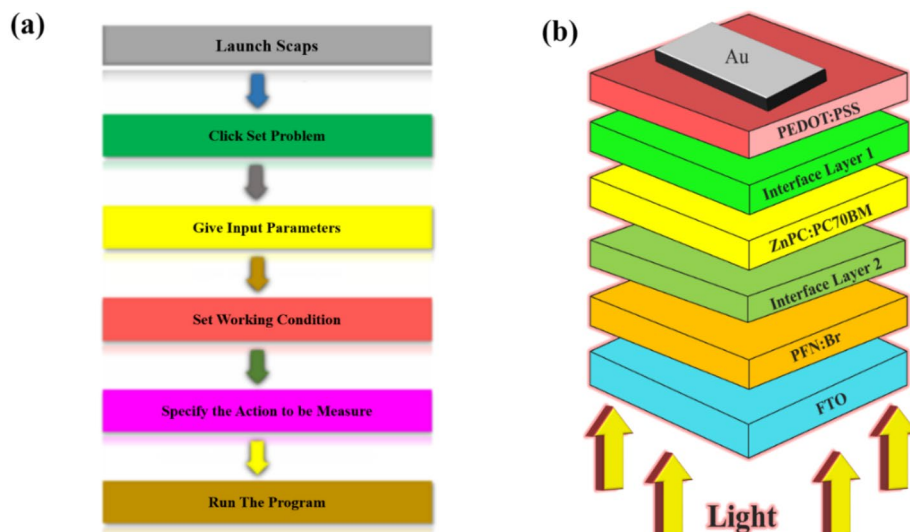


Figure 1. Schematic illustration: (a) Basic steps for numericalization SCAPS-1D, and (b) A diagrammatic representation of the intended DSSC structure.

$$J_p = + \frac{\mu_p p}{q} \frac{\partial E_{Fp}}{\partial x} \tag{5}$$

where ϵ is the dielectric constant, q is the charge on the electron, J_n and J_p are current density for electrons and holes respectively, G is the generation rate, Ψ is the electrostatic field, E_f is the electric field, U is the rate of recombination, x is the thickness, p , and n are allowed concentration of holes and electrons, N_D and N_A are ionized concentration for donors and acceptors, and μ is the mobility of charge carriers.

This research presents a heterojunction arrangement for DSSCs using ZnPC:PC70BM (Zinc-phthalocyanine: 6,6-Phenyl-C71-butyric acid methyl ester) as the dye-sensitizing layer. The other layer of the device includes an HTL which is Poly(3,4-ethylene dioxythiophene)-poly(styrene sulfonate) (PEDOT:PSS), an ETL which is Poly(9,9 bis(3'-(N,N-dimethyl)-N-ethylammonium-propyl-2,7-fluorene)-alt-2,7-(9,9-dioctylfluorene)) dibromide (PFN:Br), a transparent front electrode fluorine-doped tin oxide (FTO), and a back metal electrode gold (Au) as illustrated from Fig. 1b. The energy band diagram, so-called the highest occupied molecular orbital (HOMO)-the lowest unoccupied molecular orbital (LUMO) band diagram of the appropriate DSSC is visualized in Fig. 2a.

DSSCs have unique structural properties that set them apart from other types of photovoltaic cells. A simple photo-electrochemical cell like DSSC is a photoelectrode that combines a semiconductor material (such as TiO_2 in many cases) and sensitized dye molecules^{40,41}. We adopted ZnPC as that material in our case. This electrode, along with a catalytic counter electrode, is deposited on a transparent conducting material. The electrolyte in a DSSC is a combination of organic and inorganic materials, including inorganic salts, a redox couple, an n-type semiconductor, and conducting polymers. The dye sensitization is used to capture light and generate

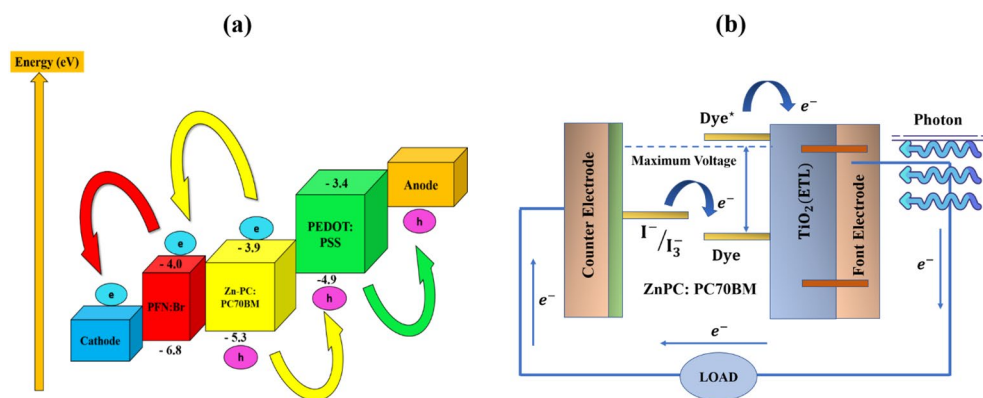


Figure 2. (a) HOMO–LUMO band diagram of the proposed DSSC, and (b) Schematic of the operation principle of DSSC.

electron–hole pairs, while the electrolyte serves as a redox mediator to transfer electrons between the electrodes. This leads to rapid electron injection into the conduction band of the TiO₂ semiconductor material. The transparent conducting material then transports the electrons to the counter electrode through an outer circuit. Meanwhile, the dye is oxidized at its ground state, and the electrolyte regenerates it. The operational diagram of DSSC is illustrated in Fig. 2b. Overall, the unique combination of materials and processes in a DSSC allows for the efficient conversion of light into electrical energy. The presence of sensitizing dye molecules and an electrolyte enables the cell to generate and transport charge carriers effectively, while the nanomaterials used in the photoelectrode provide a large surface area for light absorption.

However, alternative types of solar cells have been developed in recent years, which eliminate the need for dye sensitization and/or electrolyte. For example, some DSSC designs replace the liquid electrolyte with a solid-state electrolyte, which can improve stability and reduce leakage problems. In these cases, the cells may still be called DSSCs because they operate based on similar principles of light absorption and charge transfer, even if the specific components are different. The development of new and alternative materials for use as the active layer or sensitizer in DSSCs, such as metal–halide perovskites, has resulted in higher power conversion efficiencies and improved stability^{42–44}. Additionally, innovative device architectures such as tandem and hybrid solar cells have shown great potential in improving DSSC performance. The ongoing progress in DSSC technology and research is expected to drive further advancements and broader adoption soon.

Parameters for device simulation. The whole list of the simulation parameters used to model the structure's layers were meticulously selected from the studies described in^{17,20,21,45–53}. Tables 1 and 2 provide material parameters for each layer used in this simulation. Other material properties are also suitably adjusted: the thermal velocity of electrons and holes are adjusted at 10⁷ cm/s for instance. Device modeling was made more accessible by using absorption profiles for all layers in the simulation, which were acquired from several sources^{48,50,54–56}. To give a more realistic picture of the device, this design comprises two interface defect layers, marked by the letters IDL1 (PFN:Br/photo-harvesting layer) and IDL2 (photo-harvesting layer/PEDOT:PSS). The AM1.5G spectrum was used to model this cell at a temperature of 300 K. The incident lamp or sun power was set at 1000 W/m². Additionally, all operating point settings and numerical factors were kept at their actual value. Scanning voltage has been set to a range of 0 V to 1 V. All simulations in this software are conducted with the above values.

Parameters	PFN:Br	ZnPC:PC70BM	PEDOT:PSS
Thickness (nm)	10	500	100
Acceptor density (cm ⁻³)	0	0	10 ^{18,21}
Donor density (cm ⁻³)	9 × 10 ^{18,20}	0	0
Effective DOS for VB (cm ⁻³)	10 ^{19,20}	10 ^{19,50}	2.5 × 10 ^{21,46}
Effective DOS for CB (cm ⁻³)	10 ^{19,20}	10 ^{19,50}	1.7 × 10 ^{19,46}
Bandgap (eV)	2.8 ⁴⁷	1.4 ⁴⁸	1.5 ²¹
Relative dielectric permittivity	5 ²⁰	3 ¹⁷	3 ²¹
Mobility of electron (cm ² /Vs)	2 × 10 ^{-6,21}	5 ¹⁷	1.69 × 10 ^{-4,49}
Mobility of hole (cm ² /Vs)	1 × 10 ^{-4,21,47}	5 ¹⁷	1.69 × 10 ^{-4,49}
Electron affinity (eV)	4 ²⁰	3.9 ^{48,53}	3.4 ²¹
Defect density (cm ⁻³)	10 ^{9,47}	10 ^{12,22}	10 ^{9,21}

Table 1. Parameters of material configured in simulation for different layers.

Interface defect density ²⁰	
IDL1 (ETL/Active) defect density	10 ⁹ cm ⁻²
IDL2 (Active/HTL) defect density	10 ⁹ cm ⁻²
Back metal contact properties ^{51,52}	
The electron work function of Au	-5.1 eV
Surface recombination velocity of the electron	10 ⁵ cm/s
Surface recombination velocity of hole	10 ⁷ cm/s
Front metal contact properties ^{51,52}	
The electron work function of TCO	-4.4 eV
Surface recombination velocity of the electron	10 ⁷ cm/s
Surface recombination velocity of hole	10 ⁵ cm/s

Table 2. Device parameters set in the simulation.

Simulation findings and discussions

Photoactive current density and quantum efficiency response. In this study, we employed ZnPC:PC70BM as an absorber material for DSSC, wherein ZnPC acts as a donor component and PC70BM as an acceptor component. The hybrid material ZnPC:PC70BM results in efficient dissociation of electron–hole pair. ZnPC:PC70BM exhibits low electronic bandgap and lower reorganization energy, which results in more charge transfer and higher optical conductivity. As a result, their optoelectronic characteristics are more delicate. According to the findings, ZnPC:PC70BM can be a workable replacement as a conventional dye for DSSCs and might even be used to create DSSCs for future generations.

The numerical analysis is performed on ZnPC:PC70BM-based DSSC, with an active layer thickness of 500 nm. The materials parameters for the absorber and other supporting layers are mentioned in Table 1. The series resistance (R_s) and shunt resistance (R_{sh}) values are set at $1 \Omega\text{cm}^2$ and $1000 \Omega\text{cm}^2$. A low R_s and a high R_{sh} are desirable in a PV cell because they help to minimize power losses due to resistance and prevent localized hot spots and current leakage paths that can reduce cell efficiency and reliability. The current density–voltage (J - V) and the quantum efficiency (QE) curves are illustrated in Fig. 3. The open-circuit voltage (V_{oc}) refers to the measure of recombination and is calculated when zero current flows in a device, and its value was 0.85 V at 500 nm. While short-circuit current density (J_{sc}) is the maximum current drawn from the photovoltaic cell and it is measured when the voltage is zero. The J_{sc} and other output parameters like PCE and FF obtained at 500 nm were 27.44 mA/cm^2 , 14.61% and 62.70%. Resistive losses in photoactive and charge-transporting layers are responsible for the modest inflection at the end of the J - V curve. This indicates the presence of a series resistance (R_s), which is shown by a negative value for the inverse slope of the current–voltage curve near voltage end. The QE graph of a solar cell is expected to have a rectangular or square shape, but this ideal shape can be reduced by various factors such as recombination, reflection losses, and surface passivation. The observed variability in the QE spectra, as seen in Fig. 3a, can be attributed to these losses. The QE can be determined by the charge carriers being transported and collected by the electrodes. In this particular case, front surface recombination, low diffusion length, and reflection losses result in a drop in QE from 400 to 550 nm. However, the QE rises again from 550 to 700 nm due to a significant increase in charge carrier production from light absorption, which improves

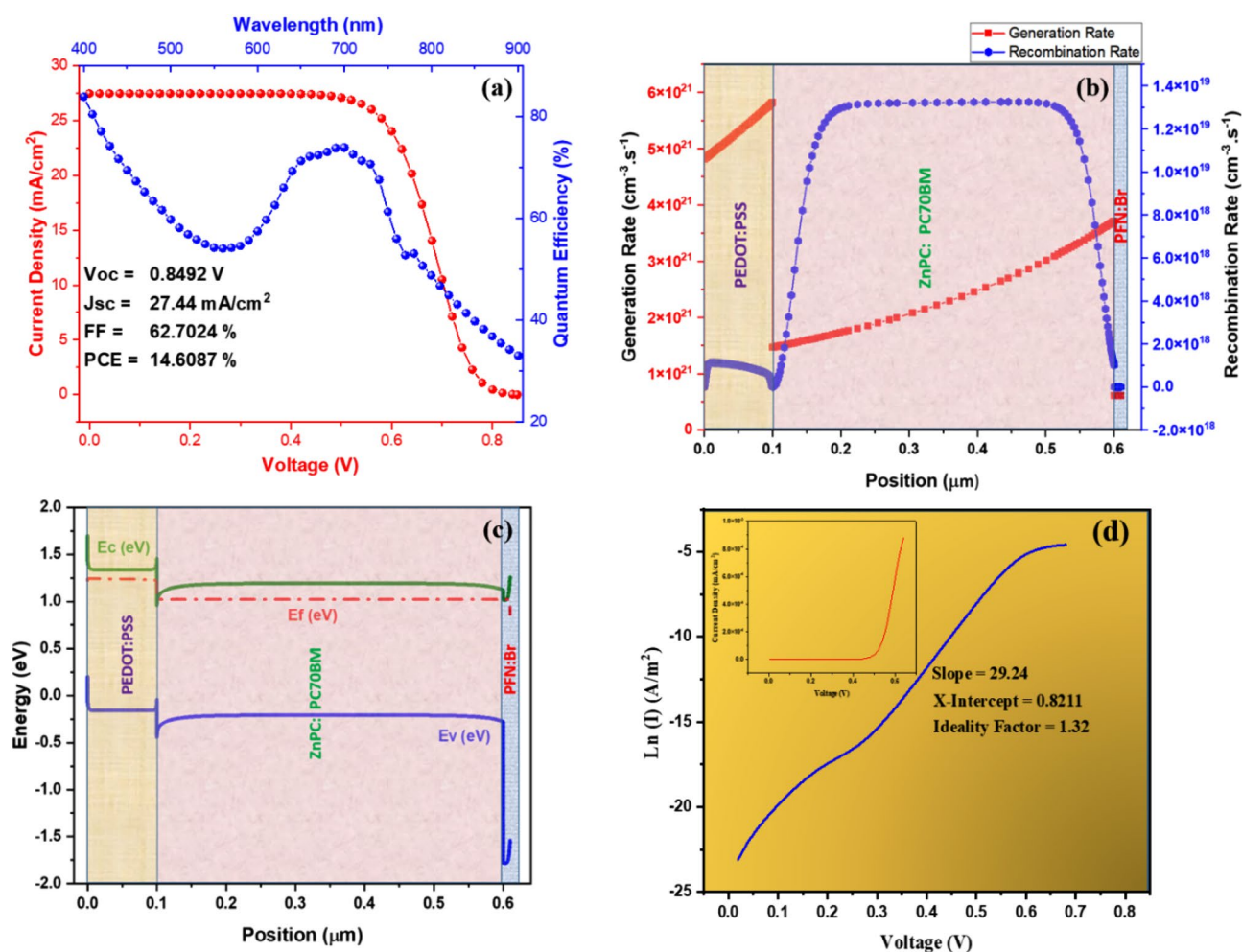


Figure 3. (a) J - V and QE curve of the intended device (b) Generation and recombination rate of photogenerated carriers in device (c) Energy band structure of purposed device, and (d) $\ln(I)$ vs voltage with an inset representing dark J - V measurement.

the collection probability and QE of the device. The analysis shows that the mean QE obtained for a 500 nm thickness of the absorber layer is 70%.

It is also important to note that the QE is a measure of the efficiency of the solar cell at converting photons into charge carriers (electrons and holes), while the J_{sc} is a measure of the maximum achievable current density. The two values are related but not identical, and it is possible for a cell to have a high J_{sc} but a lower QE if there are losses in charge collection or other factors that reduce the efficiency of the conversion process.

Figure 3c depicts the band-alignment of the proposed device structure. Excitons are produced when sunlight strikes the photoactive material (ZnPC:PC70BM). Excitons are confined electron–hole pairs that must be separated to transport and extract charge carriers effectively. Two different materials with suitably matched band levels are utilized to isolate the excitons. These materials are referred to as donor and acceptor. At the heterojunction of these materials, excitons are commonly separated, allowing free electrons and holes to be transmitted by charge-carrying layers (i.e. PFN:Br and PEDOT:PSS). To maximize charge carrier extraction at the electrode, the HOMO level of HTMs should be greater than the valence band of the absorber layer for hole extraction.

Figure 3b depicts the device's carrier production and recombination rates due to optical excitation. Since the light entered from ETL side, the generation and recombination rates of carriers in the cell are lowered from the ETL side to the HTL side in the photo-harvesting layer. The recombination rate is relatively high in the photoactive material due to many defects. Despite this, photo-generated carriers are abundant due to the absorber material's narrow bandgap and considerable thickness. That results in increased absorption and an improvement in the performance characteristics of a cell.

DSSCs are a promising cell technology for converting sunlight into electricity. One important parameter that affects the efficiency of DSSCs is the ideality factor, which is a measure of how closely the cell's behavior matches ideal photovoltaic behavior. In a DSSC, the ideality factor is related to the recombination of charge carriers in the cell. When a photon is absorbed by the dye in the cell, an electron is excited and moves into the conduction band of the semiconductor. The electron then travels through the semiconductor to the electrode, where it can be collected as current. However, some of the electrons may recombine with holes before they reach the electrode, which causes a reduction of the cell efficiency. Figure 3d represents dark J–V measurements for the determination of ideality factor.

The ideality factor can be determined experimentally by measuring the current–voltage characteristics of the DSSC and fitting the data to the equation^{57,58}:

$$I = I_0 \exp\left(\frac{qV}{NkT} - 1\right) \quad (6)$$

where I is the current, I_0 is the saturation current, q is the elementary charge, V is the voltage, N is the ideality factor, k is the Boltzmann constant, and T is the temperature. The ideality factor can be obtained from the slope of the plot of $\ln(I)$ vs V , and it quantifies how many charge carriers recombine during this procedure. In other words, the ideality factor quantifies the level of charge carrier recombination and, consequently, related to determining how efficiently a DSSC transforms sunlight into energy. In our intended design, the ideality factor was 1.32.

Influence of photoharvesting layer thickness on cell functionality. The photoactive layer of any PV cell is critical to the device's operation and output. This study increased the thickness of the harvesting layer from 100 to 1000 nm. Keeping all other variables constant allowed us to investigate how this variance affected the device results. The J–V characteristics and the effect of increasing thickness on the DSSC performance parameters are shown in Fig. 4. Figures 4a–e show that there is a relationship between the variety in device outcomes and the depth of the active material. The J–V curve is remarkably enhanced initially as the thickness of the photoactive material increases. This is attributed to the abundant formation of charge carriers. However, as the thickness continues to increase, the degree of improvement in the J–V curve begins to diminish. This is due to the increment in the recombination rate; such an increase is potentially surpassing the generation rate, leading to a decline in the carrier diffusion length.

The J_{sc} , V_{oc} , and PCE values increase noticeably when the absorber layer thickness increases from 100 to 500 nm. The increase in the concentration of electron–hole pairs in the photoactive layer causes this. As the thickness of the photoactive layer increases from 500 to 1000 nm, it will take longer for the produced carrier to reach the appropriate electrodes, resulting in a higher low diffusion and low generation rate. Which causes the J_{sc} and V_{oc} to increase steadily. The largest J_{sc} and V_{oc} achieved is 34.64 mA/cm² and 0.86 V when the device's thickness is 1000 nm. The fill factor reduces from 64.56% to 61.58% as the thinness of an absorber material grows to 1000 nm. When the FF is high, the cell can supply all of the energy it produces to the electrical demand. That might be because the absorber material is relatively thick, which boosts the cell's series resistance and increases the rate at which the cell's internal power is depleted. Therefore, the appropriate thickness value is necessary to yield better outcomes, and the low thickness makes a cost-effective PV cell.

Influence of series and shunt resistance on cell functionality. Resistive losses, which often takes place by carrier recombination and current leakage, limit the performance of DSSCs. Shunt resistance and series resistance are responsible for these losses. The behavior of the J–V curve has impacted due to these parasitic losses. The term "internal resistance" (R_i) referred to the resistance present in PV cells and caused by the materials, electrodes, and interface barriers^{59,60}. R_i was predominantly driven by the electrical resistance associated with the contacts, and the electrical loss was present in both the active and supporting layers. The series resistance (R_s) has little impact on V_{oc} but significantly impacts FF and PCE. Furthermore, it lowers J_{sc} . Therefore, during

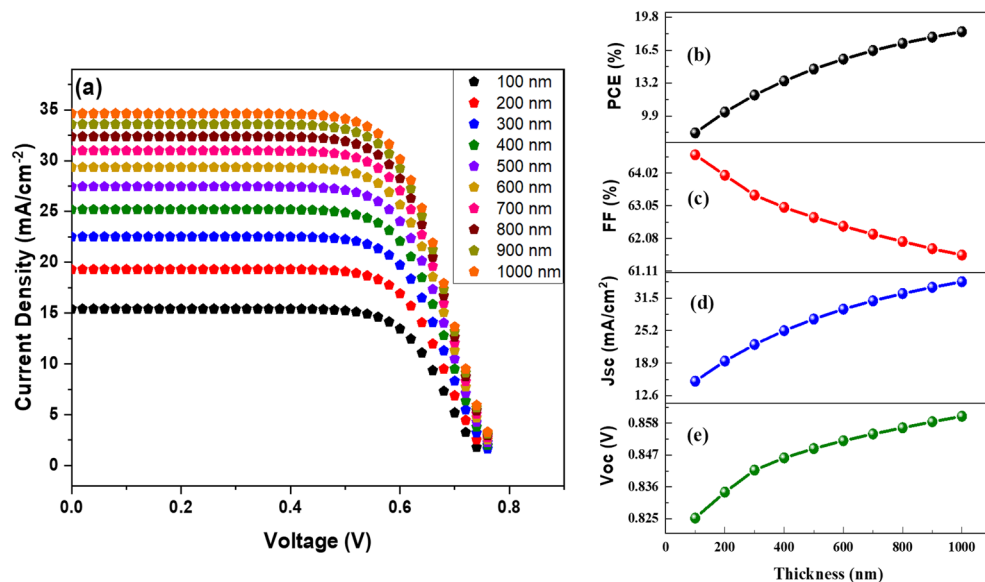


Figure 4. (a) J–V comparison at different values of thickness, (b) PCE, (c) FF, (d) J_{sc} , and (e) V_{oc} in relation to photoactive layer thickness.

real-cell production, it is essential to conduct structural optimization by studying the impact of series and shunt resistances on cell performance. Previous studies have shown that increasing R_s causes a decrease in J_{sc} but has no effect on V_{oc} in a solar cell. The R_{sh} is determined by device architecture, which includes charge recombination mechanisms (edge effects) such as active layer pinholes and recombination losses^{60,61}.

Consequently, low R_{sh} causes a loss in photovoltage and may also have a slight effect on the collected photocurrent. In this investigation, we have varied the series and shunt resistance from 1 to 5 Ωcm^2 and 100–600 Ωcm^2 to observe the influence on device performance and output parameters. Under ideal illumination, the typical J–V characteristic of a photovoltaic cell may be characterized using the Shockley equation.

$$J_{SC} = J_0 \left[\exp \left(\frac{e(V - JR_S)}{NkT} \right) - 1 \right] + \frac{V - JR_S}{R_{SH}} - J_{Ph} \quad (7)$$

where J_{Ph} denotes photocurrent density, J_0 denotes dark saturation current, T is room temperature, and e denotes charge on an electron. In ideal conditions, R_s would be 0, and R_{sh} would be limitless.

Figures 5a–e and 6a–e demonstrate the J–V aspects and the influence of R_s and R_{sh} on the DSSC performance parameters, respectively. The J–V curve shows that an increase in series resistance causes a drop in the J–V curve since it is measured from the slope of the J–V curve at the voltage end while R_{sh} 's J–V curve has improved due to its quick rise. The slope of the J–V curve towards the current end is used to compute R_{sh} . Figure 5b–e show that when series resistance increases, PCE and FF decline considerably from 14.61 to 11.52% and 62.70–49.46%, respectively. Simultaneously, a slight change in V_{oc} and J_{sc} values has been detected. The decline in the value of PCE may be ascribed to the impediment provided by series resistance to charge carrier transfer.

As a result, the carrier diffusion length shortens. These power losses add to parasitic (electrical) resistance losses. Consequently, the value of series resistance should be as low as feasible in order to obtain effective output from the cell. Figure 6b–e show that when shunt resistance increases, PCE, FF, and V_{oc} increase dramatically from 11.42 to 14.06%, 57.88–66.09%, and 0.72–0.77 V, respectively. In contrast, a negligible influence on J_{sc} value has been seen. The improvement in DSSC performance parameters is due to an increase in shunt resistance value, which increases the generation rate via the reduction of recombination losses. It has come to the conclusion that the right and optimum value of R_s and R_{sh} is critical for increasing production.

Influence of active layer defect density on cell functionality. The quality and structure of the light-harvesting layer significantly influence the device's performance for any generation of solar cells. That is because the light-harvesting layer absorbs light in the form of photons. The poor film quality and characteristics increase the number of defect states and the rate of recombination, which reduces device performance. It is essential to the endeavor's success that the device has a low defect density (see Fig. 7).

In active materials, trap states increase as the defect concentration grows. Trap states are localized energy levels within the bandgap of the active layer material, where electrons or holes can get trapped. When an electron or hole gets trapped in a trap state, it cannot contribute to the generation of electricity. This leads to a reduction in the carrier lifetime and an increase in recombination, which reduces the efficiency of the solar cell. The effect of trap states on the performance of a solar cell depends on their concentration and energy level. High concentrations of trap states, as well as trap states with energy levels close to the band edges, can have a significant impact on the performance of the solar cell.

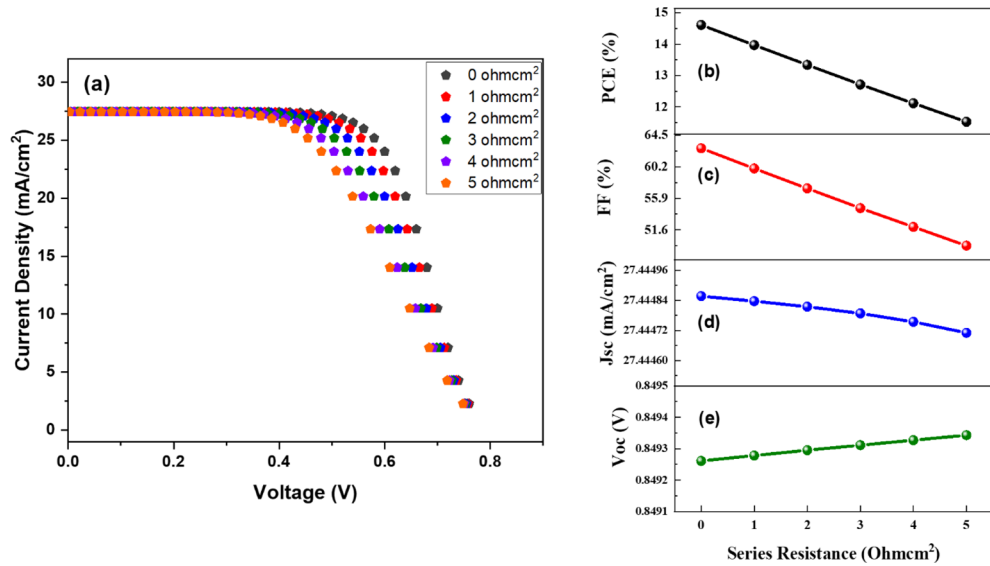


Figure 5. (a) J–V comparison at different values of series resistance, (b) PCE, (c) FF, (d) Jsc, and (e) Voc in relation to series resistance.

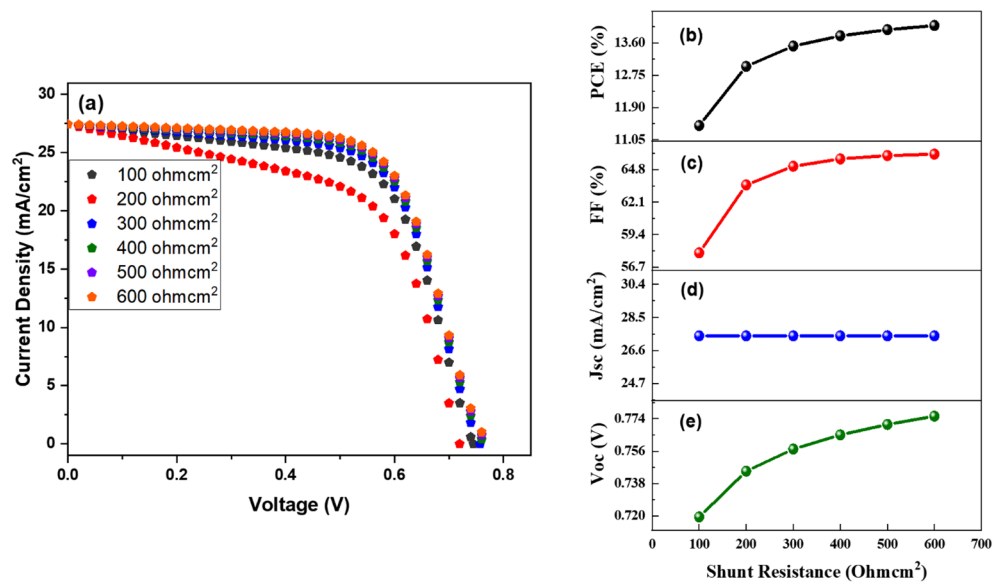


Figure 6. (a) J–V comparison at different values of shunt resistance, (b) PCE, (c) FF, (d) Jsc, and (e) Voc in relation to shunt thickness.

Figure 7b–e illustrate the output device’s performance in relation to defect density (N_t), and Fig. 7a represents the J–V curve at diverse values of defect concentration of a photoactive layer. With increasing N_t , carrier lifetime reduces, resulting in a higher recombination rate, negatively influencing output performance. It has demonstrated that a low defect density value leads to improved cell productivity. In that case, fewer traps are present in the absorber layer, and the generation rate is relatively high. In contrast, excessive defect concentrations develop additional recombination centers and traps, degrading the device’s overall performance. It is feasible to enhance the device’s output by lowering the defect density below 10^{15} cm^{-3} , which results in a higher current density. By altering the defect density from 10^{12} to 10^{15} cm^{-3} , the device output parameters PCE, FF, J_{sc} , and V_{oc} reduced from 14.60 to 9.29%, 62.70–40.20%, 27.44–27.35 mA/cm^2 , and 0.85–0.80 V.

Impact of carrier mobility of photoactive layer on cell functionality. One of the necessary factors in enhancing the productivity and outcomes of DSSCs is charge carrier mobility. The average mobility of charge carriers describes how easily charge carriers are moving from one place to another without trapping, described by a relation.

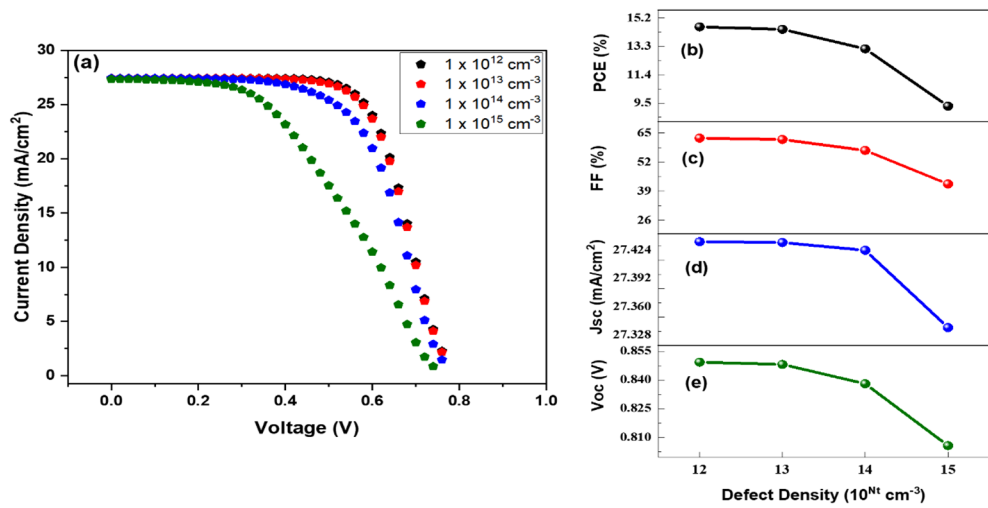


Figure 7. (a) J–V comparison at different values of defects, (b) PCE, (c) FF, (d) J_{sc} , and (e) V_{oc} in relation to defects.

$$\mu_e(n) = \frac{\mu_e^0 n_{free}}{n_{free} + n_{trap}} \tag{8}$$

In this work, charge carrier mobility was altered from 5×10^{-4} to $5 \text{ cm}^2/\text{Vs}$ to investigate the impact on device performance. It has been determined that the optimum power conversion occurs at carrier mobility of $5 \text{ cm}^2/\text{Vs}$. The outcomes and J–V characteristics of DSSC at various charge carrier mobility values has shown in Fig. 8a–e. Short-circuit current density decreases as carrier mobility reduces due to dissociation probability, reducing efficiency and FF. When carrier mobility improves, J_{sc} increases, potentially leading to high efficiency and high FF. While V_{oc} rises as mobility advances to $5 \times 10^{-3} \text{ cm}^2/\text{Vs}$, it subsequently falls as charge carrier transport improves due to less internal power depletion and a destabilized electric field in the depletion area.

Influence of HTL and ETL layer thickness on cell functionality. The charge transport layers serve as a crucial component in facilitating the extraction and transport of holes and electrons from the active layer to the electrode. For this reason, the HTL and ETL directly affect the performance of a solar cell. The performance

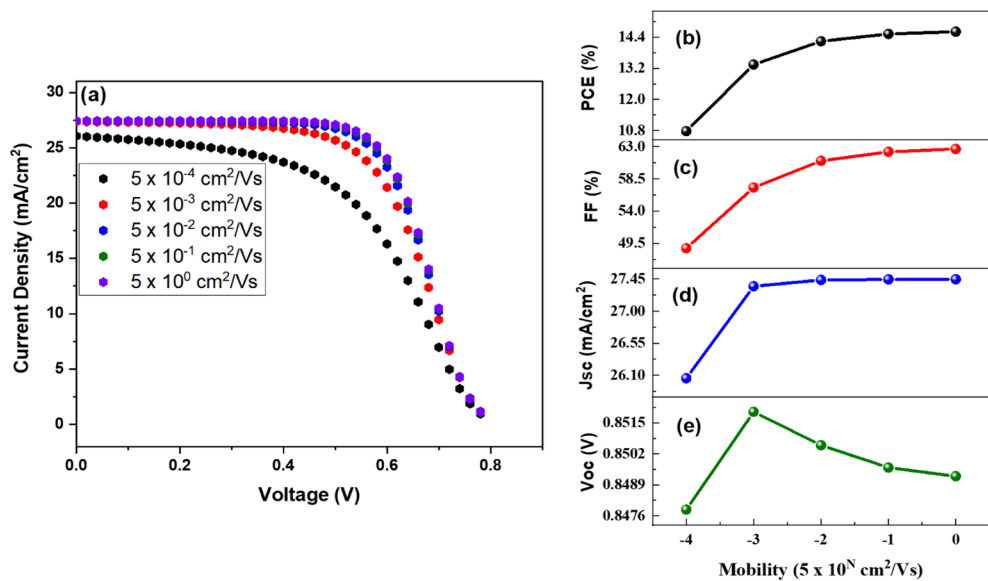


Figure 8. (a) J–V comparison at different values of carrier mobility, (b) PCE, (c) FF, (d) J_{sc} , and (e) V_{oc} in relation to photoactive layer mobility.

is mainly determined by the thickness of the HTL and ETL. The HTL and ETL are responsible for collecting and transporting the holes and electrons generated by the absorption of light.

In determining the performance of DSSCs, the optimal selection of the charge transport layers is important. By carefully choosing the hole transport material (HTM) and electron transport material (ETM), one can enhance the efficiency of charge transmission and collection at the electrodes. In this particular investigation, PEDOT:PSS and PFN:Br were employed as the HTL and ETL due to their promising optical and electrical characteristics. To comprehend the impact of HTM thickness on output parameters, the thickness of PEDOT:PSS was varied from 50 to 100 nm. Notably, as the thickness of the HTM layer increased, there was a substantial improvement in the central output parameters (i.e., PCE, Voc, and Jsc). This enhancement can be attributed to the exceptional charge transport properties exhibited by the HTL, which facilitates more efficient charge transfer. Additionally, the increased thickness resulted in better interaction between the HTL and the absorber layer, further augmenting the performance of the DSSCs. The grave impact on the output parameters can be clearly recognized in Fig. 9a–d. However, it is worth noting that the increase in HTM thickness had an adverse effect on FF. As you can see, FF declines as the thickness of the HTM layer increases. This outcome suggests that while the charge transport properties were improved, there might have been an increase in charge recombination or resistance within the device, leading to a reduction in FF.

ETL thickness directly affects the distance that electrons must travel to reach the electrode. As the ETL thickness increases, the path length for electron transport also increases. This can result in increased carrier recombination losses, as carriers have a higher probability of recombination before reaching the electrode. The thickness of the ETL contributes to the overall series resistance of the solar cell. It hinders the charge carriers' flow through the ETL. This can result in higher resistive losses, reducing the effective current output of the solar cell. Therefore, an optimal ETL thickness is required in order to minimize the series resistance and maximize the power conversion efficiency. Therefore, an excessively thick ETL can hinder efficient charge extraction, leading to reduced photocurrent and overall device performance, as observed in Fig. 9e–h. The ETL thickness can affect the optical absorption characteristics of the solar cell. Thicker ETLs may absorb more incident light, thereby reducing the amount of light reaching the active layer for photoconversion. This can decrease the overall photocurrent generation, leading to lower device performance. In practice, the optimal HTL and ETL thickness depends on various factors, such as the specific materials used, device architecture, and fabrication techniques. It often requires empirical optimization and device engineering to determine the ideal thickness for a given solar cell configuration.

Influence of HTL and ETL layer defect density on cell functionality. The defect density of the HTL and ETL in a solar cell has a notable impact on its performance. Defects in the HTL and ETL can arise from various sources, such as impurities, structural imperfections, or fabrication processes, and can significantly affect the charge transport and recombination processes within the device.

Figure 10 illustrates the variation in the characteristics of DSSCs when the defect density of HTL (see Fig. 10a–d) and ETL (see Fig. 10e–h) is altered. The visual representation reveals that a lower trap density of

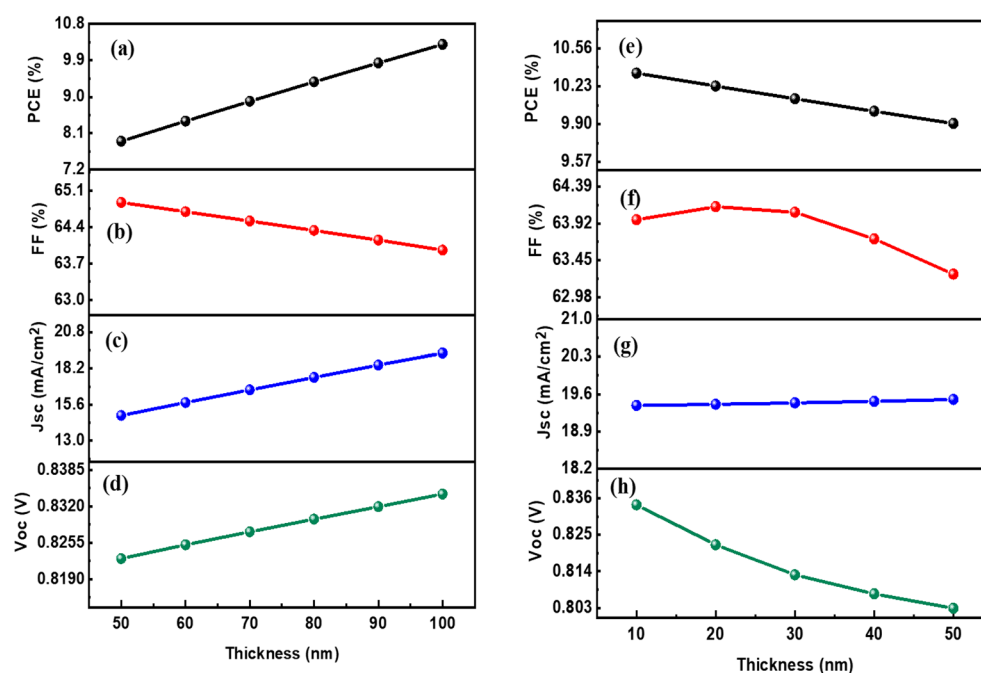


Figure 9. Parameters in relation to thickness of HTL layer (left panels): (a) PCE, (b) FF, (c) Jsc, and (d) Voc. Parameters in relation to thickness of ETL layer (right panels): (e) PCE, (f) FF, (g) Jsc, and (h) Voc.

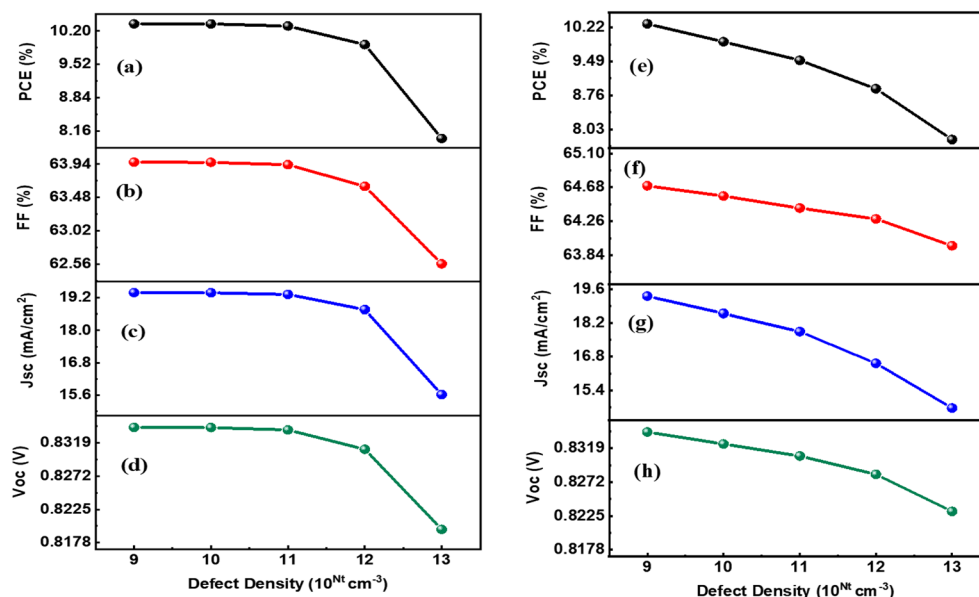


Figure 10. Parameters in relation to defect density of HTL layer (left panels): (a) PCE, (b) FF, (c) Jsc, and (d) Voc. Parameters in relation to defect density of ETL layer (right panels): (e) PCE, (f) FF, (g) Jsc, and (h) Voc.

HTL and ETL yields more favorable outcomes for cell performance. This is primarily due to the presence of fewer traps and a higher growth rate under such conditions. Consequently, both the Voc and Jsc experience an increase, leading to higher PCE and FF. Also, higher defect densities result in the generation of more capturing states, which exacerbates recombination processes within the device. As a result, the overall performance of the DSSCs is compromised. To achieve optimal results, it is recommended to maintain a defect density of $1 \times 10^9 \text{ cm}^{-3}$ for both the HTL and the ETL. This particular defect density value strikes a balance between minimizing trap-related losses and maximizing charge transport efficiency, thereby optimizing the performance of the DSSCs.

The defect density of the charge transport layers significantly influences solar cell performance by affecting charge transport, recombination pathways, series resistance, and carrier selectivity. Minimizing defects and optimizing the quality of the HTL and ETL is crucial for achieving high-efficiency solar cells with improved charge extraction, reduced recombination losses, and enhanced overall device performance. To optimize DSSC performance, it is essential to minimize the defect density in the charge transport layers. Techniques such as careful material selection, improved fabrication processes, and defect passivation strategies can help reduce defect densities. Additionally, advanced characterization techniques can be employed to identify and quantify the nature and impact of defects in the HTL and ETL, aiding in the development of strategies to mitigate their adverse effects.

Influence of HTL and ETL layer doping density on cell functionality. The doping density of the HTL and ETL in a solar cell can have a significant influence on its performance. The HTL is typically a p-type semiconductor material responsible for transporting holes from the active layer to the electrode, while the ETL is an n-type semiconductor material that facilitates the movement of electrons from the light-absorbing layer (e.g., the active layer or absorber) to the electrode. The doping density of the HTL and ETL affects their electrical conductivity and energy level alignment with the adjacent layers.

In this study, the influence of HTM and ETM layer doping density on solar cell performance was investigated. We varied the doping concentration of the HTM layer from 1×10^{16} to $1 \times 10^{20} \text{ cm}^{-3}$ and observed its effect on the device's performance, as depicted in Fig. 11a–d. The results indicated that increasing the doping concentration of the HTM layer had a positive impact on the solar cell's output performance. Specifically, it was found that, as the doping concentration increases, the FF and PCE of the solar cell improve. However, there was an initial decrease in Jsc and Voc. This phenomenon can be attributed to a decrease in carrier lifetime and an increase in the recombination rate at the interface between the HTM layer and the absorber layer.

In addition, the ETM layer doping enhances the device's performance when its density is altered from 1×10^{16} to $1 \times 10^{20} \text{ cm}^{-3}$. Doping the ETL helps to enhance charge carrier mobility, i.e., the ability of charge carriers to move through the material. Higher doping densities generally result in improved electron mobility, enabling faster and more efficient charge transport across the ETL. This, in turn, reduces carrier recombination losses and increases the overall current collection efficiency and performance of the DSSC, as illustrated in Fig. 11e–h. A slight decline in Jsc value could also be observed after increasing doping concentration. When the doping density of the ETL is raised, the density of available charge carriers (electrons) also increases. This higher concentration of carriers can enhance the likelihood of carrier recombination processes occurring within the ETL itself. The increased doping density can exacerbate carrier recombination rates, thus leading to a decline in Jsc. Furthermore, a higher doping density in the ETL can create a greater potential barrier for electron extraction

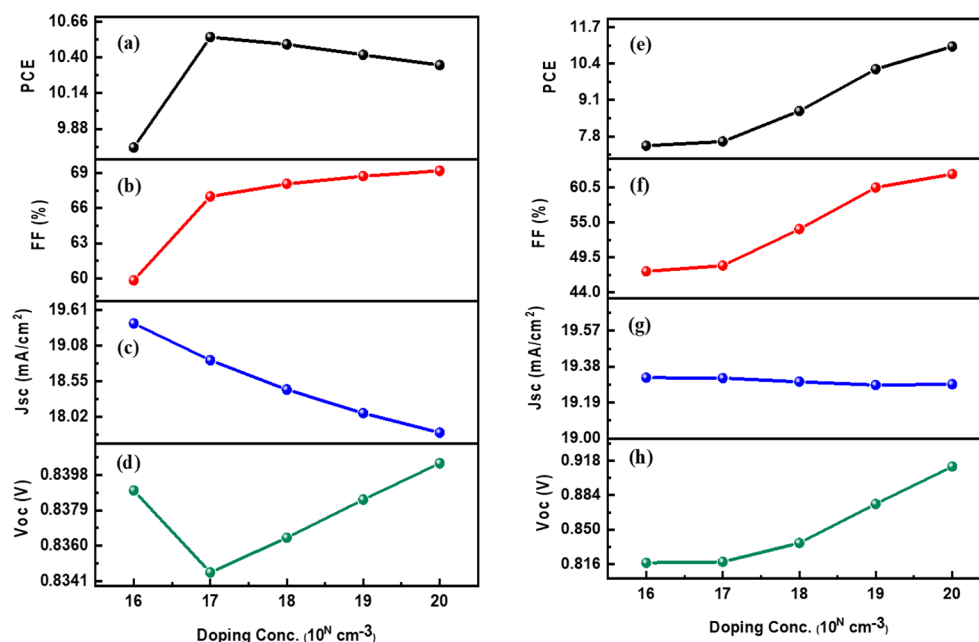


Figure 11. Parameters in relation to doping density of HTL layer (left panels): (a) PCE, (b) FF, (c) Jsc, and (d) Voc. Parameters in relation to doping density of ETL layer (right panels): (e) PCE, (f) FF, (g) Jsc, and (h) Voc.

at the interface between the ETL and the absorber layer. This barrier arises from the energy level alignment between the ETL and the absorber layer, which can be influenced by the doping density. If the potential barrier becomes too high, it can impede the efficient extraction of electrons from the absorber layer into the ETL, resulting in reduced Jsc.

Hence, a well-adjusted balance between the doping density and other device parameters is necessary to optimize the solar cell's performance in terms of performance parameters. After careful analysis, we determined that an optimized doping density value for the HTM and ETM layers could be $1 \times 10^{18} \text{ cm}^{-3}$ and $9 \times 10^{18} \text{ cm}^{-3}$, respectively. This particular doping concentration demonstrated several benefits. Firstly, it increased the cell conductivity, leading to a reduction in internal power depletion and series resistance. The decrease in series resistance subsequently contributed to improved PCE, FF, and Voc. These findings shed light on the significance of carefully selecting the doping density of the HTM and ETM layers to optimize the overall efficiency of solar cells.

Influence of varying back metal contact on device performance. The work function is the minimum amount of energy required to move an electron from a metal surface to vacuum. The purpose of the contacts is to start conduction in an external circuit. According to reports^{62–64}, an increase in the work function's value can boost the efficiency of PV cells. This is explained by the fact that the majority carrier's barrier height decreases as the work function value increases, resulting in ohmic contact. As a result, an increment of the metal's work function value raises the open circuit voltage and energy conversion efficiency. In this research, simulations had conducted to narrow the search for an appropriate earth-abundant metal that may serve as the back contact in the suggested device configuration. Previous research has found that DSSCs rely on metallic connections for dye regeneration, which indicates that the device performance is likely to be favorable for silver (Ag) and gold (Au). These are two most used metals with work functions of 4.7 eV and 5.1 eV and have been considered as suitable materials for dye regeneration to overcome the restrictions associated with the use of liquid electrolytes and to avoid the use of expensive HTLs.

However, due to the high cost of these materials, their use in producing solar cells that are sold at affordable prices is limited. Despite this, we have investigated the viability of many alternative metallic materials as feasible and affordable solar cell device components during this research. Simulations have been run on copper, silver, iron, platinum-oxide, carbon-copper, and gold potential dye-regenerating metallic contacts.

Figure 12a–e depict the effect of J–V characteristics and the influence of altering work function (metal-contact) values on the DSSC performance parameters. All of the performance parameters (PCE, FF, Jsc, and Voc) are being improved when the value of the work function grows due to effective charge carrier transport and collection at back-metal contact. The PCE increased dramatically from 11.75% to 14.61%. It can be concluded that devices with high metal work functions are necessary for better photovoltaic performance.

Table 3 compares the numerical analysis of the DSSCs with experimental data. The PCE of the simulated device model is better than experimental data from available research^{65–71}. The difference between the theoretical and empirical results may be the consequence of reflection losses, the involvement of series and shunt resistances, and the effect of operating temperature. We discovered that correct parameters and layer configuration optimization can improve efficiency. However, PCE values greater than 8% have been observed for some dyes

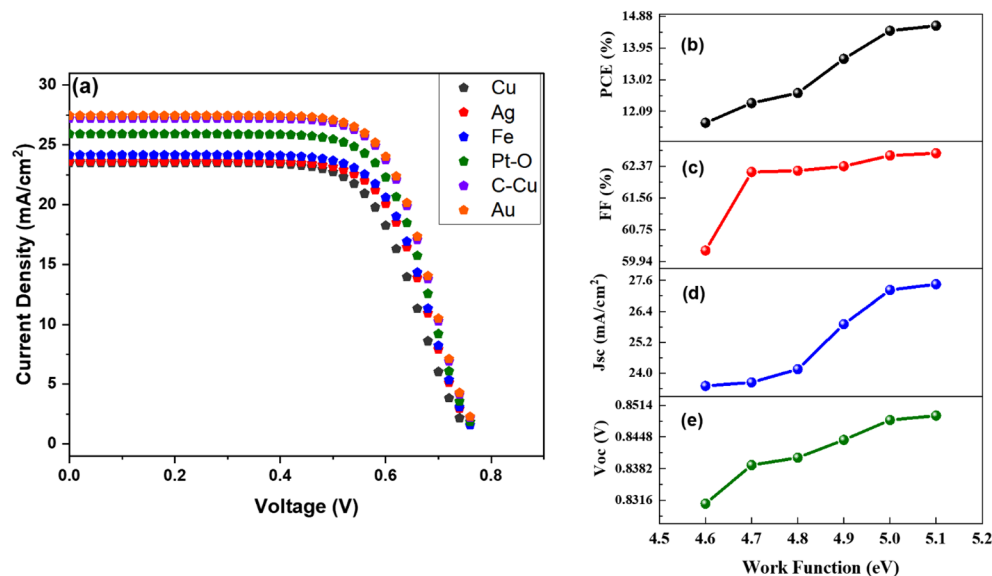


Figure 12. Parameters in relation to metal work function. (a) J–V comparison at different values of work function, (b) PCE, (c) FF, (d) Jsc, and (e) Voc.

Device configuration	PCE (%)	Refs
Experimental results		
4-HBa-ZnPc	2.99	65
4-MKBa-CoPc	4.18	66
FTO/TiO ₂ /N719/CuSCN/C	4.24	66
P3HT:PCBM/ZnPc	5.3	67
FTO/TiO ₂ / CsPbBr ₃ /ZnPc/C	7.67	68
FTO/TiO ₂ / CsPbBr ₃ /ZnPc:P3HT/C	10.03	68
FTO/dye & TiO ₂ (TNA)/Pt	8.34	69
ITO/PEDOT:PSS/PTB7:ZnPc:PC71BM/Ca	8.52	70
PTB7:PC70BM	9.55	71
Simulation results		
FTO/PFN:Br/ZnPc:PC70BM (200 nm)/PEDOT:PSS/Au	10.29	This study
FTO/PFN:Br/ZnPc:PC70BM (500 nm)/PEDOT:PSS/Au	14.61	This study

Table 3. Comparison of simulated result with experimental results.

(see Fig. 13). The use of ZnPC:PC70BM as an absorber material with efficient electron and hole transport materials (i.e., PFN:Br and PEDOT:PSS) has improved the optoelectronic properties and might replace traditional dyes, which are concerned with stability and volatility issues for next-generation DSSCs. Consequently, this research also provides theoretical guidance for the actual usage of DSSC by improving its characteristics for the next-generation DSSC.

Conclusions

This work has offered valuable insights into how the characteristics of DSSCs might be altered during the commercial manufacture of solar cells. The SCAPS-1D software package is utilized to optimize and design the desired DSSC structure of FTO/PFN:Br/ZnPc:PC70BM/PEDOT:PSS/Au. The cell's performance was maximized by making suitable modifications to thickness, series and shunt resistances, metal-contact work functions, carrier mobility, and trap density. The analysis shows that the material's photovoltaic properties have improved by reducing the defect density, adjusting the absorber layer thickness appropriately, and raising the charge carrier mobility. According to our findings, the best value for thickness is 500 nm, defect density is $1 \times 10^{12} \text{ cm}^{-3}$ and carrier mobility is $5 \text{ cm}^2/\text{Vs}$ to achieve high productivity. By optimizing all the prime parameters, we could construct DSSC with a high efficiency of 14.61% and the simulated result suggests that the performance of DSSC devices will increase in the near future. Consequently, solar energy can potentially increase energy security, provided that the conditions of its production are correctly managed. Numerical simulation of solar cell designs provides companies with essential information while saving time and money. To support the theoretical work described in this research, an experimental approach to the performance of model DSSC solar cells is suggested.

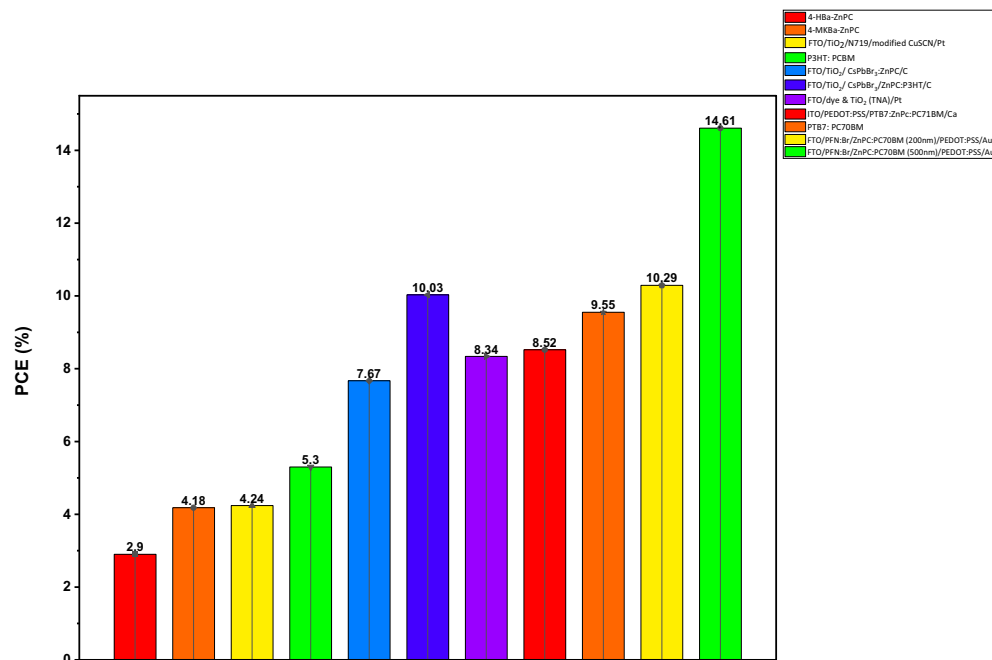


Figure 13. Sketch of comparison between simulated result with experimental results.

Data availability

The authors declare that the data supporting the findings of this study are available within the article.

Received: 21 April 2023; Accepted: 22 June 2023

Published online: 27 June 2023

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Acknowledgements

The authors thank Mark Burgelman and his team for making the SCAPS code available for free. The authors also acknowledge the support provided by Zhejiang University, China, along with the National Research Foundation of Korea (NRF) grant funded by the Korean government (MSIT) (No.: NRF-2021R1F1A1062849), while extending their appreciation to the Deanship of Scientific Research at King Khalid University for supporting this work through large groups (project under grant number R.G.P.2/1/44).

Author contributions

All authors contributed to this article and have read as well as agreed to the published version of the manuscript.

Competing interests

The authors declare no competing interests.

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