Research Article

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Dimension dependence of current injection path in GaInN/GaN multi-quantum-shell (MQS) nanowire-based light-emitting diode arrays

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Abstract: To light emitting diodes (LEDs), solving the common non-uniform current injection and efficiency degradation issues in (0001) plane micro-LED is essential. Herein, we investigated the light emission characteristics of various mesa sizes and different p-electrode areas toward the realization of coaxial GaInN/GaN multi-guantum-shell (MQS) nanowires (NWs)-based micro-LEDs. As the mesa area was reduced, the current leakage decreases, and further reduction of the area showed a possibility of realizing micro-LED with less current leakage. The large leakage path is mainly associated with the defective MQS structure on the (0001) plane area of each NW. Therefore, more NWs involved in an LED chip will induce higher reverse leakage. The current density-light output density characteristics showed considerably increased electroluminescence (EL) intensity

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as the mesa area decreased, owing to the promoted current injection into the efficient NW sidewalls under high current density. The samples with a mesa area of $50 \times 50 \ \mu m^2$ showed 1.68 times higher light output density than an area of $100 \times 100 \ \mu\text{m}^2$ under a current density of $1000 \ \text{A/cm}^2$. In particular, the emission from (1-101) and (10-10) planes did not exhibit an apparent peak shift caused by the quantumconfined Stark effect. Furthermore, by enlarging the p-electrode area, current can be uniformly injected into the entire chip with a trade-off of effective injection to the sidewall of each NW. High performance of the MQS NWbased micro-LED can be expected because of the mitigated efficiency degradation with a reducing mesa area and an optimal dimension of p-electrode.

Keywords: current injection path; electroluminescence (EL); ITO area; micro-LED; MQS nanowire; p-electrode area

1 Introduction

Under the constant innovation of display technology, group III nitride semiconductors are deemed as promising materials for the micro-light-emitting diodes (micro-LEDs) [1, 2]. Because of the high peak brightness, low power consumption, high speed, and cost reduction, micro-LEDs are expected to apply to smartwatches, smartphones, and threedimensional augmented and virtual reality displays [1, 2]. In planar LEDs, the polar (0001) plane-based active structures usually contain piezoelectric field-induced quantumconfined Stark effect (QCSE), which causes a decrease in luminous efficiency, especially in the long wavelength range [3-5]. So far, various efforts have been made toward the realization of micro-LED with different emission wavelengths [6-10], while the degradation issues in external quantum efficiency (EQE) with a decreasing LED chip size were also widely reported [11, 12]. As the device size decreases, the area of the edge with respect to the total emission area increases, leading to a raised ratio of the

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nonradiative recombination centers at the edge surface to the device area [13–16]. Even though the dry etchinginduced surface damages can be partially passivated by atomic layer deposited dielectric films, the recovery is still limited to improve the emission efficiency [17]. In addition, the intrinsic influence of QCSE can cause thermal loss of carriers before they reach the multiple quantum well regions in LEDs, increasing the nonradiative recombination in the p-GaN region, and reducing the EQE at small chip sizes [6, 18, 19]. Furthermore, blue shifts of 9 nm for blue micro-LEDs and 15 nm for green micro-LEDs occur with increasing current density from $1 \text{ A/cm}^2 - 1 \text{ kA/cm}^2$. The emission wavelength is not constant depending on the chip size and varies within a deviation range of 5 nm [20, 21]. Overall, one of the key points to improve the emission efficiency and wavelength stability in micro-LEDs is to suppress the nonradiative recombination centers in sidewalls and reduce the effect of QCSE from (0001) plane active structures.

GaInN/GaN-based multi-quantum shell (MQS) nanowire (NW) is of great interest as a promising material that can alleviate the issues mentioned above [22]. The MQS active structures are coaxially grown on the semipolar (1-101) plane and nonpolar (10-10) planes surface of the NWs, which can effectively reduce the influence of the piezoelectric field. As a result, there is a possibility for suppressing QCSE, improving luminous efficiency, solving the green gap problem, and realizing a high-efficiency micro-LED [23-27]. Compared with the planar (0001) plane LEDs, the active layer in NW LED is featured with a three-dimensional structure, increasing the luminous area and light extraction efficiency [28, 29]. In addition, there are many advantages, such as controlling the emission wavelength and obtaining low dislocation crystals by reducing the diameter of the NWs [30-32]. Moreover, the (1-101) planes are easier to incorporate in In than the (10-10) plane owing to their spatial growth characteristics, and by taking advantage of the (1-101) plane, MQS NW LEDs are also promising for realizing highly efficient LEDs in long wavelength range [33-35]. Specifically, the MQS NWs likewise show prospective application in micro-LEDs, because the surface recombination rate at the mesa edge is lower than that of the planar micro-LEDs owing to the minimal exposed region of the active region after mesa fabrication. Therefore, MQS NW is a promising candidate to suppress the enhancement of nonradiative recombination near the mesa edge region of planar micro-LEDs. Since the emission mechanism related to the dimension of devices is still unclear in MQS-NW LEDs, it is particularly essential to gain insight into the electrical and optical characteristics of the chip size and to identify the related issues. Therefore, as an initial step toward

realizing NW-based micro-LED, we investigated the dependence of the current injection paths and corresponding luminescence characteristics on the device size.

As a preliminary investigation toward the realization of GaInN/GaN MQS NW-based full-color micro-LED, we systematically compared the emission properties of NW LED with different emission areas. Eight types of NW LEDs with different mesa and p-electrode areas were fabricated on the NW samples grown under identical conditions. The current injection path and emission characteristics were evaluated and discussed from the viewpoints of current–voltage curves, current–light output characteristics, and electroluminescence (EL) spectra. The results of different p-electrodes indicated a trade-off between the current diffusion through all the NWs and effective injection to the sidewall of each NW in the LED devices.

2 Experimental section

The NW samples used in the LED process were prepared by metalorganic vapor phase epitaxy (MOVPE) using the selective area growth method [36, 37]. First, a 30 nm thick SiO₂ mask layer was deposited on the n-GaN/sapphire substrate using a radiofrequency magnetron sputtering system (CFS-4EP, Shibaura Mechatronics Co., Yokohama City, Kanagawa, Japan). After that, a hole pattern of a triangular lattice array with a diameter of 300 nm and a pitch of 1200 nm was formed by nanoimprint lithography, and the GaN inside the holes was exposed by inductively coupled plasma (ICP) etching (MV06-7001-0, ULVAC, Inc., Chigasaki City, Kanagawa Japan). Then, n-GaN core NW, five pairs of GaInN/GaN MQS with AlGaN spacers, and p-GaN shell were sequentially grown by MOVPE, as illustrated in Figure 1(a) and (b). Approximately 800 nm-tall Si-doped GaN core NWs were grown at a high temperature of 1140 °C for 70 s. The detailed growth conditions for the MQS structures can be found in our previous work [23, 24, 38-40]. Regarding the p-GaN shell, an Mg-doped ($\sim 3 \times 10^{19}$ cm⁻³) p-GaN was grown at 930 °C for 6 min, followed by a thin shell with higher Mg doping (~6 $\times 10^{19}$ cm⁻³) concentration. The Mg/Ga ratios in these two p-GaN shells were set to 5.45×10^{-3} and 1.09×10^{-2} , respectively, by controlling the supplied flow rates of trimethylgallium (TMG) and bis-cyclopentadienyl magnesium (Cp₂Mg) precursors. In conventional fabrication, activation for the p-GaN shells is generally performed via rapid thermal annealing (RTA) processing to remove the hydrogen contained in p-GaN and reduce the resistance. However, without nitrogen purification in the RTA system, the thermal activation may also introduce an oxide film on the surface of the p-GaN shell under ambient nitrogen and oxygen conditions, which can cause the degradation of the Ohmic contact formation. Therefore, in this work, the activation annealing of all the NW samples was performed inside the MOVPE reactor at 650 °C for 30 min in ambient N₂, i.e., an oxygen-free atmosphere.

Afterward, eight different types of LED devices with varied shapes and sizes of the mesa and electrode areas were fabricated on the samples. The detailed dimensions of mesa and p-electrodes in the devices are shown in Table 1. In samples b, c, d, and e, the indium tin oxide (ITO) region was designed to be same shape ($200 \times 100 \ \mu m^2$), while the p-electrode area was decreased. To investigate the effect of emission region, only the ITO area was changed in samples d, g, and h. The



Figure 1: Schematic of the structures and device fabrication for nanowire (NW)-based light-emitting diodes (LEDs): (a) cross-sectional view from the *c*-axis direction of the NW; (b) cross-section details from the *m*-axis direction of the NW; (c) NWs with activation inside the MOCVD chamber after growth; (d) removal of NWs in the n-electrode area and exposure of n-GaN layer by dry etching; (e) indium tin oxide (ITO) sputtering and thermal annealing; (f) deposition of Cr/Ni/Au films on the p- and n-electrodes.

Table 1: Chip size of ITO area and p-electrode area.

Sample name	Α	b	с	d	e	f	g	h
ITO area	340×340	200 × 100	200 × 100	200 × 100	200 × 100	120×120	100 × 100	50 × 50
S (μm²)	115,600	20,000	20,000	20,000	20,000	14,400	10,000	2500
p-electrode area (μm²)	24,000	190×40	180×30	40×40	30×30	130×30	40×40	40×40
Ratio of p-electrode to ITO area	0.208	0.38	0.27	0.08	0.045	0.27	0.16	0.64

distinctive shape of samples a and f was referred to the design that was reported in our previous work [41, 42]. Although the ITO area is not rectangular, the surface areas of samples a and f are equal to be 340×340 and $120 \times 120 \,\mu\text{m}^2$, respectively. Figure 1(c)–(f) show the fabrication process flow of NW-based LED devices, including removing NWs around the mesa area, ITO deposition, and electrode formation. To remove the NWs around the mesa and n-electrode parts, the Ni film with a thickness of 150 nm was vapor-deposited on the samples by electron beam (EB) deposition (MA08-3065, ULVAC, Inc., Chigasaki City, Kanagawa, Japan) except for the n-electrode part. After coating a resist on the entire samples, the ICP etching with a mixed gas of Cl₂ and Ar was performed to selectively etch off the NWs in the designated area. Subsequently, the transparent current spreading layer of ITO was deposited using a radio frequency magnetron sputtering system. The ITO layer with 60 nm thickness was deposited under the Ar and O₂ gas mixed atmosphere, with RF power of 20 and 100 W sequentially in two steps. To reduce the resistance in ITO, thermal annealing was performed at 600 °C for 4 min in N₂ atmosphere using rapid thermal processing (RTA) (MR094017-0, ULVACRIKO, INC., Tsuzuki City, Yokohama, Japan). Finally, Cr, Ni, and Au layers with thicknesses of 10, 20, and 200 nm, respectively, were deposited as the p- and n-electrode area by EB evaporation. The structural morphologies of the NW were observed with a scanning transmission electron microscope (SEM) (SU70, Hitachi High-Technologies Co., Minato City, Tokyo, Japan) under an acceleration voltage of 3.0 kV. The optical characteristics of NW LEDs were evaluated by EL measurements via a fiber spectrometer (USB4000, Ocean Optics Co., United States) and current-voltage-light output (I-V-L) characterizations (4156c, Agilent Technology, Santa Clara CA, USA). In both cases, the detection was carried out from the back side of the substrate, while the position of the detector was fixed for all the samples.

3 Results and discussion

3.1 Morphology inspection of the NW structures during the process

During the fabrication, the surface morphology of the NWs was inspected by SEM measurements, as shown in Figure 2(a)–(f). After MOVPE growth, the p-GaN shell was connected, forming a ridge between the NWs along the junction. The hexagonal shape of the (0001)-plane on the tip area was also clearly visible. The cross-sectional SEM image in Figure 2(b) shows that the p-GaN shell was uniformly grown on the whole NWs. Furthermore, the color contrast in Figure 2(b) confirms that the heights of the n-GaN core and the entire NW grown with the p-GaN shell are 800 and 990 nm, respectively. To promote the conformal deposition of the ITO layer, the height of the n-core NW was controlled, and the p-GaN growth was conducted based on the optimized conditions [23, 43-45]. Because the annealing temperature in the MOCVD chamber was lower than that of the p-GaN shell growth, it is unlikely that the morphology was deformed during annealing in the chamber under ambient N₂. The surface features of all the samples in this work were confirmed to be the same owing to the simultaneous growth



Figure 2: SEM characterizations of the NWs during fabrication process. (a) Tilted and (b) cross-sectional SEM images of the NWs after MOVPE growth. (c) SEM image of the region between n-electrode and NWs after etching. Tilted-view SEM image of the NWs (d) after ITO sputtering and (e) metallic film deposition on the p-electrodes. (f) SEM image of the n-electrode surface after deposition of Cr/Ni/Au films.

and annealing processes. Figure 2(c) shows the tilted-view SEM image of the region between n-electrode and NWs after etching. The observed holes for n-GaN core growth indicate that the n-GaN substrate under the NWs was fully exposed. Nevertheless, a few residual p-GaN spikes existed close to the un-etched NWs because of the edge effect by the overlap between the Ni mask in the mesa and resist in the n-electrode region. Afterward, ITO was uniformly coated on the NWs in the mesa region, as confirmed in Figure 2(d). Figure 2(e) and (f) show the surface morphologies of the p- and n-electrodes, respectively, after the Cr/Ni/Au film deposition. The dimensional details of the fabricated NW LED chips in samples a-h were confirmed by the SEM inspection, as shown in Figure 3(a)–(h), respectively. Sizes of the mesa and electrode regions were reliable as desired after the processes. The ratios of p-electrode to the ITO region for samples a-h were calculated to be 0.208, 0.38, 0.27, 0.08, 0.045, 0.27, 0.16, and 0.64, respectively. The samples a, d, f, g, and h with different mesa areas were compared to analyze the effect of the total emission region, despite the electrode size variation. For samples b, c, d, and e, the shape (200 × 100 μm²) of the mesa region was kept constant, while the sizes of p-electrodes varied as 190×40 , 180×30 , $40 \times$ 40, and 30 \times 30 μ m², respectively.

3.2 Evaluation of optical characteristics based on the difference in the mesa area

To evaluate the optical characteristics depending on the difference in the mesa area, reduced ITO regions with 340 \times 340, 200 \times 100, 120 \times 120, 100 \times 100, and 50 \times 50 μ m² were prepared in samples a, d, f, g, and h, respectively. Because the size and the shape of the NW LEDs are different in these samples, the corresponding p-electrodes were tuned for the benefit of the current injection. From the I-V curves in Figure 4(a), the threshold voltage and overall resistance increase as the ITO area decreases. Because the total number of NWs successively decreased from sample a to h, more current can be injected into the sidewall of the NWs in sample h under the same injection current. In this case, the high resistance of the thicker p-GaN shell on the sidewall path also became evident in the I-V curves. Since the p-GaN shell on the sidewall was connected and conformally covered by the ITO layer, the contact resistance between ITO and p-GaN was considered to be uniform along the NWs. To further clarify the I-V characteristics, the relationship between current and voltage was replotted as a function of the current density, as shown in Figure 4(b). Below the current density of 300 A/cm², more current was injected



Figure 3: SEM images of the fabricated NW LEDs. Details of the chip layout for (a) sample a; (b) sample b; (c) sample c; (d) sample d; (e) sample e; (f) sample f; (g) sample g; and (h) sample h (white line: length of p-electrode; yellow line: length of ITO region).



Figure 4: I-V-L curves and schematic images of the current injection paths in the NW samples. (a) I-V characteristics in samples a, d, f, g, and h with different ITO mesa areas. (b) The voltage was replotted as a function of current density. (c) The corresponding semi-log V-I characteristics measured from -6 to 9 V. (d) The I-L characteristics in samples a, d, f, g, and h. (e) The light output was replotted as a function of current density. (f) The schematic diagram of the emission surface changed depending on the current injection value and the fraction of InN.

into the sidewall of the NWs in the LEDs with a smaller mesa area, resulting in a slightly higher operation voltage. Under the same current density beyond 300 A/cm², the difference in operation voltage is relatively small. To inspect the leakage current, the voltage–current curves were measured under reverse and forward bias, as plotted with a semi-log scale in Figure 4(c). The reverse leakage current in the NW LED is relatively high (>10⁻³ A at a reverse voltage of -5 V) and increasing with an enlarged area of the mesa region. The large leakage path was mainly associated with the high-density-dislocations containing in MQS structure on the (0001) plane area of each NW. Because of the high

growth rate of MQS on the (0001) plane, the In-content was higher than those on the other planes, and the MQS thickness on the (0001) plane exceeded the critical thickness for 2D growth. Thus, the induced compressive strain easily triggered the formation of screw and Frank-type partial dislocations in the apex p-GaN shell [46, 47]. Therefore, more NWs in the LED will induce higher reverse leakage. Other possible current leakage points include formation of holes in the SiO₂ mask region and an abnormally grown p-GaN shape need to considered. Nevertheless, improved growth quality of the (0001) plane MQS region or the NWs apex truncated method is expected to alleviate the leakage, as reported in our previous work [24, 48]. Here, the remained (0001) plane MQS region was devoted to analysing the current injection paths along the NWs.

The light output characteristics of samples a, d, f, g, and h are plotted as a function of injection current in Figure 4(d). Under low current injection, the light output intensity is relatively low in all the samples. This is because the injection current was mainly localized at the apex (0001) plane region with relatively low crystallinity. When the current increases, the slope increases owing to the emission from the (1-101) plane and the top of the (10-10) plane of the NWs. A clear enhanced light output was confirmed as the mesa region reduced in the NW LED, especially at the currents above 20 mA. However, the slight saturation in sample h is attributable to the thermal effect with high current injection density. To precisely compare the effect of the ITO mesa area on the emission properties, the light output curves were plotted in terms of current density, as shown in Figure 4(e). For all the five NW LED samples with different mesa areas, the curves inevitably overlap from the current density of 0 to approximately 320 A/cm². It reveals that the emission property under low current injection density coincides with individual NWs among the LED samples regardless of the ITO mesa area. This phenomenon is assignable to the localization of current injection at the apex region under low current density. The light output density of sample h at 1000 A/cm² was approximately 1.68 times higher than that of sample g. The slope is expected to be the same in samples h and g if the current diffusion is the same. However, the ratios of p-electrode to the mesa area in samples g and h are 0.16 and 0.64, respectively. Therefore, it is considered that the current diffusion and the injection efficiency were improved in sample g, resulting in the enhanced light output. In addition, the non-radiative recombination of the edge region, which commonly increases with planar micro-LEDs, is expected to be minimal in NW LEDs, even with reduced mesa area. Figure 4(f) shows the schematic diagram of emission surface change depending on current injection density and spatial distribution of In content. When the current density is high, the current can be injected into the lower part of the NWs. Since the In content decreases from the upper to the lower part, the light emission has a shorter wavelength, and the threshold voltage becomes higher. High performance of the MQS NWbased micro-LED can be expected because of the mitigated efficiency degradation with reducing mesa area.

The EL spectra acquired at different injection currents are plotted in Figure 5(a) for samples a, d, f, g, and h, respectively. The emission peaks of samples a, d, f, g, and h are located at around 550, 500, 492, 491, and 485 nm, respectively. Two emission peaks are observed in samples d and f, especially under current injections above 50 mA. It can be seen that the emission intensity increases as the ITO mesa area of the NW LED decreases, and an enhancement of 12-folds is confirmed as compared between samples a and h. This is because the current density increases as the ITO area decreases, so that more current can reach the lower part of the NWs. As shown in Figure 5(b)-(f), the luminous photographs of the NW LEDs were taken from the front side through the microscope under injection currents of 10, 25, 50, 75, and 100 mA, respectively. For the NW LEDs with a large mesa area in sample a, the emission color was varied from red at 10 mA to bright yellow at 100 mA. The emission color in samples d, f, and g showed a similar tendency from yellow to green. Even at a low injection of 10 mA, the luminous color of sample h appeared to be more cyan. As the ratio of the current to the ITO area increases, i.e., the smaller the ITO area, the more current can reach the (1-101) and (10-10) planes, resulting in the greenish-blue color emission. Moreover, the distribution of light emission is relatively uniform over the entire ITO area for all the NW LEDs. Normally, it is a challenge to conformally deposit ITO on the sidewall of the NWs, while the uniformity of ITO can be improved by growing the p-GaN shape with a small (0001) plane and a large (1-101) plane [47]. In this experiment, the slightly connected p-GaN shell structures are beneficial to improve the uniformity of the ITO layer on the entire NW structures, and lateral current is spreading in the NW LEDs even with a small ratio of p-electrode to ITO area.

To gain insight into the relationship between emission peaks and the ITO mesa area of the LEDs, the EL peaks of each sample were separated by the Gaussian fitting. Figure 5(g) and (h) plot the peak wavelengths of samples a, d, f, g, and h as a function of injection current density from 0 to 405 A/cm² and 400–4000 A/cm², respectively. From the Gaussian fitting, two or three peak wavelengths were derived for samples a, d, f, g, and h under different injection currents. The variation tendency of the emission peaks is consistent among the samples. Basically, the NWs are featured with a blueshift from the (0001) and (1-101) planes at the apex region to the (10-10) plane, as confirmed by cathodoluminescence measurement in our previous report [48]. As described in Figure 4(f), the current reached the bottom of the NW as the current increased in MOS NW LEDs. In Figure 5(g), the long wavelength components above 525 nm were mainly emitted from the (0001) plane region with low crystallinity, which was the dominant emission in samples a, d, f, and g. Under low current injection density, the emission exhibited a clear blueshift from 675 to 525 nm. Furthermore, the short wavelength components near 490 nm



Figure 5: EL spectra and luminescence photos of the samples with different chip sizes. (a) EL spectra of the samples a, d, f, g, and h under different injection currents. Panels (b)–(f) show the luminous photographs of LED chips in samples a, d, f, g, and h, respectively. (g) And (h) show the EL peak wavelength at each current density, separated by the Gaussian fitting. The triangular marks indicate the emission from the (0001) plane at the apex region of the NWs. The circular marks were assignable to the emission from the (0001) plane and upper part of the (1-101) plane regions, while the squares were light emissions from the (1-101) plane. The diamond notation is considered light emission from the upper part of the (10-10) plane.

are relatively stable, dominated by the emission from the (1-101) planes. Regarding sample h, at the current densities above 1400 A/cm², a stable emission peak located at approximately 457 nm was confirmed, which was associated with the emission from the nonpolar (10-10) plane. Overall, the

peak wavelengths from the (1-101) semipolar and the (10-10) nonpolar planes appeared to be stable and irrelevant to the size of the ITO mesa area. The results suggest that it is the potential to realize high emission stability in NW-based micro-LEDs by suppressing the emission from the (0001) plane and promoting the current injection into the (1-101) and (10-10) planes.

Because the light output was detected from the backside and the emission light cannot be converted into an absolute value, an approximate EQE factor was derived by the formula: EQE factor [a.u.] = $\frac{P_0}{I} \times \frac{\lambda e}{hc}$ [49], where *h* is the Planck constant, *e* is the elementary electric charge, and *c* is the speed of light. The values of photons emitted per second (P_0) , injection current (I), and peak wavelength (λ) were determined at the current density of 0.48 kA/cm². Although the practical current density of micro-LEDs is guite low, the high value of current density in this work was considered based on the varied current injection through entire NWs. The derivation of EQE factors at low current density is not sufficiently corresponding to the emission from the (1-101) and (10-10) planes of the NWs, because weak emission from the (0001) plane region was dominant, as confirmed in Figure 5(g) and (h). Therefore, the EQE factors were derived at a high current density, where emission from the (1-101) and (10-10) plane regions became prominent. Focusing on the characteristics of samples d, f, g, and h with different mesa areas, the EQE factors at the same current density are plotted in Figure 6. The parameters for EQE factor calculation are summarized in Table 2, as shown on the right of Figure 6. Since the measurements were taken under identical conditions, the light outputs and calculated EQE factor are comparable. It can be confirmed that degradation of the EQE factor did not occur as the mesa area was reduced. This is attributable to the feeblish influence of non-radiative recombination from a few etched NWs. Besides, the larger the mesa area, the more likely it is that dysfunctional NWs are contained. By applying an approach to suppress emission from the (0001) plane region [46, 48], it is expected to achieve NW-based micro-LEDs without obvious EOE degradation even at low current density. Therefore, MQS NWs have a distinct possibility for realizing efficient micro-LED owing to the minimized exposed area of the active region

after mesa etching, which can reduce the surface recombination centers near the edge of the mesa.

3.3 Effect of different p-electrode areas on the optical characteristics

The luminous photos in the above section demonstrated the uniform emission in the entire mesa region in the LED, even with low injection currents. The effect of different p-electrode area on the emission properties was investigated to further confirm the current diffusion in the NW LEDs. Samples b, c, d, and e were fabricated with the same ITO area of $200 \times 100 \ \mu\text{m}^2$ and different p-electrode area of $190 \times 40, 180 \times 30, 40 \times 40$, and $30 \times 30 \,\mu\text{m}^2$, respectively. As shown in the I-V curves in Figure 7(a), a higher the operation voltage was observed in sample e with a smaller pelectrode area due to the limited current diffusion through the p-electrode. However, the light output is higher in samples d and e than that in samples b and c, as shown in Figure 7(b). The slope at high current injections above 40 mA was increased in samples d and e, which implies that the lightemitting plane in NWs changed. The light was mainly emitted from the apex region of under low current injection, and then the current was injected into the (1-101) plane and (10-10) plane as the current increased. Figure 7(c) shows that the EL intensity was relatively higher in the samples d and e with small electrode areas, and two prominent emission peaks were distinguished at high injection currents. Comparing the inserted emission photographs, samples b and c are dominated by green light emission over the entire surface, while brighter blue color was observed around the electrodes of samples d and e. As illustrated in Figure 7(d), if the p-electrode area is large, the current can be injected uniformly over the entire chip. However, the injected current was mainly localized in the top region of the NWs, resulting in a relatively longer emission wavelength and weaker emission intensity in samples b and c. In samples d and e,



Table 2. The parameters of the samples used for calculation

Sample	Po (a. u.)	I (mA)	λ (nm)	
d	0.2231	96	510	
f	0.1790	71	509	
g	0.1178	48	523	
h	0.0034	12	501	





Figure 7: I-V-L characteristics, EL spectra, and corresponding current injection paths of the samples with different types of p-electrodes. (a) I-V and (b) I-L characteristics of the samples b, c, d, and e with different p-electrode areas. (c) EL spectra of the samples b, c, d, and e under different current injections. The insets show the emission photograph at the injection current of 100 mA. Schematic diagrams of current spreading in (d) samples b, c and (e) samples d, e.

with a smaller p-electrode area located at one side of the mesa region, the current diffusion gradually decreases through the device, as shown in Figure 7(e) schematic diagram. Under the same current injection, it is considered that the small p-electrode in samples d and e increased the light emission from the (1-101) and (10-10) planes of the NWs underneath the p-electrode, which contributed to the increased emission intensity of the short wavelength component. Therefore, the thicker p-GaN shell at the bottom area of the NWs also resulted in a slightly higher operation voltage in samples d and e because the current paths were concentrated around the p-electrodes. From these results, it can be seen that there is a trade-off between the current diffusion through all the NWs and effective injection to the sidewall of each NW in the LED devices, and a further optimization is required to combine these two aspects in the micro-LEDs.

4 Conclusions

In this work, we investigated the effect of the mesa and p-electrode areas on the current injection path in coaxial GaInN/GaN MQS NW LED. As the ITO mesa area became smaller, the current density increased. The current reached the lower part of the MQS NWs, which increased the EL intensity with dominant emission in the short wavelength region. From the Gaussian fitting of the EL spectra, the peak wavelengths from the (1-101) semipolar and the (10-10) nonpolar planes appeared to be stable and irrelevant to the size of the ITO mesa area. Nevertheless, the reverse leakage current in the NW LED was linearly increased as a function of the mesa size because the leakage path is mainly associated with the defective MQS structure on the (0001) plane area of each NW. The EQE factors derived from the light outputs, suggested that the stable emission efficiency is attributable to the possible influence of non-radiative recombination from a few etched NWs. Under the same current injection, it is considered that the small p-electrode area increased the light emission from the (1-101) and (10-10) planes of the NWs underneath the p-electrode, which contributed to the increased emission intensity of the short wavelength component. From these results, it can be seen that there is a trade-off between the current diffusion through all the NWs and effective injection to the sidewall of each NW in the LED devices. Therefore, MQS NWs have a high possibility for realizing efficient micro-LED through suppressing light emission from the (0001)-plane region and optimizing the dimension of the p-electrode area for current injection.

Author contributions: W.L. devised the experiments for this work. S.K. fabricated the LED devices, measured chip performance, and wrote the first draft of the manuscript. W.L. and N. N. grew the nanowire samples using MOVPE. W.L. and S.K. prepared all the figures. W.L. analyzed the results, and re-wrote and revised the manuscript. W.L. prepared the response to reviewers' comments with input from S.K. K.I., S.I., and Y.J. contributed to the device fabrication and characterizations. N.S., A.S. and S.Y. supported the epitaxial growth and joined the discussion. K.H. joined in the discussion. S.K. joined in the discussion and revised the manuscript. S.K., T.T., and M.I. contributed to the data analysis and supervised the project. All authors have accepted responsibility for the entire content of this manuscript and approved its submission.

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References

- I. Akasaki and H. Amano, "Crystal growth and conductivity control of group III nitride semiconductors and their application to short wavelength light emitters," *Jpn. J. Appl. Phys.*, vol. 36, p. 5393, 1997.
- [2] I. Akasaki and H. Amano, "Breakthroughs in improving crystal quality of GaN and invention of the p – n junction blue-light-emitting diode," *Jpn. J. Appl. Phys.*, vol. 45, p. 9001, 2006.
- [3] R. Chaji, E. Fathi, and A. Zamani, 19-5: Invited Paper: Low-Cost Micro-LED Displays for All Applications, SID Symposium Digest of Technical Papers, Wiley Online Library, 2017, pp. 264–267.

- K. Ding, V. Avrutin, N. Izyumskaya, Ü. Özgür, and H. Morkoç, "Micro-LEDs, a manufacturability perspective," *Appl. Sci.*, vol. 9, p. 1206, 2019.
- [5] F. Xu, Y. Tan, Z. Xie, and B. Zhang, "Implantation energy-and size-dependent light output of enhanced-efficiency micro-LED arrays fabricated by ion implantation," *Opt. Express*, vol. 29, pp. 7757–7766, 2021.
- [6] J. Lin and H. Jiang, "Development of microLED," Appl. Phys. Lett., vol. 116, p. 100502, 2020.
- [7] T. Taki and M. Strassburg, "Visible LEDs: more than efficient light," ECS J. Solid State Sci. Technol., vol. 9, p. 015017, 2019.
- [8] H. Yu, M. H. Memon, D. Wang, et al., "AlGaN-based deep ultraviolet micro-LED emitting at 275 nm," *Opt. Lett.*, vol. 46, pp. 3271–3274, 2021.
- [9] M. Tian, H. Yu, M. H. Memon, et al., "Enhanced light extraction of the deep-ultraviolet micro-LED via rational design of chip sidewall," *Opt. Lett.*, vol. 46, pp. 4809–4812, 2021.
- [10] H. Yu, M. H. Memon, H. Jia, et al., "A 10× 10 deep ultraviolet light-emitting micro-LED array," J. Semicond., vol. 43, p. 062801, 2022.
- [11] F.-P. Massabuau, M. J. Davies, F. Oehler, et al., "The impact of trench defects in InGaN/GaN light emitting diodes and implications for the "green gap" problem," *Appl. Phys. Lett.*, vol. 105, p. 112110, 2014.
- [12] Y. Zhao, H. Fu, G. T. Wang, and S. Nakamura, "Toward ultimate efficiency: progress and prospects on planar and 3D nanostructured nonpolar and semipolar InGaN lightemitting diodes," *Adv. Opt. Photonics*, vol. 10, pp. 246–308, 2018.
- [13] C. Ge, J. Li, G. Wang, K. Su, and X. Lu, "Size effect on optical performance of blue light-emitting diodes," *J. Semicond.*, vol. 40, p. 102301, 2019.
- [14] L. Yang, J. Hu, L. Kim, and M. W. Shin, "Thermal analysis of GaN-based light emitting diodes with different chip sizes," *IEEE Trans. Device Mater. Reliab.*, vol. 8, pp. 571–575, 2008.
- [15] Y. Tao, S. Wang, Z. Chen, et al., "Size effect on efficiency droop of blue light emitting diode," *Phys. Status Solidi C*, vol. 9, pp. 616–619, 2012.
- [16] F. Olivier, S. Tirano, L. Dupré, B. Aventurier, C. Largeron, and F. Templier, "Influence of size-reduction on the performances of GaN-based micro-LEDs for display application," *J. Lumin.*, vol. 191, pp. 112–116, 2017.
- [17] M. S. Wong, D. Hwang, A. I. Alhassan, et al., "High efficiency of III-nitride micro-light-emitting diodes by sidewall passivation using atomic layer deposition," *Opt. Express*, vol. 26, pp. 21324–21331, 2018.
- [18] J. Park, J. H. Choi, K. Kong, et al., "Electrically driven mid-submicrometre pixelation of InGaN micro-light-emitting diode displays for augmented-reality glasses," *Nat. Photonics*, vol. 15, pp. 449–455, 2021.
- [19] M. Sheen, Y. Ko, D.-u. Kim, et al., "Highly efficient blue InGaN nanoscale light-emitting diodes," *Nature*, vol. 608, pp. 56–61, 2022.
- [20] A. Daami, F. Olivier, L. Dupré, F. Henry, and F. Templier, 59-4: Invited Paper: Electro-Optical Size-dependence Investigation in GaN Micro-LED Devices, SID Symposium Digest of Technical Papers, Wiley Online Library, 2018, pp. 790–793.
- [21] S. Tan, J. Zhang, T. Egawa, et al., "Influence of quantum-well width on the electroluminescence properties of AlGaN deep ultraviolet

light-emitting diodes at different temperatures," *Nanoscale Res. Lett.*, vol. 13, pp. 1–5, 2018.

- [22] F. Olivier, A. Daami, L. Dupré, F. Henry, B. Aventurier, and F. Templier, 25-4: Investigation and Improvement of 10μm Pixel-pitch GaN-based Micro-LED Arrays with Very High Brightness, SID Symposium Digest of Technical Papers, Wiley Online Library, 2017, pp. 353–356.
- [23] S. Kamiyama, W. Lu, T. Takeuchi, M. Iwaya, and I. Akasaki, "Growth and characterization of core-shell structures consisting of GaN nanowire core and GaInN/GaN multi-quantum shell," *ECS J. Solid State Sci. Technol.*, vol. 9, p. 015007, 2019.
- [24] W. Lu, Y. Miyamoto, R. Okuda, et al., "Correlation between optical and structural characteristics in coaxial GaInN/GaN multiple quantum shell nanowires with AlGaN spacers," ACS Appl. Mater. Interfaces, vol. 12, pp. 51082–51091, 2020.
- [25] S. Inaba, W. Lu, K. Ito, et al., "Superlattice-induced variations in morphological and emission properties of GaInN/GaN multiquantum nanowire-based micro-LEDs," ACS Appl. Mater. Interfaces, vol. 14, pp. 50343–50353, 2022.
- [26] M. Nami, A. Rashidi, M. Monavarian, et al., "Electrically injected GHz-class GaN/inGaN core – shell nanowire-based μLEDs: carrier dynamics and nanoscale homogeneity," ACS Photonics, vol. 6, pp. 1618–1625, 2019.
- [27] M. Nami, I. E. Stricklin, K. M. DaVico, et al., "Carrier dynamics and electro-optical characterization of high-performance GaN/inGaN core-shell nanowire light-emitting diodes," *Sci. Rep.*, vol. 8, p. 501, 2018.
- [28] L. Yu, B. Lu, P. Yu, et al., "Ultra-small size (1–20 μm) blue and green micro-LEDs fabricated by laser direct writing lithography," *Appl. Phys. Lett.*, vol. 121, p. 042106, 2022.
- [29] Y. J. Hong, C. H. Lee, A. Yoon, et al., "Visible-color-tunable light-emitting diodes," *Adv. Mater.*, vol. 23, pp. 3284–3288, 2011.
- [30] P.-M. Coulon, B. Alloing, V. Brändli, P. Vennéguès, M. Leroux, and J. Zúñiga-Pérez, "Dislocation filtering and polarity in the selective area growth of GaN nanowires by continuous-flow metal organic vapor phase epitaxy," *Appl. Phys. Express*, vol. 9, p. 015502, 2016.
- [31] Y. Kurisaki, S. Kamiyama, M. Iwaya, T. Takeuchi, and I. Akasaki, "Theoretical investigation of nitride nanowire-based quantum-shell lasers," *Phys. Status Solidi*, vol. 214, p. 1600867, 2017.
- [32] M. Terazawa, M. Ohya, K. Iida, et al., "Hybrid simulation of light extraction efficiency in multi-quantum-shell (MQS) NW (nanowire) LED with a current diffusion layer," *Jpn. J. Appl. Phys.*, vol. 58, p. SCCC17, 2019.
- [33] Y. Robin, S. Y. Bae, T. V. Shubina, et al., "Insight into the performance of multi-color InGaN/GaN nanorod light emitting diodes," *Sci. Rep.*, vol. 8, p. 7311, 2018.
- [34] Y. J. Hong, C.-H. Lee, J. Yoo, et al., "Emission color-tuned light-emitting diode microarrays of nonpolar InxGa1-xN/GaN multishell nanotube heterostructures," *Sci. Rep.*, vol. 5, 2015. https://doi.org/10.1038/srep18020.
- [35] K. Ito, W. Lu, S. Katsuro, et al., "Identification of multi-color emission from coaxial GaInN/GaN multiple-quantum-shell nanowire LEDs," *Nanoscale Adv.*, vol. 4, pp. 102–110, 2022.

- [36] G. Liu, J. Zhang, C.-K. Tan, and N. Tansu, "Efficiency-droop suppression by using large-bandgap AlGaInN thin barrier layers in InGaN quantum-well light-emitting diodes," *IEEE Photonics J.*, vol. 5, p. 2201011, 2013.
- [37] T. Takeuchi, H. Amano, and I. Akasaki, "Theoretical study of orientation dependence of piezoelectric effects in wurtzite strained GaInN/GaN heterostructures and quantum wells," *Jpn. J. Appl. Phys.*, vol. 39, p. 413, 2000.
- [38] S. D. Hersee, X. Sun, and X. Wang, "The controlled growth of GaN nanowires," *Nano Lett.*, vol. 6, pp. 1808–1811, 2006.
- [39] W. Lim, H. Kum, Y.-J. Choi, et al., "SiO2 nanohole arrays with high aspect ratio for InGaN/GaN nanorod-based phosphor-free white light-emitting-diodes," *J. Vac. Sci. Technol. B*, vol. 34, p. 042204, 2016.
- [40] K. Ito, W. Lu, N. Sone, et al., "Development of monolithically grown coaxial GaInN/GaN multiple quantum shell nanowires by MOCVD," *Nanomaterials*, vol. 10, p. 1354, 2020.
- [41] N. Sone, A. Suzuki, H. Murakami, et al., "Improved uniform current injection into core-shell-type GaInN nanowire LEDs by optimizing growth condition and indium-tin-oxide deposition," *Phys. Status Solidi*, p. 1900715, 2019. https://doi.org/10.1002/pssa .201900715.
- [42] H. Murakami, A. Suzuki, K. Nokimura, et al., "Fabrication and characterization of multiquantum shell light-emitting diodes with tunnel junction," *Phys. Status Solidi*, p. 1900774, 2020. https://doi .org/10.1002/pssa.201900774.
- [43] M. D. Brubaker, S. M. Duff, T. E. Harvey, et al., "Polarity-controlled GaN/AIN nucleation layers for selective-area growth of GaN nanowire arrays on Si (111) substrates by molecular beam epitaxy," *Cryst. Growth Des.*, vol. 16, pp. 596–604, 2016.
- [44] Y. T. Lin, T. W. Yeh, Y. Nakajima, and P. D. Dapkus, "Catalyst-free GaN nanorods synthesized by selective area growth," *Adv. Funct. Mater.*, vol. 24, pp. 3162–3171, 2014.
- [45] Y.-T. Lin, T.-W. Yeh, and P. D. Dapkus, "Mechanism of selective area growth of GaN nanorods by pulsed mode metalorganic chemical vapor deposition," *Nanotechnology*, vol. 23, p. 465601, 2012.
- [46] W. Lu, N. Nakayama, K. Ito, et al., "Morphology control and crystalline quality of p-type GaN shells grown on coaxial GaInN/GaN multiple quantum shell nanowires," ACS Appl. Mater. Interfaces, vol. 13, p. 54486–54496, 2021.
- [47] S. Katsuro, W. Lu, K. Ito, et al., "Emission characteristics of GaInN/GaN multiple quantum shell nanowire-based LEDs with different p-GaN growth conditions," *Nanophotonics*, vol. 10, pp. 3441–3450, 2021.
- [48] S. Katsuro, W. Lu, K. Ito, et al., "Suppression of (0001) plane emission in GaInN/GaN multi-quantum nanowires for efficient micro-LEDs," *Nanophotonics*, vol. 11, pp. 4793–4804, 2022.
- [49] K. Shugurov, A. Mozharov, G. Sapunov, V. Fedorov, A. Bolshakov, and I. Mukhin, "Influence of interface layer preparation on the electrical and spectral characteristics of GaN/Si solar cells," *J. Phys.: Conf. Ser.*, vol. 993, p. 012034, 2018.