

The future of electronics manufacturing is revealed in the fine print

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Microelectronic devices are among the most complex structures produced by modern manufacturing technology. A state-of-the-art microprocessor in a personal computer, for example, contains as many as 30 million transistors interconnected by more than one billion discrete electrical junctions (1). To achieve this level of integration, the sizes that define the device components must be small, smaller by several orders of magnitude, for example, than the size of a typical cell found in the human body. The demand for greater performance and capacity in these systems, as famously characterized by Moore's Law (2), has seen the number of individual transistors on a chip increase by a factor of 2 approximately every 18 months. These development cycles ultimately predict that, in 10 years, the advances made in design and processing methodologies will yield architectures for the most sophisticated integrated circuits that contain more than one billion transistors operating at speeds that are at least a factor of 10 faster than the most capable chips available today (International Technology Roadmap for Semiconductors Update, <http://public.itrs.net/reports.htm>). These devices will possess functionalities that certainly would have been unimaginable in 1947 from the vantage point provided by the characteristics of the original point contact transistor of Brattain, Bardeen, and Shockley.

The manufacturing methods used to fabricate these devices are highly sophisticated and rely heavily on a key processing technique called photolithographic patterning (3). This methodology has come to dominate the technologies of microfabrication in much the same way as silicon has the materials used to construct semiconductor devices. Given the dominant positions of photolithography and silicon in this area of technology, it would be extremely difficult to bring forward advances in research that might serve to challenge their standing as defining paradigms. It would be altogether remarkable that both of these central dogmas might be challenged in this way. This issue of PNAS presents a report by

Rogers *et al.* (4) that does precisely that. Those authors describe the application and validation of soft lithography (5) as a revolutionary new method for manufacturing complex, highly functional microelectronic structures, here a working prototype of a flexible display—an electronic sheet of paper—that incorporates pixel elements controlled by organic transistors (6).

Photolithography uses light to generate patterns in a photosensitive polymer, a so-called resist (7). These resist patterns are used in turn as templates for forming structures in a material, typically by such etching methods as reactive ion beam or wet chemical etching (8). Because the device structures in Si-based microelectronics are complex multilevel architectures, many applications of photolithographic patterning are needed to generate systems with any useful level of functionality. In this processing, the complex, hierarchical organizations found in an integrated circuit are generated on a layer-by-layer basis by using multiple deposition and etching steps mediated by photolithography-defined resist patterns (9). Because it is an optical technique, this type of patterning, although powerful, remains limited in some important ways. Ideally it is a planar patterning method and is poorly suited for making either three-dimensional structures or devices supported on a nonplanar substrate. It is a cumbersome process for making structures bearing specific chemical functionality. On large area substrates, many stepped exposures are needed to generate a complete pattern, which in turn generates a requirement for costly, high-precision processing tools. The short wavelengths needed to generate feature sizes in the 0.12- μm range also brings with it the need for process tools that are extremely expensive.

Soft lithography (5) refers to a complementary set of patterning tools for fabri-

cating small structures in thin-film materials that lift many of these constraints. Most of these methods share the property that they enable the construction of high-quality microstructures in a broad range of materials without the use of photochemical processes. Rogers and his coworkers (4) use a specific soft lithography patterning method called microcontact printing (μCP) (10) to construct the array of thin film transistors that serve as the switching elements of their display. In concept, the nature of the μCP process is a familiar one. Anyone who has used an inked stamp to print an address on an envelope or mark a date on their correspondence has performed a similar pattern transfer step. In μCP , however, the feature sizes on the elastomeric stamp used as the patterning tool are much smaller and the inks used vastly more specialized. The stamp used in μCP is prepared by casting and curing a soft elastomeric polymer (e.g., polydimethylsiloxane) against a master that carries in its relief structure the pattern one wants to transfer. This master can be made by a variety of techniques, including

direct-write photolithographic patterning. The stamp can be reused many times as a pattern transfer tool, though, and in this way generates great potential for improving both the throughput and cost performance of the patterning process. To carry out the printing process, the stamp is inked with a compound that, when transferred to the substrate surface, will form a layer that can serve as an effective resist for subsequent patterning. In this report, the ink used consists of a molecule that binds very strongly to the substrate surface in the form of a self-assembled monolayer (SAM) (11). The latent image defined by the patterned SAM is sufficient to

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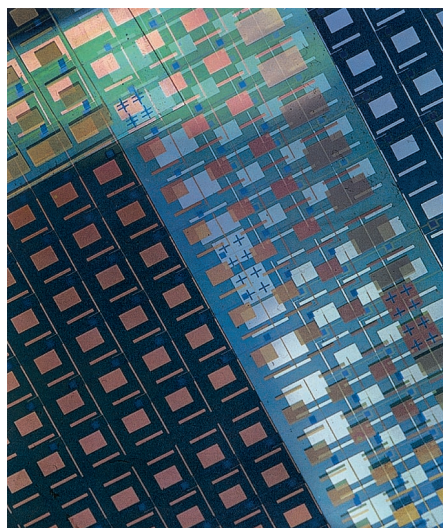


Fig. 1. Plastic display backplane circuits microfabricated using soft lithographic patterning methods (courtesy of F. Frankel, Massachusetts Institute of Technology, Cambridge).

direct the subsequent patterning of a thin film material by an etching step. Rogers *et al.* (4) use a SAM formed by an n-alkane thiol, $\text{CH}_3(\text{CH}_2)_n\text{SH}$, to direct the patterning of Au by wet etching (12); the metal microstructures obtained in this way form the electrodes and conduction paths needed to construct the organic thin film transistor arrays of the display. Fig. 1 shows an example of an integrated structure related to the subject of the PNAS report (4) that was fabricated in this way. The quality of the microstructures generated by the combined use of μCP and wet etching is exceptionally good. Of particular interest in the report of Rogers *et al.* (4) is the demonstration that soft lithographic patterning yields transistors with electrical properties that match those of similar devices fabricated with traditional processing methods.

A critical aspect of this work is the use made of a SAM as an etch resist. There are many molecular systems known that form SAMs on different substrate surfaces (13). These materials form via the spontaneous organization of molecules on a surface, a process that is thermodynamically driven and leads to thin organic films with extremely low densities of structural defects. Because they are only one molecule thick, SAMs are much thinner than the polymeric resists typically used in photolithographic patterning. The molecular ink used by Rogers *et al.* (4) produces a densely packed assembly of adsorbate molecules bonded to the Au surface in the form of a thiolate species (11). The surface chemistry involved in the formation of this SAM is shown in Fig. 2 along with an illustration of the surface properties that can be produced with them.

Alkanethiol SAMs on Au are among the most widely studied of these systems and provide one of the best-understood instances of spontaneous molecular organization known outside of the examples found in biology. These SAMs allow the physical properties of the surface to be varied by direct molecular design and are now widely used in studies of complex systems—microlithography, tribology (14), adhesion (15), biomaterials science (16), cell biology (17)—and as components of devices in molecular electronics (18), bioanalytical systems (19), and sensors (20). Fig. 2 shows a now classic application of a SAM, namely its use as a methodology for engineering the wetting properties of an interface at the molecular level (21): substitutions of the chain end functional groups (in this case by either carboxylic acid or methyl groups) allow the wetting properties of the gold surface to be patterned with hydrophilic and hydrophobic regions. As demonstrated by Whitesides and his coworkers (5), and reinforced by Rogers *et al.* (4), the dimensions of the patterning can be extended by soft lithography methods such as μCP to generate feature sizes that are both small and of extremely high quality. These latter capabilities are generating many new applications for microfabricated structures in broad areas of scientific research that traditionally have not enjoyed access to the high-cost capital equipment resources used for manufacturing microelectronic devices. Perhaps the most significant aspect of the report of Rogers *et al.* is that

this patterning can be carried out with multiple levels of registration to construct the complex integrated architecture of a working flexible display. This is a central requirement for any patterning method to be useful in a commercially relevant way.

The second remarkable aspect of the report by Rogers *et al.* (4) is their use of an organic semiconductor (pentacene) as the active material in the construction of the display's thin-film transistor array. It was not so long ago that an organic material would be found only as a passive component in a device—for example, as the insulation on a wire. In seminal work recently recognized by the Nobel Prize in chemistry, MacDiarmid, Heeger, and Shirakawa taught us that organic materials in fact could perform active functions, notably serving as metallic conductors (22). Much of the recent interest in the area of molecular electronics, though, has been driven largely by an interest in organic semiconductors, materials that have demonstrated commercially useful performances in light-emitting diodes (23). The success of this application has engendered great interest in other device applications for these materials. Rogers *et al.* (4) report on the fabrication of devices (transistors) that find an inspiration in the venerable history of Bell Laboratories. The organic transistor (6) array that forms the heart of the display backplane establishes a major benchmark for the field. The display itself is a prototype but the metrics it demonstrates are instructive. The display incorporates 256 pixels, each with its own

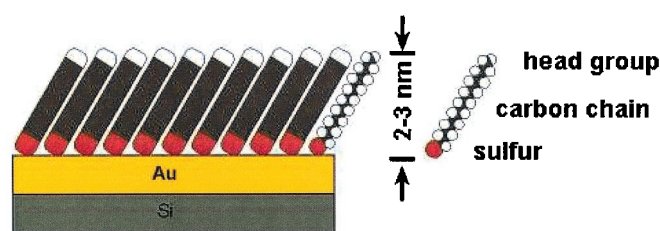


Image of Gold Surface Patterned with Hydrophobic and Hydrophilic SAMs

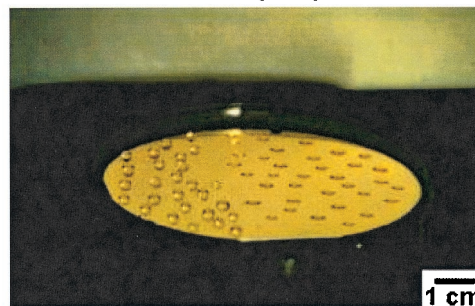


Fig. 2. A schematic depiction of the structure of a SAM formed by the adsorption of an n-alkane thiol on a gold surface (Upper). The physical properties of the surface can be controlled by substituting functional groups at the chain ends of the thiol adsorbate. In the example shown, the wafer presents carboxylic acid and methyl groups in the hydrophilic and hydrophobic regions, respectively (courtesy of G. Whitesides, Harvard University, Cambridge, MA).

switching transistor. The techniques developed in this work, though, provide a clear demonstration that the pixel sizes and numbers can be scaled easily to levels that would be suitable for a commercial product.

In closing, it seems appropriate to give consideration to what some might conclude are shortcomings of the designs described by Rogers *et al.* (4). The performances of the transistors in this demonstration certainly do not approach those of the most advanced silicon devices. The design rules and registration achieved by soft lithographic patterning in the con-

struction of this prototype display, although impressive, still fall short of the benchmarks provided by the photolithographic processes used in state-of-the-art microelectronics manufacturing. These latter products and processes are those associated with the highest profit and highest performance segments of what is currently an almost \$1 trillion industry. The prototype reported here, though, corresponds to an altogether new type of product—one that via its portability and low cost has the potential to fundamentally change how people interact with electronic systems. Further, it is fabricated by

a new set of process technologies that have many important contrasts with and potential benefits (especially in the area of cost) over the manufacturing methods used in the high end of the microelectronics market. When viewed in this way, soft lithographic patterning has all of the earmarks of being a disruptive technology (24). The history of such technologies is worth considering carefully. It was not all that long ago, after all, that Noyce (25) and Kilby (26) brought forward their own visions of a disruptive technology, the integrated circuit, and in so doing forever changed the nature of the electronics industry.

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