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## Continuous-flow Mass Production of Silicon Nanowires via Substrate-Enhanced Metal-Catalyzed Electroless Etching of Silicon with Dissolved Oxygen as an Oxidant

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Silicon nanowires (SiNWs) are attracting growing interest due to their unique properties and promising applications in photovoltaic devices, thermoelectric devices, lithium-ion batteries, and biotechnology. Low-cost mass production of SiNWs is essential for SiNWs-based nanotechnology commercialization. However, economic, controlled large-scale production of SiNWs remains challenging and rarely attainable. Here, we demonstrate a facile strategy capable of low-cost, continuous-flow mass production of SiNWs on an industrial scale. The strategy relies on substrate-enhanced metal-catalyzed electroless etching (MCEE) of silicon using dissolved oxygen in aqueous hydrofluoric acid (HF) solution as an oxidant. The distinct advantages of this novel MCEE approach, such as simplicity, scalability and flexibility, make it an attractive alternative to conventional MCEE methods.

arge-scale fabrication of SiNWs<sup>1-7</sup> is of great importance in pursuit of exploring SiNWs for wide-ranging technological applications<sup>8-16</sup> in electronics, photonics, renewable energy, and biotechnology. Despite intense efforts, mass production of SiNWs on an industrial scale remains unattainable. SiNWs are commonly produced by chemical vapor deposition methods<sup>7</sup> via vapor-liquid-solid (VLS) mechanism. However the VLS method can only produce SiNWs in limited quantities. Recently, metal-catalyzed electroless etching (MCEE) of silicon<sup>17-30</sup>, also known as metal-assisted chemical etching (MACE), has been widely used to fabricate high-aspect ratio SiNWs and other silicon nanostructures such as nanoholes due to its simplicity and low cost. The nature of MCEE of silicon in aqueous HF solution is galvanic corrosion. In traditional well-established MCEE methods, common oxidizing agent such as hydrogen peroxide or silver nitrate is indispensable<sup>17-30</sup>. To date, MCEE SiNWs have been widely investigated for photovoltaic device, thermoelectric device, lithium-ion battery, and biological applications<sup>9,31-37</sup>.

Here we report simple and low-cost MCEE of silicon in aerated aqueous HF solution enabling mass production of SiNWs with unprecedented ease. The process relies on substrate-enhanced MCEE of silicon using dissolved oxygen in aqueous HF solution as a green oxidant, which is ubiquitous in nature, yet completely neglected in traditional MCEE methods due to sluggish oxygen reduction kinetics. We demonstrate that this approach can establish continuous-flow production of SiNWs on an industrial scale, offering unparalleled advantages over traditional approaches in terms of cost, processing time, and versatility. The continuous-flow mass-production process for SiNWs is schematically shown in Fig. 1 (see Methods for details). The production line consists of conductive graphite (or noble metal such as Ag, Au, and Pt) substrates immersed in aerated aqueous HF solution, in which SiNW arrays are produced by continuously flowing Si substrates through the aerated aqueous HF solution.



Figure 1 | Schematic overview of continuous production process for SiNWs via substrate-enhanced MCEE of silicon in aerated aqueous HF solution. Silicon substrates coated with noble metal particles are placed in close contact with graphite substrates, and silicon nanowires are produced after passing silicon substrates once through the aerated aqueous HF solution.

#### Results

Graphite substrate-enhanced MCEE of silicon in aerated aqueous HF solution. To promote selective Si corrosion and create highaspect ratio SiNWs, the surfaces of the starting Si materials must be pre-patterned with noble metal catalysts such as silver or gold (silver nanoparticles, AgNPs are used here). Otherwise, only a thin porous Si layer would be formed from bare Si wafer or powders (Fig. S1). Our experimental results show that aligned SiNWs could be created on Si wafers of different doping types and concentrations. Figure 2a and 2b show respectively the typical cross-sectional scanning electron microscope (SEM) images of large-area vertically aligned SiNW arrays prepared on p-type 1–10  $\Omega \cdot$  cm Si(100) and ntype 2-3  $\Omega$  · cm Si(100) wafers after one pass (2 hours) through aerated aqueous HF solution. The SEM images clearly show that the corrosion rate of p-Si is faster than that of n-Si under the same conditions, revealing different corrosion behaviors of p- and n-type silicon due to their distinct charge carrier characteristics. The asprepared SiNWs are typically 20-250 nm in diameter, while wire lengths could be simply controlled by changing corrosion time.

High-resolution transmission electron microscopic (TEM) images in Figure 2c and 2d reveal the single crystalline nature of SiNWs in Figure 2a and 2b, respectively. The images show that the axial orientations of the nanowires are along the [100] directions, indicating the preferential anisotropic etching of <100> silicon. HR-TEM images also reveal that the surface of SiNWs from lowdoped p-type 1–10  $\Omega \cdot$  cm Si(100) wafer is relatively smooth, while that from highly-doped p-type 0.001–0.008  $\Omega \cdot$  cm Si(100) wafer is extremely mesoporous (Fig. S2). Note that the geometries of etched Si nanostructures are dependent on the pattern or shape of noble metal catalysts, therefore a variety of Si nanostructures with distinct features could be fabricated by precisely predefining the patterns of noble metal catalysts. Fig. S3 shows the typical SEM image of regular silicon nanoholes with controlled geometry and density.

We evaluate the substrate-enhanced MCEE of silicon by galvanic current measurements, which were performed in a home-made electrochemical cell. The silicon area exposed to aqueous HF solution is 1.0 cm<sup>2</sup> and the Si/Graphite surface area ratio is set to 1:200. Figure 3 shows the galvanic current densities as a function of time for silicon and graphite coupled together through an external wire. The galvanic current rises sharply as the circuit is short-circuited and then levels off with time. The initial abrupt drop in corrosion current is due to the limited solubility of oxygen in aqueous HF solution. In the presence of AgNPs, the measured current density of the p-Si/Graphite galvanic cell is considerably larger than that of n-Si/Graphite galvanic cell in the presence of AgNPs (Fig. 3a), and nearly two orders of magnitude greater than that of p-Si/Graphite galvanic cell in the absence of AgNPs (Fig. 3b). These results are well consistent with our SEM observations. Due to direct contact of noble metal catalysts with silicon in aqueous HF solution, the microscopic Si/AgNPs galvanic cells do not contribute to the measured corrosion current, which originates from the macroscopic Si/Graphite galvanic cell. Assuming a constant corrosion rate, the corrosion current density



Figure 2 | SEM and TEM images of SiNWs prepared by graphite substrate-enhanced MCEE of silicon in aerated aqueous HF solution. (a), Crosssectional SEM image of SiNW array prepared on 1–10  $\Omega \cdot \text{cm}$  p-Si(100) wafer. (b), Cross-sectional SEM image of SiNW array prepared on 2–3  $\Omega \cdot \text{cm}$  n-Si(100) wafer. (c), High-resolution TEM image of a Si nanowire from 1–10  $\Omega \cdot \text{cm}$  p-Si(100) wafer. (d), High-resolution TEM image of a Si nanowire from 2–3  $\Omega \cdot \text{cm}$  n-Si(100) wafer. (d), High-resolution TEM image of a Si nanowire from 2–3  $\Omega \cdot \text{cm}$  p-Si(100) wafer. (d), High-resolution TEM image of a Si nanowire from 2–3  $\Omega \cdot \text{cm}$  n-Si(100) wafer.



Figure 3 | Time dependence of galvanic current for Si and graphite coupled together in aerated aqueous HF solution. (a), AgNPs/p-Si/ Graphite and AgNPs/n-Si/Graphite galvanic cells in aerated aqueous HF solution. (b), p-Si/Graphite and n-Si/Graphite galvanic cells in aerated HF electrolyte; AgNPs/p-Si/Graphite galvanic cells in de-aerated aqueous HF solution.

from microscopic Si/AgNPs galvanic cells reaches 1.3 mA/cm<sup>2</sup> using Faraday's law based on the mass loss of silicon and galvanic currenttime curve measured for macroscopic p-Si/Graphite galvanic cell in the presence of AgNPs. The role of dissolved oxygen in aqueous HF solution was investigated by conducting corrosion experiment in deaerated aqueous HF solution bubbling with high-purity nitrogen gas. In the presence of AgNPs, the steady-state current density of the p-Si/ Graphite galvanic cell in de-aerated aqueous HF solution was 0.007 mA/cm<sup>2</sup> (Fig. 3b), which is three orders of magnitude lower than that of a similar cell in aerated aqueous HF solution, showing silicon corrosion is completely governed by oxygen reduction reaction.

Noble metal substrate-enhanced MCEE of silicon in aerated aqueous HF solution. We further investigated noble metals (Ag, Au, and Pt) substrate-enhanced MCEE of silicon in aerated aqueous HF solution for SiNWs fabrication under the same conditions. Figure 4a-c show the typical cross-sectional SEM images of SiNW arrays prepared on p-type 1–10  $\Omega$  · cm Si(100) wafers placed on Ag, Au and Pt substrates, respectively. The etching time is 2 hours. These SEM images clearly reveal that the length of produced SiNWs or the etching rate strongly depends on the type of used noble metal substrates and increases in the order of Ag, Au, and Pt, indicating these metal substrates show quite different catalytic activity in the cathodic oxygen reduction reaction. To investigate catalytic activities of noble metal substrates in oxygen reduction, we carry out galvanic corrosion experiments by measuring the galvanic current of noble metal substrates coupled with silicon substrates in aerated aqueous HF solution. Figure 4d shows the measured galvanic current densities as a function of time for silicon substrates coupled with Ag, Au, and Pt substrates. It clearly reveals that the galvanic current increases in the same order of Ag, Au, and Pt, which is well consistent with SEM observations. These results imply that catalytic activities of the cathodes considerably affect oxygen reduction and accompanied silicon etching rates in aerated aqueous HF solution.



Figure 4 | Effects of the noble metal substrate types on the corrosion rates of silicon wafers in aerated aqueous HF solution. (a), SEM image of SiNW array prepared by Ag substrate-enhanced MCEE of silicon. (b), SEM image of SiNW array prepared by Au substrate-enhanced MCEE of silicon. (c), SEM image of SiNW array prepared by Pt substrate-enhanced MCEE of silicon. (d), Time dependence of galvanic current for Si coupled with different noble metal substrates in aerated aqueous HF solution. All wafers used are  $1-10 \Omega \cdot \text{cm p-Si}(100)$  wafers.



Figure 5 | Schematic illustration of substrate-enhanced MCEE of silicon using dissolved oxygen in aqueous HF solution as an oxidant. (a), Rapid galvanic corrosion of silicon takes place when oxygen is reduced on both noble metal catalysts and graphite, generating electrical current flowing through the Si/metal interface within propagating pits. (b), Highlylocalized selective galvanic corrosion of silicon leads to formation of threedimensional silicon nanostructures with considerable depth.

#### Discussion

Graphite

The above results demonstrate that the present substrate-enhanced MCEE approach is a very simple method for preparing SiNW arrays without needing the common toxic oxidizing agents such as silver nitrate and hydrogen peroxide. In the absence of graphite or noble metal substrates, only shallow pits, as shown in Fig. S4, are produced on Si surface. Additionally, since only thin porous silicon is produced in the absence of noble metal catalysts, we conclude that the corrosion rate of silicon in the presence of both graphite and noble metal catalysts is much faster than that when either one of them is used alone, revealing the strong synergistic enhancing effect of graphite and noble metal for galvanic corrosion of silicon. An electrochemical model of the synergistic galvanic corrosion process is proposed as schematically illustrated in Fig. 5a. Due to the electrode potential difference, a macroscopic galvanic cell between Si and graphite, accompanying by microscopic galvanic cells between Si and noble metal catalysts, is constructed when Si wafer coated with noble metal catalysts are electrically coupled to graphite substrate in aerated aqueous HF solution. While random and stochastic microscopic galvanic cells may exist on Si surface<sup>38</sup>, they contribute negligibly to silicon corrosion in aerated aqueous HF solution. The anodic and cathodic half-cell reactions for the substrate-enhanced MCEE of silicon are described as follows:

Anodic (Silicon) :  $Si + 6HF \rightarrow H_2SiF_6 + 4H^+ + 4e^-$ 

Cathodic (Graphite and Metal) :  $O_2 + 4H^+ + 4e^- \rightarrow 2H_2O$ 

Briefly, the dissolved oxygen is reduced on both surfaces of graphite and noble metal catalysts, while silicon is oxidized by releasing electrons. Due to enhanced cathodic oxygen reduction, the anodic silicon dissolution reaction is simultaneously accelerated. As current lines always follow the minimal ohmic resistance, thus the electrical current flowing through the silicon/metal interface within the propagating pits would be very intense, resulting in highly selective pitting corrosion of silicon underneath the noble metal catalysts, and consequential formation of silicon nanostructures with considerable depth (Fig. 5b). We suggest that the macroscopic galvanic cell between Si and graphite in combination with the microscopic galvanic cells between Si and noble metal catalysts contribute to the intense corrosion current, which is also significantly affected by the charge carrier characteristics of Si substrates. Consequently, the corrosion rate of p-type Si is faster due to the abundant holes in p-Si, while that of n-type Si is slower due to the limited supply of holes.

In conclusion, we demonstrate for the first time the simple, continuous-flow mass production of SiNWs via substrate-enhanced MCEE of silicon using dissolved oxygen in aqueous HF solution as a green oxidant. The distinct advantages of this novel approach such as simplicity, scalability and flexibility would make it an attractive alternative to conventional MCEE methods. Since galvanic corrosion occurs in all semiconductor-metal and metal-metal systems, we suggest that such galvanic corrosion processes may be generally adopted to fabricate other semiconducting and metallic nanostructures.

#### Methods

Fabrication of silicon nanowires. SiNWs can be prepared on different starting Si materials, including single-crystalline silicon wafer, multi-crystalline silicon wafer, thin silicon film, and silicon particles, by means of substrate-enhanced MCEE of silicon in a continuous production line consisting of graphite or noble metal (Ag, Au, and Pt) substrates immersed in aerated aqueous HF solution. To prepare high-aspect ratio SiNWs, Si materials must be pre-coated with thin noble metal (Ag, Au) particles before being placed onto graphite or noble metal substrates immersed in aerated aqueous HF solution. Only porous silicon was prepared from clean Si materials placed on a graphite substrate immersed in aerated aqueous HF electrolyte (10% in weight) for 10–300 minutes. All experiments were carried out at room temperature. All chemicals were purchased from Sigma-Aldrich.

Structural and electrochemical characterization. The morphologies and microstructures of silicon nanostructures were investigated using a field-emission scanning electron microscope (SEM, HITACHI S-4800) and a transmission electron microscope (TEM, JEOL JEM-2100). The galvanic corrosion current was measured in a Teflon-made electrochemical cell. The de-aerated aqueous HF electrolyte was prepared by purging high-purity N<sub>2</sub> through the solution for 1 hour. The electrochemical measurements were performed using a Zennium electrochemical workstation (IM6, Zahner, Germany) with silicon as the working electrode and a graphite rod as the counter electrode. Ohmic contact was made by pasting high-purity In-Ga alloy onto the rear side of the Si wafer with 1 cm<sup>2</sup> of the front surface area exposed to aqueous HF solution.

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#### Author contributions

Y.H. and K.Q.P. designed the experiments. Y.H., L.L., Z.Q. and X.L.W. performed the fabrication experiments and electrochemical measurements. X.H. and X.M.M. performed TEM characterization. K.Q.P. supervised the project, performed data analysis and wrote the paper. S.T.L. discussed the results, performed data analysis and contributed to manuscript writing. All the authors reviewed and commented on the manuscript.

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