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Stable Cycling of SiO₂ Nanotubes as High-Performance Anodes for Lithium-Ion Batteries

SUBJECT AREAS:
MATERIALS FOR ENERGY
AND CATALYSIS

BATTERIES

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Herein, SiO₂ nanotubes have been fabricated via a facile two step hard-template growth method and evaluated as an anode for Li-ion batteries. SiO₂ nanotubes exhibit a highly stable reversible capacity of 1266 mAhg⁻¹ after 100 cycles with negligible capacity fading. SiO₂ NT anodes experience a capacity increase throughout the first 80 cycles through Si phase growth via SiO₂ reduction. The hollow morphology of the SiO₂ nanotubes accommodates the large volume expansion experienced by Si-based anodes during lithiation and promotes preservation of the solid electrolyte interphase layer. The thin walls of the SiO₂ nanotubes allow for effective reduction in Li-ion diffusion path distance and, thus, afford a favorable rate cyclability. The high aspect ratio character of these nanotubes allow for a relatively scalable fabrication method of nanoscale SiO₂-based anodes.

Silicon, as an anode material, has been extensively researched in recent years due to its high theoretical capacity of 3579 mAhg⁻¹ and relative abundance^{1,2}. However, Si undergoes volume expansion upwards of 300% upon lithiation generating large mechanical stresses and subsequent pulverization and solid electrolyte interphase (SEI) degradation^{3,4}. Effective structuring of Si below a critical dimension of 150 nm via nanospheres, nanoparticles, nanotubes, and nanowires has alleviated pulverization and subsequent active material loss associated with the lithiation induced volume expansion⁵⁻⁹. Even fewer structures have addressed the crucial stability of the SEI layer such as double walled silicon nanotubes, highly porous silicon nanowires, and yolk-shell silicon nanoparticles¹⁰⁻¹². However, many of these exotic structures lack scalability such as those fabricated via chemical vapor deposition (CVD) using silane: an expensive, toxic, and pyrophoric precursor^{12,13}. Attention has recently turned to SiO₂ as a viable anode material for Li-ion batteries due to its high abundance in the earth's crust, low discharge potential, and high initial irreversible capacity and reversible capacity of 3744 mAhg⁻¹ and 1961 mAhg⁻¹, respectively¹⁴⁻¹⁶. Myriad SiO₂-based architectures have been fabricated and characterized as anodes including nanocubes, tree-like thin films, and carbon coated nanoparticles¹⁷⁻¹⁹. Non-stoichiometric silicon oxides (SiO_x, where 0 < x < 2) have also attracted attention due the higher molar ratio of silicon to oxygen^{20,21}. A lower oxygen content allows for higher specific capacity at the expense of cyclability²².

Numerous other material systems have been investigated as anodes for Li-ion batteries. Carbon-based anodes such as graphite, 3D graphene sheets, carbon nanotubes (CNTs), and CNT pillared graphene have all been demonstrated as feasible material systems for lithium-ion batteries²³⁻²⁶. Many of these carbon nanostructures can also be used as high-performance supercapacitor materials, such as chrysanthemum like carbon nanofibers, 3D graphene and CNT foam, and functionalized graphene sheets²⁷⁻³⁰. While the rate capability of carbon-based anodes is superior to that of silicon-based anodes, the specific capacity is inferior to that of silicon-based anodes. Graphite, typically used in commercial Li-ion battery anodes, has a theoretical capacity of 372 mAhg⁻¹, which is not sufficient for next generation Li-ion batteries³¹. Tin, with a theoretical capacity of 972 mAhg⁻¹, has also garnered interest as a possible anode material with structures such as encapsulated Sn nanoparticles and Sn nanowires^{32,33}. However, Sn experiences similar lithiation-induced expansion issues like Si and has a lower specific capacity.

Polydimethylsiloxane (PDMS) is an optically transparent, non-toxic, and environmentally benign organosilicon widely used in pharmaceutical and consumer applications^{34,35}. PDMS produces SiO₂ vapor species when



heated in ambient atmosphere, which makes it an ideal precursor for templated deposition of SiO₂ at the nanoscale. Beginning at 290 °C, PDMS will thermally degrade into volatile cyclic oligomers via chain-folded scission of Si-O bonds by oxygen-catalyzed depolymerization^{36,37}. The ability for PDMS to produce SiO₂ vapor allows for deposition of SiO₂ on a variety of templates. Specifically, hollow nanostructures are of interest for Li-ion batteries due to reduced Li-ion diffusion path distance via increased surface area and small wall thicknesses^{38,39}. Alleviation of lithiation-induced mechanical stresses can also be accomplished through engineering interior voids in the active material^{40,41}. Herein, we present a modified procedure for fabricating high aspect ratio SiO₂ NTs, previously presented by Hu et al., and evaluate their performance as Li-ion battery anodes⁴².

Results

The fabrication process for silicon sub-oxide nanotubes (SiO₂ NTs) is illustrated schematically in Fig. 1. An amorphous layer of SiO₂ is deposited onto commercial AAO templates via vapor phase deposition through thermal degradation of PDMS in air under vacuum. Our improved synthesis procedure eliminates the need to sand excess SiO₂ deposits off of the fragile AAO templates after deposition. By placing the AAO templates downstream from, rather than covering, the PDMS blocks and performing the deposition process under vacuum, we are better able to control SiO₂ deposition throughout the AAO template, preventing unwanted thick deposits of SiO₂. SiO₂ conformally coats all exposed surfaces of the AAO including the top and bottom of the template, creating a connected tubular network of SiO₂. The AAO is subsequently removed via a heated phosphoric acid bath to leave SiO₂ NTs. After rinsing several times in DI water to remove phosphoric acid, the tubes are sonicated to separate the bundles of SiO₂ NTs into individual tubes. The connected SiO₂ NT network obtained after AAO removal is not mechanically sound, therefore the tubes must be sonicated apart so that they may be handled facily in powder form. A 20 nm coating of SiO₂ on a 13 mm diameter AAO with a thickness of 50 μm produces a volumetric density of SiO₂ of 0.515 gcm⁻³ and an areal density of 2.57 mgcm⁻².

SEM images in Fig. 2 reveal the tubular morphology of the SiO₂ NTs as well as their high aspect ratio. Bundles of SiO₂ NTs occur due to deposition of SiO₂ on the tops and bottoms of the AAO templates,

but brief sonication serves to easily liberate the tubes. The SEM image in Fig. 2(a) reveals the excellent uniformity of the SiO₂ coating across all dimensions of the AAO templates and the interconnected nature of the SiO₂ NTs after removal of the AAO template. These small bundles occur after a brief period of sonication and further sonication serves to fully separate all of the tubes, as seen in Fig. 2(b). The tubes have a very high aspect ratio of 250 : 1 at a length of 50 μm and an average diameter of 200 nm. Lengthy periods of sonication fully separate and shorten the SiO₂ NTs, revealing their tubular morphology as seen in Fig. 2(c). The SEM image in Fig. 2(d) reveals the branched morphology of the SiO₂ NTs, which serves to further increase the surface area of the tubes and is purely a result of anodization in the presence of aluminum imperfections⁴³.

TEM images reveal the wall thickness is 20 nm and highly uniform throughout the length of the tubes as in Fig. 2(e). The branched nature of the NTs is confirmed via TEM and no evidence suggests porosity exists in the walls. TEM confirms the SiO₂ NTs have an average diameter of 200 nm, which is expected given the commercial AAO template specifications. Based on the highly random fracture patterns generated via sonication, we conclude that the tubes are composed of amorphous SiO₂. XRD analysis confirms that the SiO₂ NTs are amorphous as seen in Fig. 2(f). Regardless of initial crystallinity, SiO₂ NTs will undoubtedly be amorphous in subsequent cycles^{44,45}.

Scanning transmission electron microscopy (STEM) and energy dispersive spectroscopy (EDS) are further performed to confirm the composition of the as-prepared nanotube samples. The STEM-EDS sample was simply prepared via transferring vacuum-dried SiO₂ NTs onto a copper TEM grid. As shown in Figure 3a, SiO₂ NTs are randomly oriented, and a selected area EDS mapping was performed on the region within the yellow box. EDS microanalysis on the selected region shows the SiO₂ NTs consists of primarily Si and O (Figure 3b). EDS element mapping micrographs of Si and O suggest a very uniform distribution of these two elements. Traceable amount of C, Al, P (wt% < 1%) were observed due to carbon contaminants, unetched AAO, and unremoved H₃PO₄ etchant, respectively. An EDS quantitative analysis on the selected region was performed to characterize the weight and atomic percentages of elements and to confirm the existence of SiO₂, as in Fig. 3 e–f. We believe the contaminants and the copper grid both contribute to the oxygen peak due to the existence of respective oxides from each.

The Langmuir and BET surface areas were measured to be 45.17 and 26.64 m²g⁻¹, respectively, for the as-prepared SiO₂ NTs. The obtained surface area and pore distribution suggest the as-prepared SiO₂ NTs have limited surface area and porosity as in Fig. 4. This surface area is more than double the surface area of as-received AAO templates which is expected given the tubular morphology⁴⁶. This limited surface area is beneficial in reducing the amount of SEI layer formation in the first few cycles by limiting active material contact with the electrolyte⁴⁷. However, the rate capability suffers as a result of the limited surface area due to a higher reliance on bulk diffusion of Li into SiO₂.

The electrochemical performance of SiO₂ NTs was characterized by fabricating 2032 coin cells with SiO₂ anodes and Li metal counter electrodes. Cyclic voltammetry (CV) was performed in the 0–3.0 V range with a scan rate of 0.1 mVs⁻¹, shown in Fig. 4(a). The CV plot is shown to 1.75 V to emphasize the noteworthy reactions taking place at lower voltages. Decomposition of the electrolyte and formation of the SEI layer occurs at the broad peak of 0.43 V as in Fig. 5(a). A much broader, less discernable peak occurs at 1.40 V which can be attributed to a reaction between electrolyte and electrode and the beginning of SEI formation⁴⁸. Both of these peaks become undiscernible in the 2nd cycle suggesting SEI formation takes place mostly during the first cycle and that these initial reactions are irreversible. During the initial charge cycle a noticeable peak occurs at 0.33 V, which can be attributed to dealloying. In subsequent cycles this peak

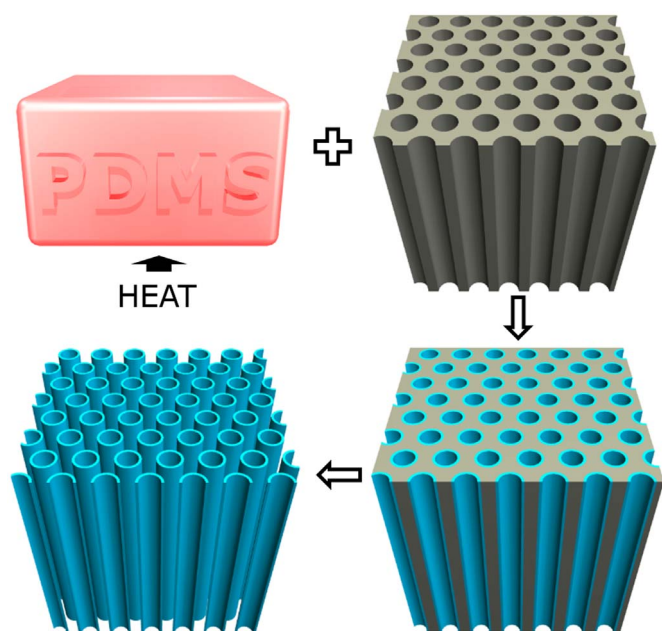


Figure 1 | Schematic representation of the fabrication process for SiO₂ NTs.

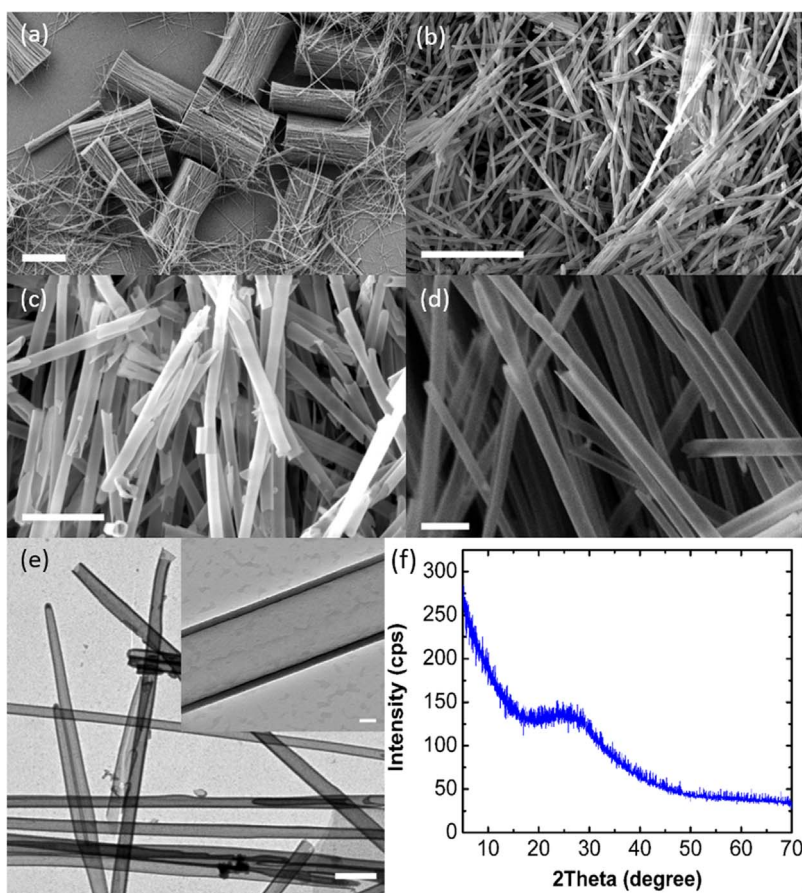


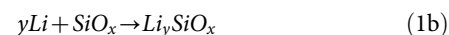
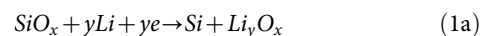
Figure 2 | SEM image of (a) partially separated SiO₂ NTs showing NT bundles and dispersed NTs, (b) fully separated dispersed SiO₂ NTs, (c) separated SiO₂ NTs showing tubular morphology, and (d) image showing branched morphology. Scale bars are 25 μm, 10 μm, 2 μm, and 1 μm for (a), (b), (c), and (d), respectively. (e) TEM image of dispersed SiO₂ NTs and inset showing tube diameter and wall thickness. Scale bar is 400 nm and 50 nm for the inset. (f) XRD analysis of SiO₂ NTs.

becomes very pronounced and shifts downward to 0.25 V. The sharpening and growth of this dealloying peak implies a rate enhancement in the kinetic process of delithiation of SiO₂ NTs. The kinetic enhancement may be due to the formation of an embedded nano-Si phase as it has been reported that one of the oxidation peaks of Si is 0.25 V during Li extraction from Li_xSi⁴⁹. By the 10th cycle there is an emergence of an anodic peak located at 0.22 V while the peak at 0.01 V has decreased. It is known in the literature that the 0.01 V and 0.22 V peaks are associated with the lithiation of Si^{48,50}. The CV curves are in good agreement with the charge-discharge profiles in Fig. 5(c) and Fig. 5(d).

Galvanostatic cycling of SiO₂ NTs using a C rate of 100 mA g⁻¹ was performed for 100 cycles at selected current densities. The initial sharp decrease in charge capacity over the first few cycles, seen in Fig 5(b), can be attributed to the formation of the SEI layer. The very thin walls of the SiO₂ NTs allows for lithiation of a larger percentage of active material and thus the marked high capacity relative to other published SiO₂ anodes utilizing thicker structures^{20,51}. The initial charge capacity is 2404 mA h g⁻¹ using a rate of C/2, and the initial discharge capacity is 1040 mA h g⁻¹ yielding a 1st cycle efficiency of 43.3%; this is attributed to the SEI formation. After 10 cycles the charge capacity levels off to 1101 mA h g⁻¹ and the discharge capacity increases to 1055 mA h g⁻¹; this yields an efficiency of 95.8%. Expectedly, cycling at higher rates produces lower charge capacities as follows: 1008 mA h g⁻¹ at 1C, 914 mA h g⁻¹ at 2C, and 814 mA h g⁻¹ at 4C. After 100 cycles the charge and discharge capacity increase to 1266 mA h g⁻¹ and 1247 mA h g⁻¹, respectively; the efficiency is 98.5%.

Discussion

Yan et al. confirmed a coexistence of the irreversible lithiation mechanisms in reaction 1a and 1b^{19,21}. The electrochemically inactive and thermodynamically stable compounds in reaction 1 and 2 are also responsible for the low efficiency in the first charge-discharge cycle, but they may form a stable scaffolding that alleviates volume expansion related issues associated with lithiation of Si⁵². This inactive lithium silicate scaffolding coupled with the tubular morphology of the SiO₂ NTs may be responsible for the highly stable cycling and low capacity fading. As the silicon phase grows larger, volume expansion effects become more significant and lead to capacity fading as seen in other Si and SiO₂ anodes^{18,53}. The losses associated with expansion related effects such as SEI degradation and active material pulverization begin to outweigh gains in capacity from Si phase growth. Reaction 1c is responsible for the reversible capacity as seen in traditional Si-based anodes resulting in the formation of Li₁₅Si₄ at room temperature⁵.



After the initial decrease in capacity due to SEI formation, the capacity steadily increases until stabilizing at around 80 cycles. We believe this capacity increase is due to the increasing amount of silicon as the SiO₂ is partially reduced by Li and not fully reduced back to SiO₂. Ban

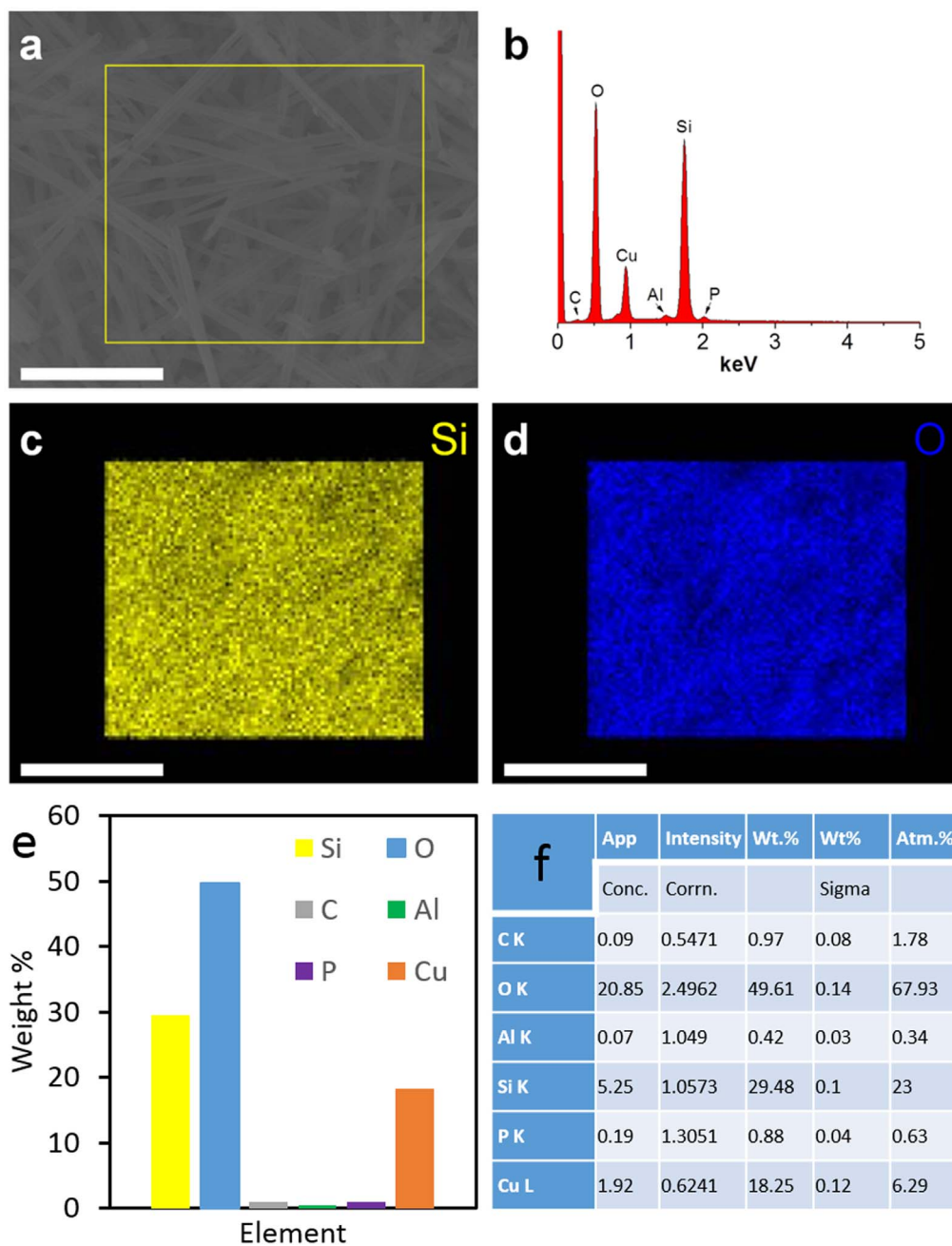


Figure 3 | (a) SEM image of SiO₂ NTs. (b) EDS spectra of SiO₂ NTs on selected region (yellow rectangle) of image a. (c–d) show the EDS microanalysis of element Si and O for this selected region. Scale bar: 5 μ m. (e–f) EDS quantitative analysis of selected region.

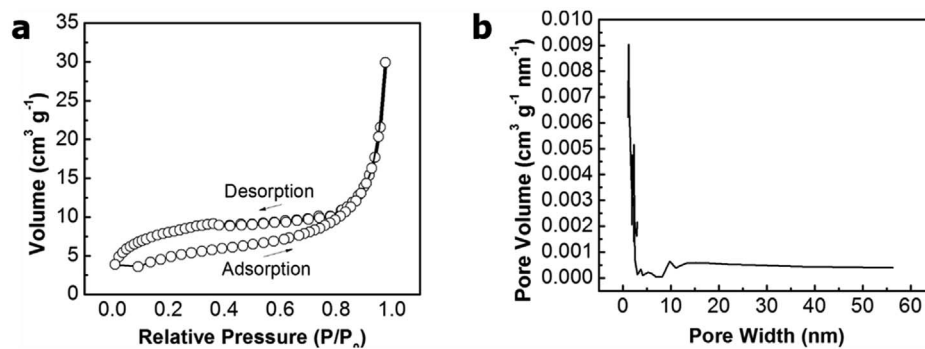


Figure 4 | (a) Type IV N₂ adsorption and desorption isotherms for SiO₂ NTs. (b) Pore size distribution of SiO₂ NTs.

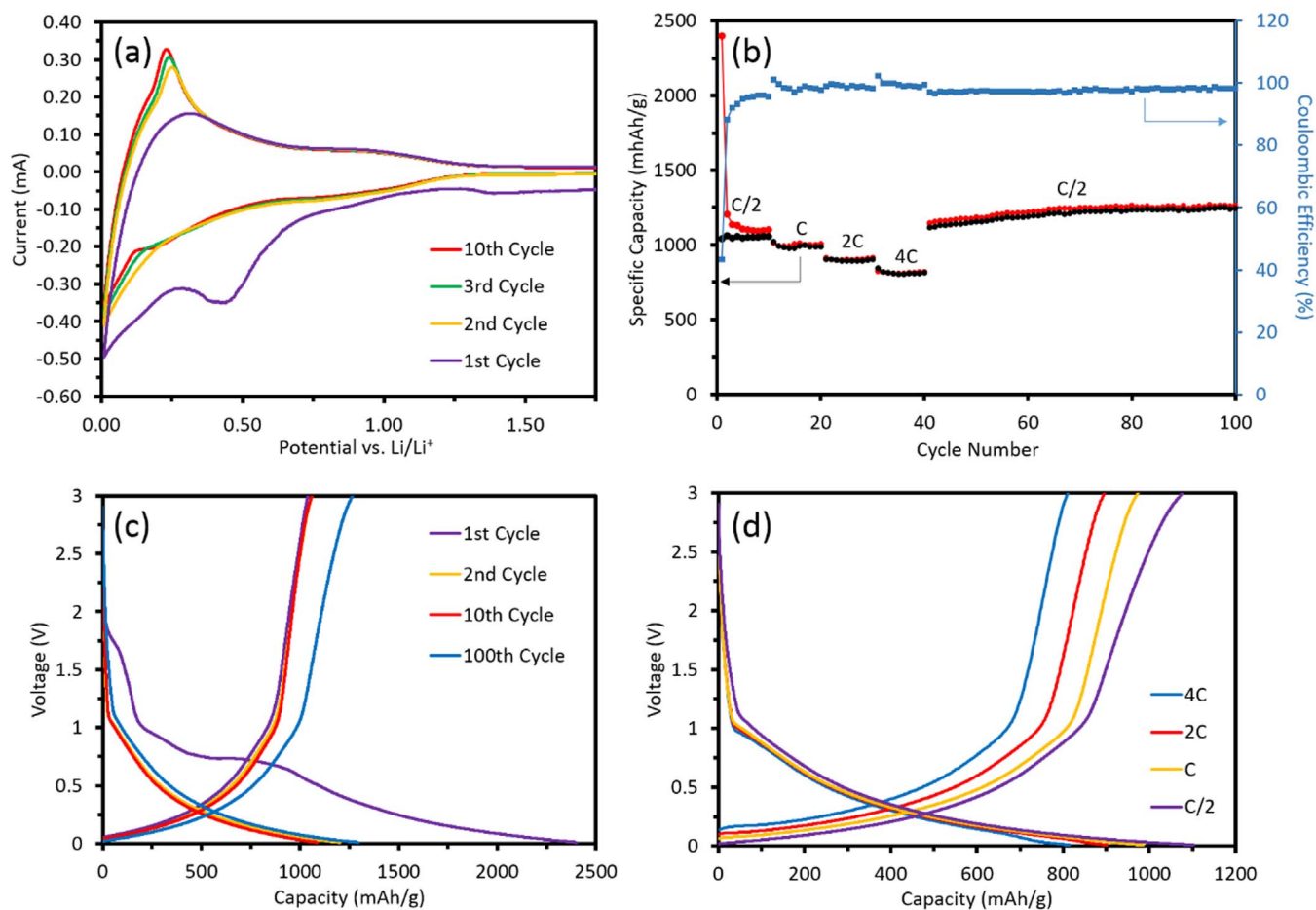


Figure 5 | (a) CV of SiO₂ NTs using a scan rate of 0.1 mVs⁻¹. (b) Charge-discharge capacities versus cycle number using a C rate of 100 mA g⁻¹. (c) Galvanostatic voltage profiles for SiO₂ NTs at a C/2 rate at selected cycles. (d) Galvanostatic voltage profiles for SiO₂ NTs at selected C rates.

et al. proposed capacity in SiO₂ anodes increases over time due to growth of the Si phase and, thus, a growth in Si volume. The formation of Li_ySiO_x at Si/SiO_x boundaries leads to the formation of three-fold coordinated Si [Si(III)] which reflects through SiO₄ tetrahedra to bond to the silicon phase. The capacity gained by inclusion of new Si atoms (~4 Li per Si) in the Si phase outweighs the loss in capacity due to the consumption of SiO₂ in the irreversible formation of Li_ySiO_x⁵⁴. We do not attribute this increase in capacity to increases in operating environment temperatures as several cells were tested in a staggered sequence with the same phenomenon observed in all cells. CV also supports this claim via the significant heightening and narrowing of

the dealloying peak, suggesting more Li⁺ is able to be dealloyed from the SiO₂ NTs in subsequent cycles. The emergence of an anodic peak at 0.22 V in the CV plot by the 10th cycle is consistent with the lithiation of Si.

Coin cells were disassembled after 100 galvanostatic charge-discharge cycles in an argon-filled glovebox. Electrodes were fully charged to remove reversibly alloyed lithium from the tubes. SiO₂ NT electrodes were rinsed with acetonitrile to remove electrolyte and then etched in 0.1 M HCl to remove the SEI layer and reveal post-cycling tube morphology. The SiO₂ NTs have undergone slight changes in morphology as evidenced in the TEM image in Fig. 6.

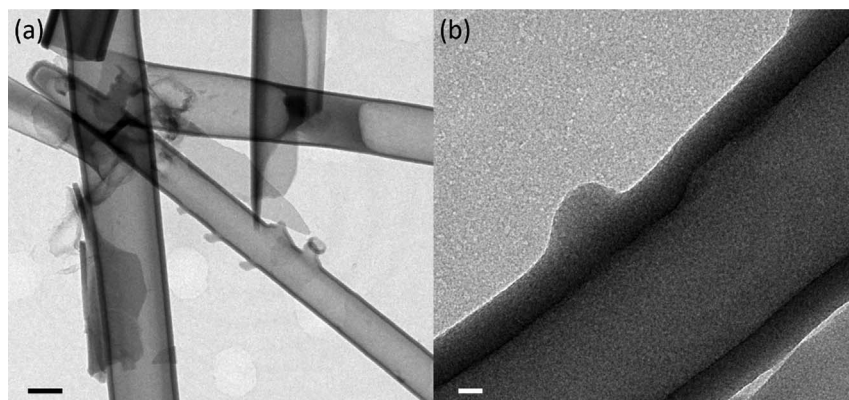


Figure 6 | (a) TEM image of an isolated SiO₂ NT after 100 galvanostatic charge-discharge cycles. (b) Higher magnification TEM image of the same SiO₂ NT emphasizing tube wall thickness and morphology. The scale bars are 200 nm and 20 nm for (a) and (b), respectively.



Cycled SiO₂ NTs still retain their tubular morphology but have developed slight undulations in the walls with variations in wall thickness. This preservation of the SiO₂ tube morphology post-cycling has also been confirmed by Wu et al. even after 2000 cycles¹⁰.

In conclusion, SiO₂ NTs have been fabricated via facile two step templated process through thermal degradation of PDMS in air under vacuum. This environmentally friendly synthesis route uses abundant, non-toxic, and non-flammable precursors and yields SiO₂ NTs that produce a capacity of 1266 mAh g⁻¹ after 100 cycles with minimal capacity fading. Galvanostatic cycling reveals a noticeable increase in capacity over the first 80 cycles, which is attributed to growth of a nano-Si phase upon generation of Li_ySiO₂ at Si/SiO₂ boundaries.

Methods

Synthesis of SiO₂ NTs were achieved via the following synthesis steps: Sylgard silicone elastomer was mixed in a 10 : 1 ratio with the curing agent and the mixture was set at 140 °C for 10 minutes to form a solid PDMS block. The PDMS block was cut via straight-blade into 50 mg blocks and placed in a graphite crucible. Whatman Anodic Aluminum Oxide templates with the following properties were used: 13 mm in diameter, 0.2 μm pore diameter, and 50 μm template thickness. Six AAO templates were placed inside the crucible next to the PDMS block and placed inside a quartz tube in a MTI GSL1600X box furnace. The system was pumped down to 300 torr with a slow ambient air flow to allow for sufficient oxygen supply for the PDMS thermal degradation reaction. The system was heated to 650 °C and held for 1 hour to allow for complete reaction of all PDMS. After cooling, templates were sonicated in IPA for 10 s to remove excess and loosely-bonded SiO₂ and dried under nitrogen stream. The SiO₂ coated AAO templates were placed in 50% wt H₃PO₄ and etched for 48 hours at 70 °C to completely dissolve the AAO template. The SiO₂ NTs were washed several times with DI water and dried at 90 °C under vacuum for 1 hour. SiO₂ NTs were then sonicated in IPA for 30 minutes to break apart the bundles of SiO₂ NTs and then dried under vacuum at 90 °C for 1 hour.

Characterization. The morphology of the sample is studied via scanning electron microscopy (SEM, leo-supra, 1550) with an X-ray energy-dispersive spectroscopy (EDS). Transmission electron microscopy (TEM, Philips, CM300) with an acceleration voltage at 300 kV is used to perform the high resolution imaging. The TEM sample was prepared by dropping pre-dispersed SiO₂ NTs onto carbon film coated TEM grids.

Electrochemical Measurements. Electrochemical performance of SiO₂ NTs was characterized vs. Li using CR2032 coin cells with an electrolyte comprising 1 M LiPF₆ in ethylene carbonate and diethyl carbonate (EC:DEC = 1 : 1, v/v). Electrodes were prepared via mixing SiO₂ NT powder, Super P acetylene black, and polyvinylidene fluoride (PVdF) in a weight ratio of 5 : 3 : 2. The slurry was then compressed onto copper foils and allowed to dry at 90 °C for 12 hours. Cells were assembled in an Argon-filled VAC Omni-lab glovebox. All cells were tested vs. Li from 0.01 to 3.0 V using an Arbin BT2000 at varying current densities. Cyclic voltammetry measurements were conducted on a Biologic VMP3 at a scan rate of 0.1 mVs⁻¹. The loading density of SiO₂ NTs on copper foils was 2.73 mgcm⁻².

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Author contributions

Z.F., W.W., M.O. and C.S.O. designed the experiments and wrote the main manuscript. Z.F., W.W., H.H.B. and A.G. worked on materials synthesis, battery fabrication, galvanostatic charge-discharge measurements, and testing at selected C rates. C.S.O. managed the research team. All authors reviewed the manuscript.

Additional information

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