

# **[Flexible digital signal processing architecture for narrowband](http://dx.doi.org/10.1063/1.4916261) [and spread-spectrum lock-in detection in multiphoton microscopy](http://dx.doi.org/10.1063/1.4916261) [and time-resolved spectroscopy](http://dx.doi.org/10.1063/1.4916261)**

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The lock-in amplifier is a critical component in many different types of experiments, because of its ability to reduce spurious or environmental noise components by restricting detection to a single frequency and phase. One example application is pump-probe microscopy, a multiphoton technique that leverages excited-state dynamics for imaging contrast. With this application in mind, we present here the design and implementation of a high-speed lock-in amplifier on the field-programmable gate array (FPGA) coprocessor of a data acquisition board. The most important advantage is the inherent ability to filter signals based on more complex modulation patterns. As an example, we use the flexibility of the FPGA approach to enable a novel pump-probe detection scheme based on spread-spectrum communications techniques. <sup>C</sup> *2015 AIP Publishing LLC.* [\[http:](http://dx.doi.org/10.1063/1.4916261)//[dx.doi.org](http://dx.doi.org/10.1063/1.4916261)/[10.1063](http://dx.doi.org/10.1063/1.4916261)/[1.4916261\]](http://dx.doi.org/10.1063/1.4916261)

## **I. INTRODUCTION**

Lock-in amplifiers (also known as phase-sensitive detectors) enable detection and measurement of small ac signals in the presence of overwhelming noise. These instruments are used in a variety of optical measurements and are especially critical in nonlinear and ultrafast optics laboratories. Implementing a lock-in amplifier with digital signal processing (DSP) techniques enables flexibility, applicationspecific customizations, and novel detection techniques innovations that go beyond mere improvements in speed and signal-to-noise ratio (SNR).<sup>1-[3](#page-8-1)</sup> Increasingly, DSP lock-ins are being developed on field-programmable gate array (FPGA) hardware instead of DSP microprocessors, $1-3$  $1-3$  in order to benefit from increased performance.<sup>[4](#page-8-2)</sup> Here, we present a digital lock-in implemented on the coprocessor FPGA of a highspeed data acquisition board, tailored to the requirements of nonlinear optical microscopy (i.e., multiphoton microscopy). The most important advantage is the inherent ability to filter based on more complex modulations. We also demonstrate a novel detection scheme for time-resolved optical spectroscopy based on spread-spectrum communication techniques.<sup>[5](#page-8-3)[,6](#page-8-4)</sup>

In recent years, a number of advanced microscopy techniques have emerged on the basis of detecting nonlinear optical interactions in biological tissue.<sup>[7](#page-8-5)[,8](#page-8-6)</sup> Examples include multiphoton-excited fluorescence, second- and third-harmonic generation, coherent anti-Stokes Raman scattering (CARS), stimulated Raman scattering (SRS), instantaneous and timedelayed optical Kerr effect, photothermal heterodyne mixing, and transient absorption (i.e., pump-probe). The challenges in implementing these come from the need to maximize both speed and sensitivity, in order to mitigate optical damage and sample larger volumes of tissue. Potential tissue damage

from both peak power and average power is a cause for  $concern;9$  $concern;9$  often, the safe deliverable power is far below what the laser can produce. In addition, most tissue imaging applications put a high premium on acquisition speed, to adequately sample the tissue volume in a reasonable time. This is especially important in cancer applications, and inadequate spatial sampling of the intrinsic heterogeneity can lead to misdiagnosis and underestimation of tumor aggressiveness.<sup>[10](#page-8-8)</sup>

Multiphoton microscopy techniques may be divided into two categories, based on the way in which the nonlinear interaction generates a detectable signal. Those that emit light at wavelengths that are distinct from the incident laser fluorescence, harmonic generation, and CARS—are readily detected with an optical filter and a sensitive photodetector (such as a photomultiplier tube (PMT)). However, nonemissive interactions (which often give completely different and useful contrast) must be detected via their perturbations to the incident light fields. In biological matter, these perturbations are small (on order of  $\lt 10^{-5}$ , limited by the multiphoton ionization and thermal damage considerations multiphoton ionization and thermal damage considerations mentioned above) and are easily overwhelmed by noise such as laser intensity fluctuations and Johnson noise. These perturbations may be detected interferometrically,  $11-13$  $11-13$  but the most common approach is to modulate the pump beam and filter the detected probe signal with a lock-in amplifier synchronized to the pump modulation. Lock-in amplifiers can also be used to measure lifetimes of other transient optical phenomena such as fluorescence and phosphorescence decay.

The requirements of lock-in amplifiers for multiphoton imaging applications are unique, in that speed is more important than phase accuracy. In order to mitigate  $1/f$  noise and be fast enough to permit imaging with reasonable frame rates, the lock-in must be able to detect modulations >1 MHz and have integration times  $<$ 100  $\mu$ s. Though suitable commercial instruments are available (e.g., from Signal Recovery, Stanford Research Systems, and Zurich Instruments), there

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has been growing interest lately in custom-built hardware for multiphoton imaging. For example, Saar *et al.* fabricated an analog lock-in to achieve video rate (25 frames/s) SRS imaging for *in vivo* applications.<sup>[14](#page-8-11)</sup> Other approaches forego the lock-in amplifier altogether by placing a resonant *LC* circuit in the photodiode amplifier, $15$  performing collinear balanced detection, $16$  or directly digitizing the photodiode signal, performing lock-in demodulation and further post-processing in software.<sup>[17](#page-8-14)</sup>

In this work, we have replaced the lock-in amplifier with a high-speed data acquisition board, performing all signal processing in the coprocessor FPGA. This approach is extremely flexible, as all aspects of the signal processing pipeline can be modified by editing the source code, recompiling, and downloading to the FPGA. This approach also has an advantage over analog hardware, in that the source code and/or compiled binaries can be shared electronically and downloaded to the target FPGA board—there is no need to fabricate new circuit boards, place, and solder components, etc. In this article, we first present details of the design and implementation, followed by two applications. The first is the basic single-frequency lock-in demodulation with an *in vivo* imaging application. The second goes beyond the capability of single-frequency lock-in amplifiers by using a spread-spectrum approach for characterizing long-time  $(\mu s)$ dynamics.

## **II. DIRECT NARROW-BAND LOCK-IN DETECTION BY DIGITAL SIGNAL PROCESSING**

## **A. Optical setup**

The experiments described here are performed on a custom-built laser scanning microscope, as depicted in Figure [1.](#page-1-0) Pump and probe pulses are generated by a Chameleon femtosecond oscillator (Coherent, Santa Clara, CA, USA) and a Mira optical parametric oscillator (Coherent). The pump is modulated by an acousto-optic modulator (AOM), then combined with the probe by a dichroic mirror, and directed into the microscope. For multiphoton fluorescence/SHG and confocal imaging, a data acquisition board (NI PXI-6259, National Instruments, Austin, TX) is used both for collecting data and controlling the galvanometric scan mirrors. Pumpprobe signal is collected with a polarizing beamsplitter and photodiode and digitized by an ATS-9626 high-speed data acquisition board (AlazarTech, Pointe-Claire, QC, Canada). All DSP operations are performed in the coprocessor FPGA of the ATS-9626. The two data acquisition boards are synchronized with a common 10 MHz timebase. Alternatively, a commercial lock-in amplifier (SR844, Stanford Research Systems, Sunnyvale, CA, USA) is used for comparison.

## **B. Design and implementation of FPGA-based lock-in amplifier**

Figure [2](#page-2-0) shows a block diagram of the lock-in amplifier. A brief overview follows; for more detail, see the Appendix. After digitization at 250 MS/s, the 16-bit input is downsampled to 25 MS/s, filtered to remove the DC offset, and directed to the mixer, where it is multiplied by the reference. The reference waveform is derived from a digital (square wave) synchronization signal in a few steps. A narrowband filter isolates the fundamental component of the square wave, which is differentiated to produce the quadrature component. These two components are summed in appropriate proportion to produce the reference sinusoid with the desired phase shift. This is then multiplied by the input signal in the mixer. Mixer output gain is controlled at this point by selecting a 16-bit slice from the 32-bit product to pass on to the lowpass filter stages. Three successive low-pass filter stages, each with  $100\times$  downsampling, produce final output rates of 250 kS/s, 2.5 kS/s, and 25 S/s, which are selected by

<span id="page-1-0"></span>

FIG. 1. Optical setup and electronics layout for nonlinear optical microscope including detection channels for confocal reflectance, multiphoton fluorescence, and pump-probe. Galvo scanners are controlled by the analog output of the NI PXI-6259 card. Confocal reflectance and multiphoton fluorescence signals are captured by the NI PXI-6259 analog inputs. Pump-probe detection is accomplished through either the SR844 lock-in or the custom FPGA firmware loaded on to the Alazar ATS-9626.

<span id="page-2-0"></span>

FIG. 2. Block diagram schematic of FPGA-based lock-in amplifier. Fixed-point datapath widths specified in Qn.m format (see the Appendix).

the output multiplexer (MUX). Filters are implemented as multi-stage finite impulse response (FIR) in order to conserve resources (see the Appendix). Other signals, such as the phaseshifted reference wave or the mixer output, can be optionally selected by the MUX. The output of the MUX is passed to the data acquisition board's main FPGA, cached in the on-board memory, and retrieved by the microscope control software through the peripheral component interface (PCI) bus.

#### **C. Results**

#### *1. 2-photon absorption in rhodamine 6G*

We compare signal to noise performance in a multiphoton imaging application against the SR844 lock-in amplifier, measuring 2-photon absorption at 720/817 nm in rhodamine 6G crystals, for varying pixel sample rates. The SR844 is operated in "no filter" mode (nominal output rate of 48–96 kS/s, depending on operating frequency); its output is captured by the National Instruments data acquisition board (NI-DAQ), running at a 250 kS/s sample rate. The FPGA lock-in is operated in 250 kS/s output mode. Effective pixel rate is varied by changing the number of samples averaged per pixel, e.g., 10 samples/pixel =  $25$  kS/s. To make a side-by-side comparison, each image is normalized by subtracting the mean pixel value and dividing by the overall standard deviation. The results are shown in Figure [3.](#page-3-0) Signal to noise ratio is compared for the slowest pixel rate, 12.5 kS/s. Noise is estimated as the standard deviation in a blank region, outlined by the white box, and signal is estimated as the standard deviation in the region outlined with a blue dashed box. Both instruments have comparable SNR at low imaging speeds, but only the FPGA lock-in produces sharp images at high speeds.

A practical upper limit on imaging speed will depend on several factors. On our microscope, the galvo scanners currently limit pixel rates to 250 kS/s, but the ATS-9626 is capable of acquiring and streaming the full 250 MS/s over the PCI express bus. In our design, all multipliers are pipelined, so that in principle, the output of the mixing step can run at 250 MS/s. From there, the maximum demodulation frequency will be limited to the Nyquist frequency of  $f_{\text{max}} = 125 \text{ MHz}$ , and the maximum output sample rate from the low-pass filtering step will be limited to 125 MS/s, if the initial  $10\times$  decimation step is bypassed. However, the typical 80 MHz Ti:sapphire

oscillator employed in multiphoton microscopes will limit  $f_{\text{max}}$  to 40 MHz. Note that potential high-speed applications were one of the reasons for using FIR instead of infinite impulse response (IIR) filters (see the Appendix). It should also be noted that there are commercially available highspeed data acquisition/FPGA boards that exceed several GS/s throughput, so much higher speeds can eventually be realized with the approach described here.

#### *2. In vivo pump-probe imaging of melanin*

Our primary target application in developing this instrument is for *in vivo* pump-probe microscopy of pigmented lesions and melanoma. Using the traditional lock-in amplifier, we have been imaging models of human melanoma in mice, in order to characterize changes in melanocyte pigment chemistry during the early stages of tumor formation.<sup>[18–](#page-8-15)[20](#page-8-16)</sup> For this application, imaging speed is important because of the limited time window for imaging during anesthesia and the large size of the areas to be sampled. The image shown in Figure [4](#page-4-0) is the first *in vivo* z-stack acquired with the new FPGA-based lock-in amplifier, acquired under a protocol approved by the Institutional Animal Care and Use Committee at Duke University. The subject is a brown-haired mouse (Jackson Labs Strain C57BR/cdJ), under isoflurane anesthesia. The contrast in the pump-probe channel comes from excited state absorption in melanin in the hair.<sup>[21](#page-8-17)</sup> Multiphoton fluorescence/SHG is acquired simultaneously with a PMT through the NI-DAQ board, providing complementary structural contrast. The entire  $512 \times 512 \times 150$  stack was acquired in less than  $3 \text{ min} - 10 \times$  faster than would be possible with the SR844.

## **III. DIRECT-SEQUENCE SPREAD SPECTRUM APPROACH FOR MEASURING LONG-LIVED STATES**

In this section, we demonstrate an application that goes beyond the capabilities of a traditional lock-in amplifier. Timedomain measurements of excited-state photodynamics are often measured with low repetition rate lasers, so that the material has time to relax back to a thermal equilibrium state in between repetitions of the pump pulse. In biomedical imaging applications, high repetition rate lasers are employed, trading

<span id="page-3-0"></span>

FIG. 3. Comparing speed and SNR between SR844 and FPGA lock-in devices and detecting 2-photon absorption on crystals of rhodamine 6G on a slide. Images with 256×256 resolution were acquired at 3 scan rates: 12.5 kS/s (5.2 s/frame), 50 kS/s (1.3 s/frame), and 250 kS/s (0.26 s/frame). Signal and noise estimated from regions indicated with blue dashed box and white box, respectively.

off pulse energy for more signal averaging; in this scenario, using a pulse picker to reduce repetition rate and allowing for relaxation between pulses would severely limit SNR, because far fewer pulses would be averaged in the measurement period. Here, we employ a spread-spectrum modulation to recover long-lived pump-probe signals directly in the time domain, using a high repetition rate laser.

## **A. Frequency-domain lifetime measurements with lock-in detection**

The most common strategy for measuring lifetimes that exceed the laser repetition rate is to modulate the pulse train at a lower frequency and use a lock-in to determine the frequency-dependent phase response of the system. The detected signal is a convolution of the optical response with the excitation waveform. In the case of a sinusoidally modulated excitation beam and a single-exponential decay,

$$
y = \cos(2\pi f t) \otimes \exp(-t/\tau).
$$

In the case of a single exponential, the relationship between the modulation frequency  $f$ , decay time  $\tau$ , and the measured phase  $\theta$  is<sup>[22](#page-8-18)</sup>

$$
\theta = \operatorname{atan}(2\pi f \tau).
$$

The frequency must be selected near  $2\pi f \approx 1/\tau$  in order to accurately determine the lifetime from the measured phase. In practice, *f* is scanned over a range (i.e., swept-spectrum or chirped excitation frequency) and  $\theta$  is fit to an arctangent to estimate  $\tau$ . Of course, the above analysis assumes a singleexponential decay behavior. Multi-exponential kinetics can be recovered from these measurements through numerical model fitting.<sup>[23](#page-8-19)</sup> Though frequency-domain and time-domain approaches are in principle equivalent, being related to one another by a Fourier transform, there are tradeoffs between the two methods that determine which will be more appropriate to a given measurement scenario. $6,24$  $6,24$ 

#### **B. Principle of direct-sequence spread spectrum modulation for measuring excited state dynamics**

Here, we implement a spread-spectrum approach to encode the excited state dynamics with a pseudo-random binary sequence, such that the time-resolved kinetics can

<span id="page-4-0"></span>

FIG. 4. *In vivo* image of mouse hair and follicle acquired at 250 kS/s (0.26 s/frame). Fluorescence/SHG and pump-probe images acquired simultaneously. 3D reconstruction rendered from a z-stack using Avizo (FEI, Hillsboro, Oregon, USA).

be directly retrieved from the measured signal. Such an approach to time-resolved luminescence has been proposed and simulated, $6$  but has not yet to our knowledge been implemented for real-time measurements of optical transients nor has it been applied to pump-probe microscopy. Modern communications devices (e.g., cellular phones and WiFi) encode their transmissions with a broad-band carrier.<sup>[25](#page-8-21)[,26](#page-8-22)</sup> One way to do this is to mix the signal with a pseudorandom binary sequence (PRBS); the receiver can only decode the transmission if it can produce a copy of the encoding sequence. This approach, known as direct-sequence spread spectrum (DSSS), makes both eavesdropping and jamming more difficult, but it has another important feature which we shall leverage here. The decoder must not only reproduce the encoding sequence but also match the time delay between the received signal and the sequence precisely. This is because a pseudo-random sequence is non-repetitive and is uncorrelated with time-delayed copies of itself. This autocorrelation property can be used in a pump-probe or phosphorescence lifetime experiment to selectively "tune" specific time delays of the excited state relaxation by varying the electronic delay between the pseudo-random sequence and the measured signal. This we call a time-resolved directsequence spread spectrum (TR-DSSS) measurement, and it could have significant advantages for high-speed imaging scenarios where the excited state relaxation is slower than the pixel dwell time, such as phosphorescence lifetime<sup>[27](#page-8-23)</sup> and photothermal heterodyne microscopy.[28](#page-8-24)

Similarly to the frequency domain case, the measured signal is a convolution of the PRBS with the optical impulse response *h*(*t*),

$$
y(t) = [h \otimes \text{PRBS}](t) = \int_{-\infty}^{\infty} h(t') \cdot \text{PRBS}(t - t') dt'
$$

The signal at a particular electronic time delay ∆*t* is demodulated by multiplication with a time-delayed copy of the PRBS and low-pass filtering (integration),

$$
s(\Delta t) = \int_{-\infty}^{\infty} y(t) \cdot \text{PRBS}(t - \Delta t) dt
$$

$$
= \int_{-\infty}^{\infty} [h \otimes \text{PRBS}](t) \cdot \text{PRBS}(t - \Delta t) dt.
$$

It can be shown that the measured signal is the optical response convolved with the autocorrelation of the pseudo-random sequence

$$
s(\Delta t) = \int_{-\infty}^{\infty} [h \otimes \text{PRBS}](t) \cdot \text{PRBS}(t - \Delta t) dt
$$
  
= 
$$
\int_{-\infty}^{\infty} [h \otimes \text{PRBS}](t) \cdot \text{PRBS}(-(\Delta t - t)) dt
$$
  
= 
$$
[[h \otimes \text{PRBS}](t) \otimes \text{PRBS}(-t)](\Delta t)
$$
  
= 
$$
[h \otimes [\text{PRBS}(t) \otimes \text{PRBS}(-t)]](\Delta t)
$$
  
= 
$$
h \otimes [\text{PRBS} * \text{PRBS}],
$$

where ∗ denotes the cross-correlation operator. This enables TR-DSSS to directly resolve decay kinetics by varying the electronic time delay ∆*t*, without requiring *a priori* knowledge of the optical response *h*. Note that the instrument response function (IRF) is, by definition, PRBS ∗ PRBS.

#### **C. Implementation and experimental setup**

Figure [5](#page-5-0) shows a schematic of the TR-DSSS FPGA layout. The main difference between this and the lock-in depicted in Figure [2](#page-2-0) is the use of a linear feedback shift register to generate a pseudo-random modulation sequence, which is fed to both an internal delay line preceding the mixer and the digital output port. This sequence is passed to the AOM modulation electronics to impose the pseudo-random sequence on the pump beam. The pseudo-random sequence is generated at 1 MS/s with a linear feedback shift register, and the electronic time delay is accomplished by a 256-bit shift register run at the 25 MHz DSP clock, and a time delay selector MUX. Each time delay step is therefore  $1/25$  MHz = 40 ns and the total range is  $10.24 \mu s$ . Because the pseudo-random sequence is non-repetitive over the measurement timescale, detection operates at baseband and a balanced photodiode must be used to subtract laser intensity fluctuations from the probe beam.

## **D. Experimental demonstration: Measurement of triplet state relaxation**

For a proof-of concept experiment, we use a porphyrin- $Ru(II)$ polypyridyl chromophore (Pyr<sub>1</sub>RuPZnRuPyr<sub>1</sub>)<sup>[29](#page-8-25)</sup> that has a <sup>∼</sup><sup>9</sup> µs triplet lifetime under oxygen-free conditions, which would normally require a laser repetition rate well below 100 kHz to ensure relaxation between subsequent pump pulses. Here, we use the same 80 MHz oscillator and optical

<span id="page-5-0"></span>

FIG. 5. Block diagram schematic for direct-sequence spread spectrum detection.

<span id="page-5-1"></span>

FIG. 6. Lifetime measurements of porphyrin-Ru(II)polypyridyl chromophore. (a) TR-DSSS measurement, along with fit to biexponential model. IRF shown for comparison. (b) Traditional laser flash transient absorption measurement, excitation 550 nm, probe 812–822 nm, along with fit to single exponential model.

parametric oscillator (OPO) as described before, tuned to  $\lambda_{\text{pu}} = 720$  nm and  $\lambda_{\text{pr}} = 817$  nm. The results are shown in Figure [6.](#page-5-1) The blue dashed line in Fig.  $6(a)$  shows the TR-DSSS IRF, which was measured by admitting a small amount of the pump beam directly on the probe detector (100  $\mu$ V<sub>pp</sub>). The IRF is the autocorrelation of the pseudo-random sequence. The 1  $\mu$ s half-width reflects the 1 MS/s sequence rate (referred to as the "chip rate" in the spread-spectrum literature). The peak position at sequence delay increment 28 reflects the round-trip electronic delay of the sequence from generation to demodulation. This delay includes the FPGA auxiliary output, modulation electronics and AOM, photodetector, and FPGA digitizer input. This autocorrelation peak position is used to calibrate  $t = 0$  delay. The TR-DSSS scan of the chromophore (with the optical delay fixed at −1 ps and scanning the electronic delay) is shown in the green line. Nonlinear fitting with MATLAB reveals a biexponential, with a long-time constant of  $8.95 \pm 0.36$  µs (black line). For comparison, measurement with a 550 nm laser pulse excitation and a flash lamp probe in the range of 812–822 nm (the approximate bandwidth of the probe used in the TR-DSSS measurement) is shown in Figure  $6(b)$ . These data reveal a triplet state lifetime of 8.14  $\pm$  1.32  $\mu$ s. Considering the different focal conditions, excitation wavelength, and low SNR of the laser flash transient absorption measurement, the agreement between the two measurements is reasonable. These results demonstrate that a spread-spectrum approach to pump-probe can measure timeresolved dynamics, even when the relaxation timescales far exceed the laser repetition period. Here, the DSSS approach was able to resolve microsecond relaxation kinetics with an 80 MHz source (12.5 ns repetition rate). Furthermore, it is in principle possible to instantiate multiple mixers and low-pass filters on a single device, to decode multiple sequence delays simultaneously, analogous to a DSSS rake receiver $30$ —this could enable an entire TR-DSSS delay scan to be acquired in one step.

#### **IV. CONCLUSION**

We have implemented a lock-in amplifier on the coprocessor FPGA of a high-speed data acquisition board and demonstrated its use in a pump-probe microscope. The FPGA approach improved imaging speed significantly, while

enabling a novel approach to pump-probe detection using a spread-spectrum technique, TR-DSSS. In TR-DSSS, the pump is modulated by a pseudorandom binary sequence, so that optical transients with long relaxation timescales can be probed with high repetition rate lasers. This could provide a significant improvement for methods that detect slowly relaxing optical transients, such as photothermal microscopy<sup>[28](#page-8-24)</sup> and phosphorescence lifetime microscopy. $2<sup>7</sup>$  In both of these, the pixel scan rate is limited by the long relaxation time—if the focal spot moves too quickly, residual signal from the previous pixels will spill over to the current pixel. A parallelized TR-DSSS approach would allow fast scanning by "tagging" the signal of each previous pixel with a unique pseudorandom code. Finally, it is our hope that this FPGA approach will enable more sophisticated real-time analysis, such as discriminating the linear and nonlinear contributions to pumpprobe signals, which is becoming increasingly important in the applications of pump-probe microscopy for melanoma imaging. $19,21$  $19,21$ 

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#### **APPENDIX: IMPLEMENTATION DETAILS**

The lock-in was implemented on an ATS-9626 data acquisition board (AlazarTech, Pointe-Claire, QC, Canada), with 250 MS/s sample rate and 16-bit analog-to-digital converter (ADC), with  $\pm 1.25$  V input range, each discrete step corresponding to approximately 38  $\mu$ V. Image acquisition is controlled through custom software written in C, using the LabWindows/CVI development environment (National Instruments, Austin, TX, USA). The FPGA design was implemented in VHDL (very high-speed integrated circuit hardware description language), using the Quartus II development environment (Altera, San Jose, CA, USA), targeting the Altera Stratix III EP3SL50F780C4 FPGA on the ATS-9626. Our VHDL project used the example digital downconverter project supplied by AlazarTech as a starting point. The compiled design uses 37% of the available logic elements, 30% of the embedded memory, and 45% of the DSP 18-bit multipliers.

#### **1. Data format and representation**

The ADC provides data as 16-bit unsigned values, at 250 MS/s. These are converted to 16-bit signed (two's complement) values, representing fixed point numbers with 1 sign bit, 1 integer bit, and 14 fractional bits—Q16.14, in Qn.m format ( $n =$  total number of bits and  $m =$  number of fractional bits). Note that a 16-bit multiplication results in a 32-bit wide output,  $Q16.14 \times Q16.14 = Q32.28$ . Therefore, each multiplier (except in the FIR filters) is followed by a rescaler that casts the result back to a Q16.14 number. This can introduce errors from loss of precision and saturation, which can be avoided by using floating-point numbers instead. However, floating-point operations come at the expense of reduced speed and may introduce problems with timing closure (see Subsection 4 of the Appendix).

#### **2. Effective digitizer resolution and sample rate**

Digital lock-in requires first that the input be sampled by an ADC, which, together with the analog amplifiers and filters that precede it, is the main determinant of performance characteristics such as sensitivity and input noise.[2](#page-8-28) We chose to directly sample the signal provided by the amplified photodiode rather than to use a highpass filter and/or preamplifier. This was done to avoid added noise associated with a preamplifier and to preserve the lowfrequency components, which are required for DSSS despreading. At first glance, it may seem that this requires a digitizer with extremely high resolution to adequately sample a small probe perturbation. For example, a 1  $\mu$ V modulation on a 1 V background in principle requires a million discrete levels (20 bits). However, subsequent low-pass filtering brings with it an effective bit gain. $31$  This allows sensitive lock-in detection with reasonably manageable ADC widths (12 bits). For example, a 16-bit ADC at 250 MS/s downsampled to 250 kS/s results in 21 effective bits output, or 1.2  $\mu$ V per step. To keep this added precision, the data path after the front-end  $10\times$  downsampler is expanded to 19 bits and the rescaler following the demodulation mixer selects a 16-bit output window that is shifted by 2 bits toward the least significant bit.

#### **3. Output low-pass filters**

Broadly speaking, digital filters can be implemented in one of two forms: FIR and IIR. FIR filters can be more straightforward to design but may consume more resources. IIR filters use feedback, which can allow for longer-time response with fewer resources, but can be challenging to implement at high data rates, because data must finish propagating through the feedback loop before the next sample arrives. In addition, it can be challenging to design IIR filters with linear phase response (phase curvature can lead to ringing artifacts at the output). For these reasons, we implemented all filtering operations as FIR filters with linear phase. Long time constants were achieved by chaining several FIR filters together. To implement the lowpass preceding the

<span id="page-7-0"></span>

## (a) 2x decimation Nyquist filter design

## (b) 5x decimation Nyquist filter design



# (c) Measured 100x decimation filter response



FIG. 7. 100× decimating lowpass filter design and validation. MATLAB fdatool parameters and theoretical response for the 2× stage shown in (a) and the 5× stage shown in (b). Measured response of the composite 100× decimation filter shown in (c).

 $100\times$  decimation step with a single FIR filter would require 1000 filter taps; by contrast, a chain of  $5\times$ ,  $5\times$ ,  $2\times$ , and  $2\times$ decimating filters requires only 156 taps. These filters were designed in MATLAB using the filter design and analysis tool, with the goal of minimizing frequency content above

the Nyquist rate of 125 kHz, for the 250 kS/s output. The position of the 16-bit output slice was selected to maintain approximately unity gain through the filter. Design parameters for the individual filters and response of the composite 100× decimating low-pass filters are shown in Figure [7.](#page-7-0)

#### **4. Timing and synchronization**

Here, we discuss several aspects of timing and synchronization related to the DSP lock-in: pixel clock synchronization with a secondary data acquisition board, timing closure within the design, and reference wave synchronization with the pump modulation. Pixel clocks are synchronized by driving the ATS-9626 master 250 MHz ADC clock with a phase-locked loop (PLL) that is locked to a 10 MHz reference supplied by the NI-DAQ board.

Timing closure is very important for high-speed FPGA circuits. A mismatch in propagation delay between different data paths that exceeds one clock period can lead to serious problems. Because our target demodulation frequency is only 1–10 MHz, we can relax these timing constraints by first downsampling the incoming 250 MS/s data stream to 25 MS/s and perform all DSP operations at 25 MHz, a factor of 10 below the maximum clock rate of the ATS-9626. The 25 MHz DSP clock is derived from the master 250 MHz clock by a PLL. In addition, we perform all arithmetic in signed fixed-point format rather than floating point, for the sake of increased speed (a 16-bit floating point multiply requires  $4 \times 250$  MHz clock cycles, while a 16-bit fixed-point multiply requires only one).

As described in the text earlier, the reference waveform is derived from an external transistor-transistor logic (TTL) signal as follows. The TTL is cast as a square wave and filtered through a narrow-band 2 MHz IIR filter to produce a sinusoidal waveform. A quadrature reference is produced by numerically differentiating the reference wave and an arbitrarily phase-shifted reference is produced by summing the in-phase and quadrature references in varying proportion.

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