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High Breakdown Voltage and Low Dynamic ON-Resistance AlGaIn/GaN HEMT with Fluorine Ion Implantation in SiN_x Passivation Layer

Chao Yang, Xiaorong Luo^{*} , Tao Sun, Anbang Zhang, Dongfa Ouyang, Siyu Deng, Jie Wei and Bo Zhang

Abstract

In this study, we proposed and experimentally demonstrated a high breakdown voltage (BV) and low dynamic ON-resistance ($R_{ON, D}$) AlGaIn/GaN high electron mobility transistor (HEMT) by implanting fluorine ions in the thick SiN_x passivation layer between the gate and drain electrodes. Instead of the fluorine ion implantation in the thin AlGaIn barrier layer, the peak position and vacancy distributions are far from the two-dimensional electron gas (2DEG) channel in the case of fluorine ion implantation in the thick passivation layer, which effectively suppresses the direct current (DC) static and pulsed dynamic characteristic degradation. The fluorine ions in the passivation layer also extend the depletion region and increase the average electric field (E-field) strength between the gate and drain, leading to an enhanced BV. The BV of the proposed HEMT increases to 803 V from 680 V of the conventional AlGaIn/GaN HEMT (Conv. HEMT) with the same dimensional parameters. The measured $R_{ON, D}$ of the proposed HEMT is only increased by 23% at a high drain quiescent bias of 100 V, while the $R_{ON, D}$ of the HEMT with fluorine ion implantation in the thin AlGaIn barrier layer is increased by 98%.

Keywords: AlGaIn/GaN HEMT, Fluorine ion implantation, SiN_x passivation layer, Breakdown voltage, Dynamic ON-resistance

Background

In recent decades, novel semiconductor materials, such as GaN, metal oxides, and 2D materials, have been widely studied to further enhance the energy conversion and storage efficiency, owing to their superior material and device properties [1–8]. Among them, GaN-based AlGaIn/GaN high electron mobility transistors (HEMTs) are good candidates for high power, high frequency, and low loss applications because of high critical breakdown field and high electron mobility [9–14]. The breakdown voltage (BV) is one of the most important design targets, and the reported values are still far below the theoretical limit [15, 16]. Therefore, it is of great importance to further improve the BV, especially not at the cost of increasing the device size. Several termination

techniques have been proposed to improve the BV, such as field plate [17–19], fluorine ion implantation [20–22], and recessed gate-edge termination [23, 24]. Fluorine ions implanted in the thin AlGaIn barrier layer (FBL) [22] has a simple fabrication process without inducing an additional parasitic capacitance; however, the peak position of the fluorine profile and vacancy distributions is near to the two-dimensional electron gas (2DEG) channel, which would inevitably cause significant static and dynamic characteristic degradation.

In this work, a high breakdown voltage and low dynamic ON-resistance ($R_{ON, D}$) AlGaIn/GaN HEMT with fluorine ion implantation in the SiN_x passivation layer (FPL HEMT) is experimentally investigated. Unlike in the case of the fluorine ion implantation in the thin AlGaIn barrier layer, fluorine ion implantation in the thick passivation layer could keep the peak position of fluorine and vacancy distributions far away from the 2DEG channel, thus effectively suppress the static and

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dynamic characteristic degradation. Fluorine ions in the passivation layer as a termination technique are also used to optimize the surface electric field (E-field) distribution, thus achieving an enhanced BV. In conclusion, the FPL HEMT demonstrates excellent static characteristics and dynamic characteristics.

Fabrication Methods

Figure 1 is the three-dimensional schematic of FPL HEMT, FBL HEMT, and Conv. HEMT, respectively. All devices feature a gate length L_G of 2.5 μm , a gate-source distance L_{GS} of 1.5 μm , and a gate-drain distance L_{GD} of 10 μm . The epitaxial AlGaIn/GaN heterostructure used for fabricating the FPL HEMT was grown on 6-in (111) silicon substrate by metal organic chemical vapor deposition (MOCVD). The epitaxial layers consist of a 2-nm GaN cap, 23-nm $\text{Al}_{0.25}\text{Ga}_{0.75}\text{N}$ barrier, 1-nm AlN interlayer, 150-nm GaN channel, and 3.5- μm GaN buffer. The Hall effect measured density and mobility of the 2DEG were $9.5 \times 10^{12} \text{ cm}^{-2}$ and $1500 \text{ cm}^2/\text{Vs}$, respectively. The proposed FPL HEMT started with mesa isolation which was implemented by a high power Cl_2/BCl_3 -based inductively coupled plasma (ICP) etching. Then, a 40-nm-thick low pressure chemical vapor deposition (LPCVD) SiN_x layer was deposited at $780^\circ\text{C}/300 \text{ mTorr}$ with a NH_3 flow of 280 sccm and a SiH_2Cl_2 flow of 70 sccm, yielding a deposition rate of 3.7 nm/min. The refractive index is measured by ellipsometer as 1.978, and the N/Si ratio of SiN_x is 1.31 [25]. The crystallinity of LPCVD SiN_x is amorphous, which is confirmed by high-resolution transmission electron microscope (HR-TEM) micrograph (see the inset of Fig. 1a). After opening the source and drain contact

windows by SF_6 plasma dry etching, the Ti/Al/Ni/Au (20/150/45/55 nm) ohmic contact was deposited and annealed at 890°C for 30 s in N_2 ambient. The contact resistance of $1 \Omega \text{ mm}$ and sheet resistance of $400 \Omega/\text{square}$ were extracted by the linear transmission line method. Next, the gate metal electrode is formed by Ni/Au (50 nm/150 nm) deposition and lift-off process. Then, the fluorine ion implantation window (Length of window = 3 μm) is formed by AZ5214 photoresist, and fluorine ions were implanted by SEN NV-GSD-HE ion implanter at an energy of 10 keV at a dose of $1 \times 10^{12} \text{ cm}^{-2}$. Finally, the samples were annealed at 400°C for 15 min in N_2 ambient to complete the transistor fabrication flow [26].

Results and Discussion

Figure 2 shows the measured secondary ion mass spectroscopy (SIMS) profile of fluorine ion concentration and simulated vacancy concentration by TRIM along the cut lines: (a) A-A' of FPL HEMT and (b) B-B' of FBL HEMT, respectively. At the same energy and dose of fluorine ion implantation, the measured peak position from the surface and maximum concentration of the fluorine profile is almost the same for the two structures. In the case of the fluorine ion implantation in the thin AlGaIn barrier layer, the vacancies induced by fluorine extend to the 2DEG channel region. The distribution of vacancy concentration is discontinuous at each interface because the bond energy of every material is different [27]. However, in the case of the fluorine ion implantation in the thick SiN_x passivation layer, the vacancy distribution is located within the SiN_x passivation layer and far from the 2DEG channel. The vacancies induced by

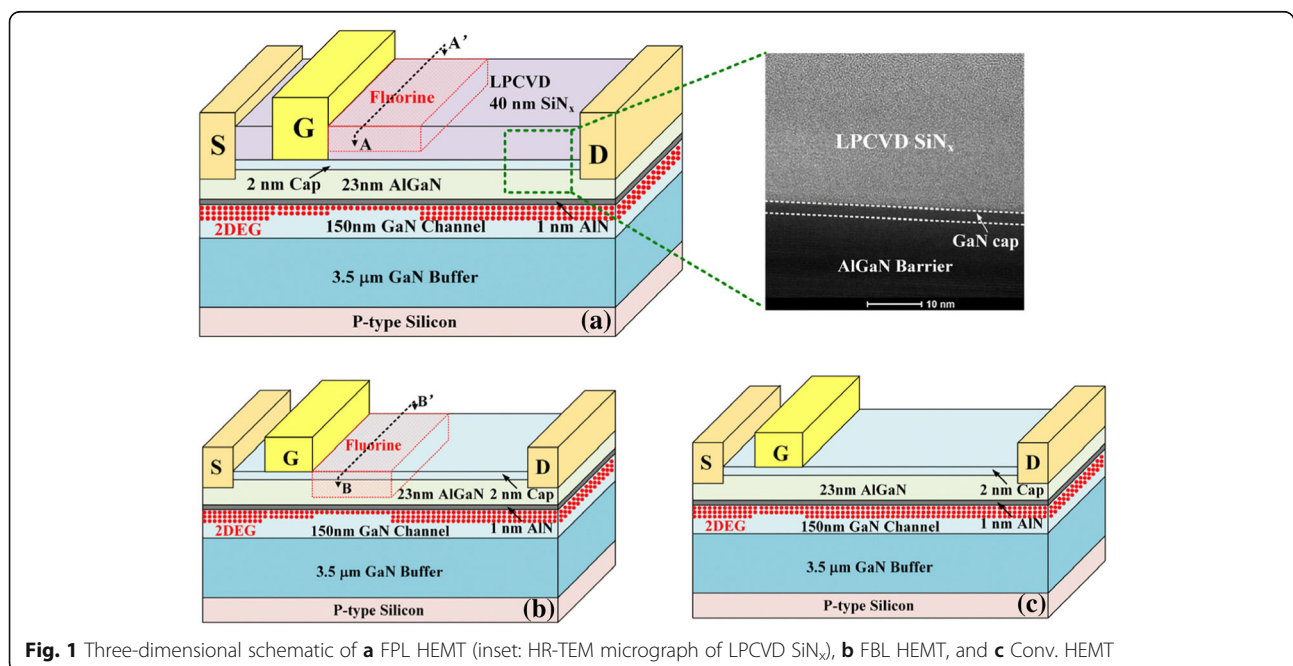
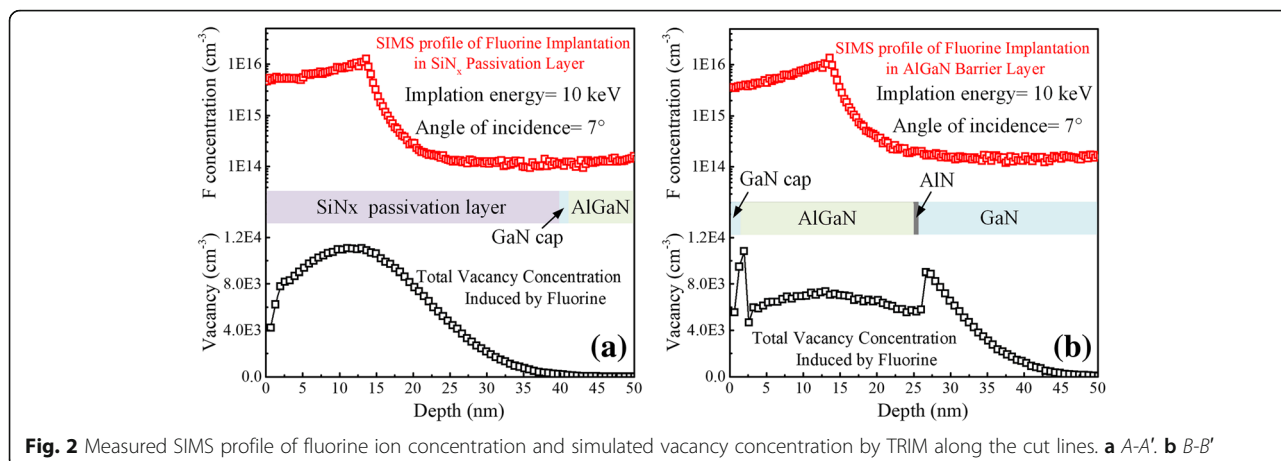


Fig. 1 Three-dimensional schematic of **a** FPL HEMT (inset: HR-TEM micrograph of LPCVD SiN_x), **b** FBL HEMT, and **c** Conv. HEMT

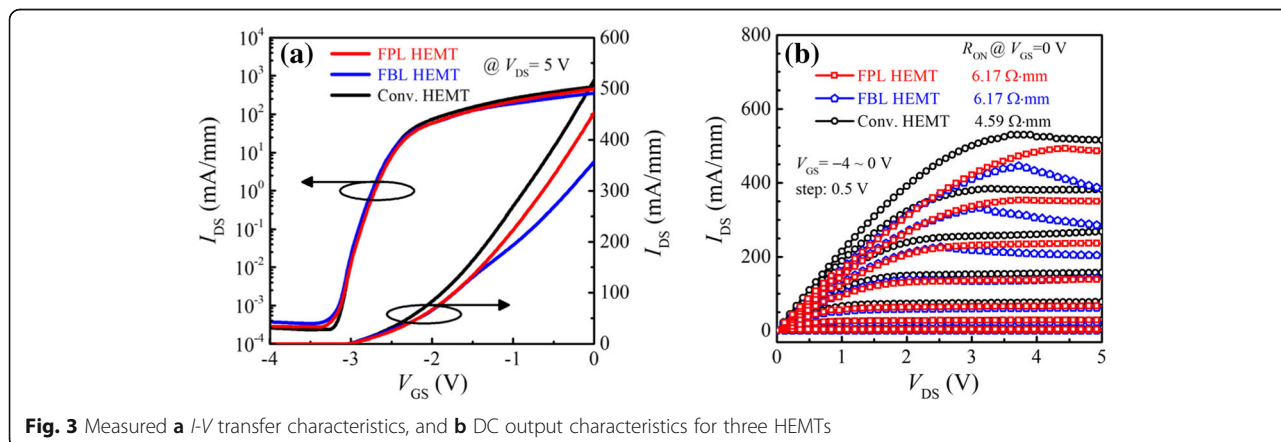


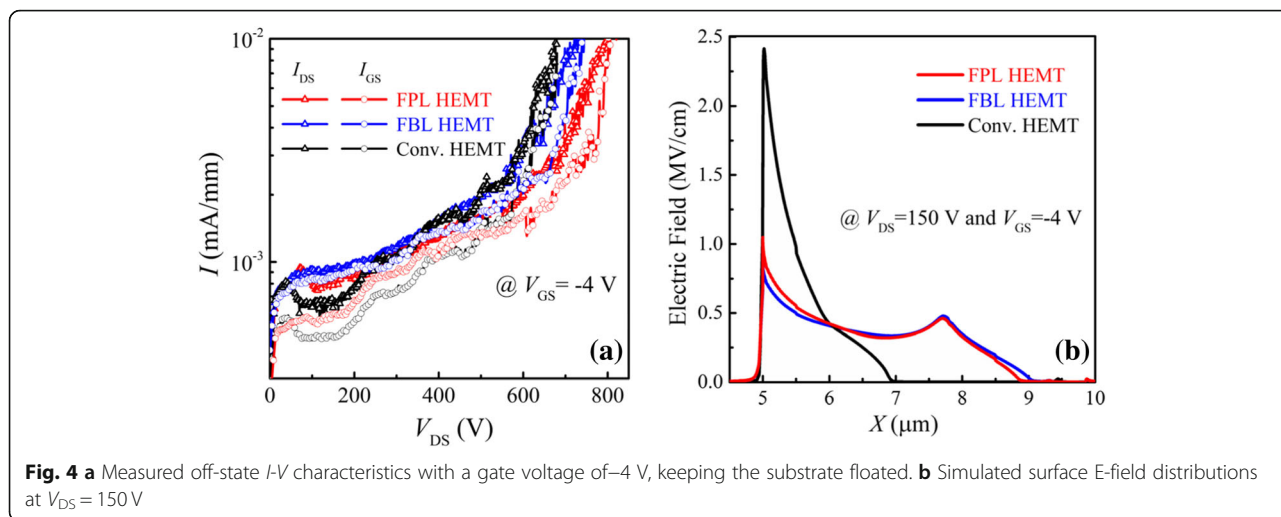
ion implantation would trap the 2DEG channel, and 2DEG could be easily trapped if the vacancy distribution is near to the 2DEG [28]. In conclusion, fluorine ion implantation in the thick SiN_x passivation layer could significantly reduce the influence of ion implantation on the 2DEG channel and suppress the static and dynamic characteristic degradation effectively.

Figure 3 illuminates the measured *I*-*V* transfer characteristics and direct current (DC) output characteristics. Compared with the Conv. HEMT, both the FPL HEMT and FBL HEMT show a decrease in *I*_{DS} and an increase in static ON-resistance (*R*_{ON}), because the fluorine ions cause the assisted depletion of the 2DEG in the drift region and thus decrease the 2DEG density [29]. In addition, the ion implantation also decreases the 2DEG mobility. The Hall effect measured 2DEG mobilities of the FPL and FBL HEMTs are 228 cm²/Vs and 203 cm²/Vs after ion implantation, respectively. Owing to the same dose of fluorine ions, the output characteristics and *R*_{ON} of FPL HEMT and FBL HEMT are almost the same at a low drain voltage (e.g., *V*_{DS} < 3 V). However,

when *V*_{DS} > 3 V, the saturation drain current collapse occurs in the FBL HEMT, because the vacancy profile of fluorine extends to the 2DEG channel region, and the 2DEG could be easily trapped by these deep level vacancies induced by fluorine when drain voltage is large than critical drain voltage (e.g., *V*_{DS} > 3 V) [30], thereby decreasing the drain current. The vacancy distribution of FPL HEMT is far from the 2DEG channel, thus suppressing the saturation drain current collapse effectively.

Figure 4 shows the measured *I*-*V* characteristics and simulated surface E-field distributions on the blocking state. The BVs of the FPL/FBL/Conv. HEMTs are 803/746/680 V, respectively. The BV is defined as the drain-source voltage at the drain current (*I*_{DS}) of 1 μA/mm with *V*_{GS} = -4 V. The fluorine ions between the gate and drain as a termination technique reduce the E-field peak at the gate edge and cause a new E-field peak at the end of ion implantation region, and thus, FPL HEMT and FBL HEMT achieve more uniform surface E-field distribution and higher BV than that of the Conv. HEMT. Compared with FPL HEMT, FBL HEMT has an

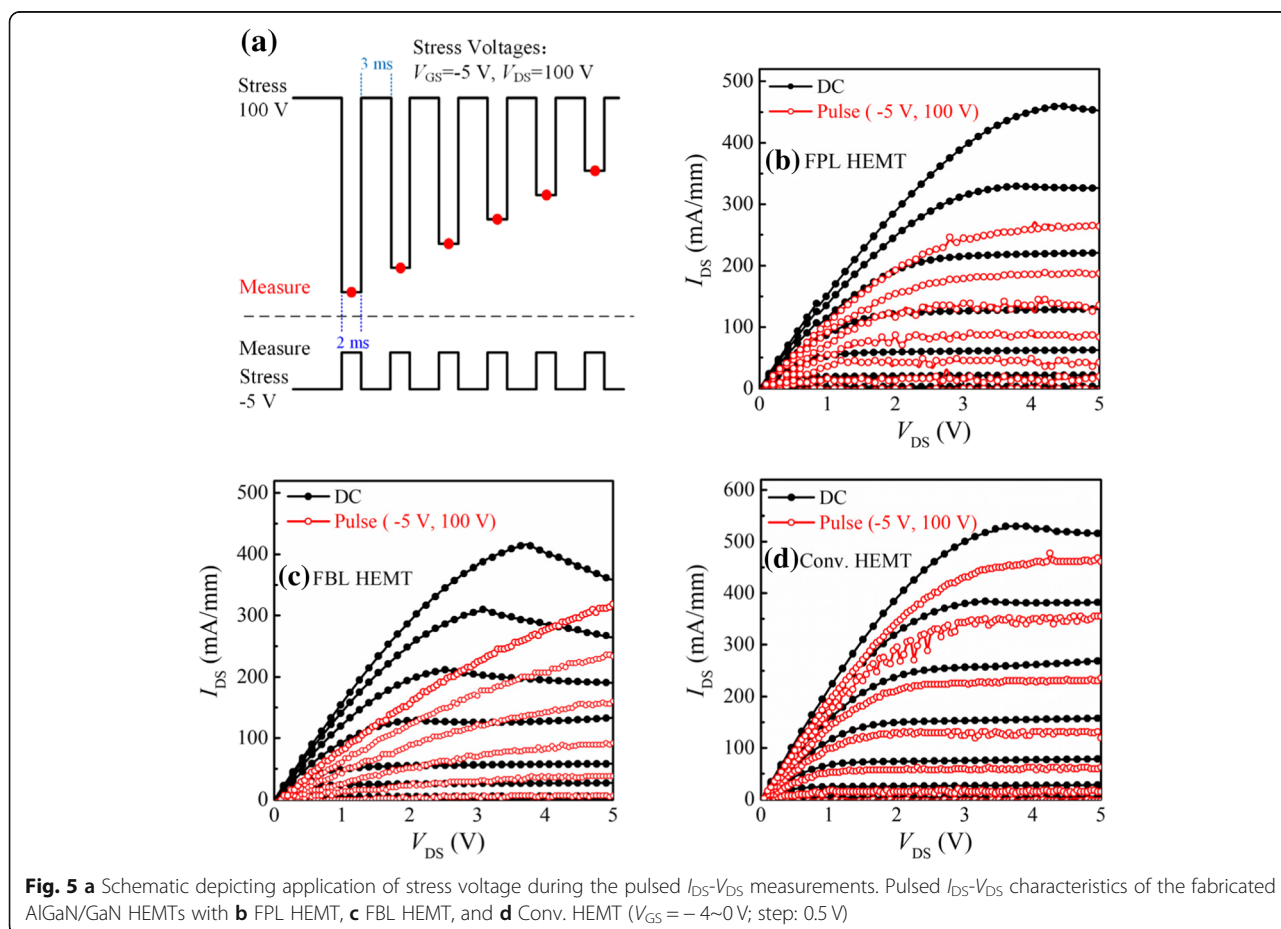




enhanced electric field modulation effect, because the fluorine ion profile is near to the 2DEG channel. However, for the FBL HEMT, ion implantation would inevitably induce additional damages in AlGa_N barrier [31, 32], leading to a continuous gate leakage current path of

gate-barrier layer-2DEG; therefore, the BV of FBL HMET is slightly smaller than that of the FPL HEMT.

Pulsed I_{DS} - V_{DS} measurements [33] under slow switching were performed to characterize the behavior of dynamic ON-resistance ($R_{ON, D}$) of the fabricated AlGa_N/



GaN HEMTs. Figure 5a is the schematic depicting the application of stress voltage during the pulsed $I_{DS}-V_{DS}$ measurements. In pulsed $I-V$ measurements, the gate and drain electrodes of the GaN HEMTs were subjected to short voltage pulses before each $I-V$ measurement to ensure that the devices were in the off-state. The pulse width is 3 ms and the period is 5 ms. Figure 5 b–d show the comparison of the pulsed output characteristics of the devices under (V_{GS0}, V_{DS0}) of (0 V, 0 V) and (0 V, 100 V). It can be seen that there is a slightest degradation (12.3%) of dynamic ON-resistance for the Conv. HEMT, owing to the absence of fluorine ion implantation process. In comparison with FBL HEMT, FPL HEMT has a low degradation of dynamic ON-resistance. Owing to the passivation layer, the vacancy distribution is far away from the 2DEG channel and is located within the passivation layer, which suppresses the charge trapping in the FPL HEMT. Figure 6 summarizes the ratio values of $R_{ON,D}/R_{ON}$ for the three HEMTs under (V_{GS0}, V_{DS0}) from (0 V, 0 V) and (0 V, 100 V) at a step of 20 V. For the FBL HEMT, the measured $R_{ON,D}$ is already increased by 98% of the static one at (V_{GS0}, V_{DS0}) of (0 V, 0 V) and (0 V, 100 V), while the $R_{ON,D}$ of the FPL HEMT is increased by only 23% at a high drain quiescent bias of 100 V.

Conclusions

In conclusion, we proposed a novel AlGaIn/GaN HEMT with a high breakdown voltage and low dynamic ON-resistance. It features fluorine ion implantation in the thick SiN_x passivation layer. Fluorine ion implantation in passivation layer could effectively suppress electrical

characteristic degradation. The dynamic ON-resistance is only 1.23 times as larger as the static ON-resistance after off-state V_{DS} stress of 100 V, while it is 1.98 times for the FBL HEMT. In addition, the fluorine ions in the passivation layer also modulate the E-field distribution and spread the depletion region; thus, the BV of the proposed HEMT increases to 803 V from 680 V of conventional AlGaIn/GaN HEMT.

Abbreviations

2DEG: Two-dimensional electron gas; HEMT: High electron mobility transistor; ICP: Inductively coupled plasma; LPCVD: Low pressure chemical vapor deposition; MOCVD: Metal organic chemical vapor deposition; SIMS: Secondary ion mass spectroscopy; TEM: Transmission electron microscope

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Not applicable

Authors' Contributions

CY conceived and performed the experiments and data analysis. XRL supervised this work. All authors discussed the results and contributed to the final manuscript. All authors read and approved the final manuscript.

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Availability of Data and Materials

All data generated or analyzed during this study are included in this published article.

Competing Interests

The authors declare that they have no competing interests.

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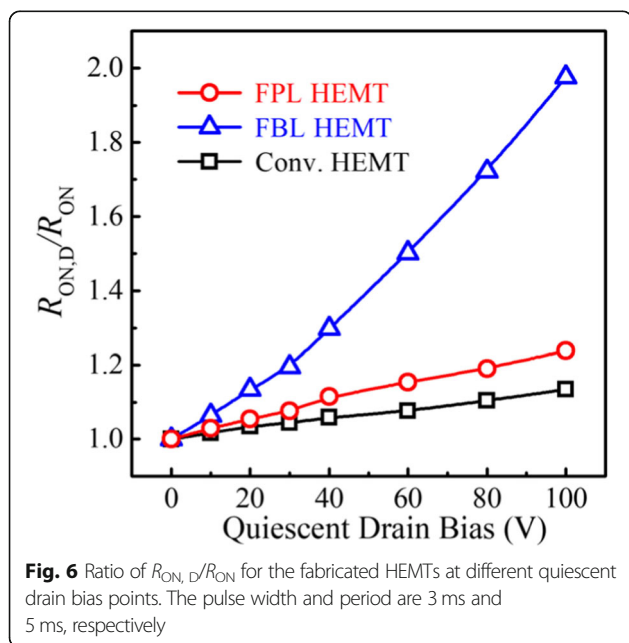


Fig. 6 Ratio of $R_{ON,D}/R_{ON}$ for the fabricated HEMTs at different quiescent drain bias points. The pulse width and period are 3 ms and 5 ms, respectively

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