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An Advanced 100-Channel Readout System for Nuclear Imaging

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Abstract

Reading out from large-scale silicon photomultiplier (SiPM) arrays is a fundamental technical obstacle blocking the application of revolutionary SiPM technologies in nuclear imaging systems. Typically, it requires using dedicated application-specific integrated circuits (ASICs) that need a long iterative process, special expertise, and tools to develop. The pico-positron emission tomography (Pico-PET) electronics system is an advanced 100-channel readout system based on 1-bit sigma-delta modulation and a field-programmable gate array (FPGA). It is compact ($6 \times 6 \times$ 0.8 cm³ in size), consumes little power (less than 3W), and is constructed with off-the-shelf lowcost components. In experimental studies, the Pico-PET system demonstrates excellent and consistent performance. In addition, it has some unique features that are essential for nuclear imaging systems, such as its ability to measure V-I curves, breakdown voltages, and the dark currents of 100 SiPMs accurately, simultaneously, and in real time. The flexibility afforded by FPGAs allows multiple-channel clustering and intelligent triggering for different detector designs. These highly sought-after features are not offered by any other ASICs and electronics systems developed for nuclear imaging. We conclude that the Pico-PET electronics system provides a practical solution to the long-standing bottleneck problem that has limited the development of potentially advanced nuclear imaging technology using SiPMs.

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Index Terms

Field-programmable gate array (FPGA); nuclear imaging; readout electronics; sigma-delta modulation; silicon photomultiplier (SiPM)

I. Introduction

SILICON photomultipliers (SiPMs) have become the photon sensor of choice for nuclear imaging detector and system development. This has enabled a great deal of promising and novel technology research and development Digital Object Identifier 10.1109/TIM. 2018.2877952 for advanced nuclear imaging, including positron emission tomography (PET), single-photon emission computed tomography, Gamma cameras, and multimodality imaging applications [1]–[5]. Compared to conventional photomultiplier tubes (PMTs), SiPMs not only provide compatible high-gain and fast performance but also many other unique features, such as nearly uniform gain over an active area on the same element, negligible inactive edges, low-sensitivity to strong magnetic fields, a very compact size, a lower voltage bias, and a potentially much lower cost of manufacture. This makes SiPMs very attractive for a range of applications. In nuclear imaging, the availability of large-scale SiPM arrays has led to technology breakthroughs in the development of compact PET detectors, scanners, and related imaging systems, such as commercially available high-performance clinical PET/MRIs, and so on [4], [5].

However, it is very challenging to read out large-scale analog SiPM arrays consisting of a large number of detection channels: hundreds to thousands, or more. Several ways of mitigating this problem have been investigated. One approach is to use a resistor-based multiplexing network circuit to reduce the number of readout channels, such as that used for reading a multichannel or position-sensitive PMT [6]. This was tested using small size prototype PET detectors with older SiPM arrays. However, due to a notably high intrinsic capacitance (~100 pf) and high levels of dark current for each SiPM, the signal combined from the output of multiple SiPMs was substantially slower and noisy, leading to severely compromised timing performance [7]. The bulky component size, error-prone discrete component connection, and a limited number of channels that can be multiplexed make this method not an ideal solution for clinic PET systems consisting of a large number of detectors.

The time-over-threshold (TOT) approach is another commonly used method [8]–[10]. In this method, one or a few comparators are used to convert the input analog pulses to digital pulses. The timing and the energy of the input analog pulses are obtained by measuring the starting edge and the duration of the digital pulses, respectively. The TOT methods are very cost efficient and power efficient, since they do not require the conventional analog-to-digital conversion (ADC) for energy measurement. However, the accuracy of the energy measurements using the TOT methods is significantly lower than the conventional ADC methods. Thus, the TOT methods are mainly used in detectors with crystals and SiPMs one-to-one coupled to each other, not in applications that require to measure the energy accurately. For example, the commonly used dual-ended readout detectors require to read

the energies from two SiPMs accurately to decode the depth of interaction [11], [12]. The conventional light-sharing detectors and the advanced monolithic and semimonolithic scintillator detectors require to read the energies from many SiPMs accurately to decode the position of interaction [13]–[15].

Another approach is to use compact application-specific integrated circuit (ASIC) electronics. This has been the preferred solution for developing SiPM-based PET systems [16]–[24]. Compared with regular printed circuit design with off-the-shelf components, the ASIC design requires more expertise, a larger budget, and a longer development time. This has prevented most researchers from using ASICs to develop practical SiPM-based PET systems. In addition, each ASIC is usually dedicated to specific SiPM and detector design requirements. This makes it even harder to develop different ASICs to match different SiPM arrays—a serious concern for ASIC development in view of the recent speed at which SiPM technology has been developing, with new products and significant performance improvements (across different device and signal characteristics) constantly appearing. In short, ASICs might provide suitably high performance, but they are better suited to dedicated SiPM and detector designs and lack the versatility needed to handle diverse SiPM devices and designs.

Yet another approach is to use digital SiPMs (dSiPMs, Philips digital photon counting [25], invented in 2005). These put the readout electronics on the same chip as the SiPM. In principle, this is an ideal solution and initial observations of their application look encouraging. However, dSiPMs have a much longer readout time (680 ns [26], higher power consumption (600 mW for an array of 4×4 chips [27]), are difficult to scale up because of their bulky size, and are very expensive. Therefore, most small research groups and companies cannot develop these kinds of imaging systems. The strict licensing agreement is a further limiting factor for most technology research and development groups. At present, dSiPMs are still being researched and offer few prototype imaging systems. Therefore, it is unlikely they will replace analog SiPMs any time soon, especially as more and more lower cost SiPM products are being introduced with steadily improved performance, not to mention the exponentially increasing range of applications for SiPMs in radiation detection, nuclear imaging, and other fields.

Recently, a novel readout electronics design based on 1-bit sigma-delta modulation has been proposed and validated in both simulations and experimental studies [28], [29]. Very different from conventional front-end electronics designs, the key innovation here is to include almost all functions of the front-end readout electronics, including signal splitting for energy and timing measurements, signal clustering, and event triggering for detectors with different configurations, ADC, time-to-digital conversion (TDC), and so on, inside a low-cost field-programmable gate array (FPGA). This not only simplifies the analog components and reduces the cost but also provides powerful and flexible signal processing. This, in turn, can support the application of different algorithms to both enhance performance and add new real-time dark current measurement and calibration features.

In this paper, we present a 100-channel readout electronics system (called Pico-PET) based on 1-bit sigma-delta modulation, FPGAs, and off-the-shelf low-cost components. We

describe the design of Pico-PET and evaluate its performance across direct current offset, energy, and dark current measurements. Pico-PET's flexibility and decoding performances were assessed using four different detector modules constructed with discrete and monolithic scintillator crystals. This paper is organized as follows. In Section II, the hardware and firmware configurations of the Pico-PET electronics are introduced. The experimental setups used to assess the Pico-PET system are also given in detail. In Section III, we present the experimental results. In Sections IV and V, the main features of Pico-PET are summarized and discussed.

II. Methods

A. Hardware Configuration

Fig. 1(a) shows the hardware configuration. A 100-channel Pico-PET electronics system consists of an FPGA board and an analog board. The FPGA board has: a low-cost FPGA (Intel Cyclone VE 5CEBA7); a 67-Mb EPCS flash memory; a 16-Mb synchronous dynamic random-access memory; 10 pairs of low-voltage differential signaling (LVDS) input output ports for clock distribution, synchronization, command communication, and list mode data transmission; a joint test action group port for on-line system debugging; an onboard 50-MHz local clock; and a micro universal serial bus (USB) 2.0 port (data rate: ~40 Mb/s) for plug-and-play event data communications from/to a host device such as a computer. The 50-MHz local clock was fed into the phase-locked loop in the FPGA to generate a 1-GHz system clock.

The Pico-PET electronics deploys four 1.25G LVDS transceivers to enable data and command communications between modules. In addition, two low-voltage positive emitter-coupled logic (LVPECL) input ports and two LVPECL output ports are reserved for receiving and transmitting the global clocks and the signal for global synchronizations. Thus, multiple Pico-PET electronics modules can be conveniently scaled up to construct large-scale systems with different network topologies, such as tree or daisy chain.

The analog board consists of 100 low-power operational amplifiers and two 60-pin flexible printed circuit cables to connect to photosensors. The amplifiers were configured as shown in Fig. 1(b) to implement 1-bit over sampled delta-sigma ADCs [28]–[30]. Note that the comparator and the driver were implemented using a digital LVDS input port and a digital TTL output port from the FPGA [28], [29], respectively. Thus, the Pico-PET system only requires one amplifier, one capacitor, and two resistors per readout channel.

Fig. 2 shows pictures of the custom-designed FPGA board, the analog board, and the assembled 100-channel Pico-PET electronics module. The size of both boards is $6 \text{ cm} \times 6$ cm. They are connected together with three board-to-board connectors. The size of the assembled module is $6 \text{ cm} \times 6 \text{ cm}$.

B. Firmware Configuration

Fig. 3 shows the firmware framework for the Pico-PET system. The internal functional blocks are grouped into three categories: 1) event data processing blocks (green) read data from analog input and generate list mode data; 2) command blocks (black) process the

commands for the system settings and calibrations; and 3) communication blocks (blue) provide digital data communication services for the system. Note that the event data processing blocks measure both the energy and time of a single event. The time measurements involve high-precision FPGA-based TDCs [31], [32]. This, however, is beyond the scope of this paper.

C. Experimental Settings

Six experiments were conducted to evaluate the system's performance and assess its unique features. The experimental settings are described in detail in the following. All the experiments were performed at room temperature (about 21 $^{\circ}$ C).

1) DC Current Measurements: A simple direct current testing board (shown in Fig. 4) was designed and manufactured to characterize the performance of the 100-channel 1-bit oversampled delta-sigma ADCs in the Pico-PET system. The test board consisted of 100 1- $M\Omega$ resistors. A dc power supply was controlled by a host PC to provide output voltage at 0.001 V per step to generate dc currents ranging from 1 to 3000 nA. The outputs of the 1-bit registers in the FPGA were summed for 5 s for each step (*N*₅). The dc currents (*Im*) were calculated as [28]

$$I_m = \frac{N_5}{5} \cdot \frac{V_f}{R_f} \cdot \frac{1}{f} \quad (1)$$

where Vf is the voltage (2.5 V) when the FPGA TTL output is "1." Rf is the value of the feedback resistor (5 k Ω). f is the clock frequency (1 GHz).

In this experiment, all 100 dc current channels were measured simultaneously and transferred to the host PC via a USB 2.0 port. The transfer function between the input current (I_{in}) and the calculated current (Im) was then derived and analyzed. A simple linear model was used to assess the linearity of the transfer function

$$I_m = kI_{\rm in} + I_0 \quad (2)$$

where k and I_0 are the slope and offset, respectively.

2) SiPM V-I Curve Measurements: The dark current generated in a SiPM is the product of thermally induced dark pulses, afterpulses, and crosstalk. It is temperature and bias voltage dependent [33], [34]. Many applications need the breakdown voltages [35] and gains [36] of individual SiPMs to be characterized. Traditionally, this is done by measuring one by one the dark current of single SiPMs with different voltages (called the *V-I* curve) [37], [38].

Six 10 × 10 SiPM arrays (MicroFC30035, SensL Inc.) were designed and manufactured inhouse to assess the capacity of the 100-channel Pico-PET electronics with SiPM *V*-*I* curve measurements. The size of the individual SiPMs and the 10 × 10 arrays were 3 mm × 3 mm

and 30 mm × 30 mm, respectively. As shown in Fig. 5, the SiPM arrays were powered by a dc power supply that was controlled by a host PC to output voltages from 20 to 30 V, with a step of 0.1 V. Like the dc current experiments, the outputs of all the 1-bit registers in the FPGA were summed simultaneously for 5 s for each step and transferred to the host PC via a USB 2.0 port. The dark current for each step was calculated using (1) to derive the *V-I* curve. The breakdown voltage (V_{br}) and the dark current at V_{br} + 2.5 V for 600 SiPMs in all six arrays were calculated and compared to the values provided by the SiPM vendor.

3) Energy of Test Pulses: An important task for the front-end electronics in a nuclear imaging system is to measure the energy of gamma events. This is accomplished using the amplitudes and/or durations of electronic pulses. A simple test pulse board was fabricated to test the ability of the 100-channel Pico-PET electronics to measure the energy of input pulses. As shown in Fig. 6, the FPGA boards generated test pulses with widths ranging from 2to 128 ns at 2-ns steps. These were fed to the analog board. The FPGA was programmed to calculate the energy of all the input pulses by summing the 1-bit digital output from the registers. The calculated energies were transferred to the host PC via the USB 2.0 data interface. The transfer functions between the widths and the energy measurements of the input pulses were derived for all 100 channels. Note that all the test pulses had the same amplitude. Thus, the energy measurements can be expected to be proportional to the widths of the pulses.

The pulse experiments were repeated $200 \times$ to characterize repeatability and uniformity across 100 channels of energy measurements. The scatter plots of the pulse widths versus the calculated energies were derived and fit using linear models for all 100 channels

$$E = k \cdot PW + b \quad (3)$$

where E is the calculated energies and PW is the width of the input pulses, k and b are the slope and offset, respectively. The least significant bits (LSBs) of the calculated energy E are equivalent to electric charge

$$Q_{\rm LSB} = \frac{V_f}{R_f} \cdot \frac{1}{f} \quad (4)$$

where $V_f = 2.5$ V, $R_f = 5$ k Ω , and f = 1 GHz. Therefore, $Q_{LSB} = 0.5$ pC.

The scatter plot was then subtracted using the fitted model to show the variation in the energy calculations. The mean and standard deviation (STD) of the 100 slopes k and intercepts b were calculated and compared.

4) One-to-One Coupled Detector: A PET detector was constructed to assess the performance of the 100-channel Pico-PET system when measuring gamma energy. The detector (see Fig. 7) consisted of a 10×10 array of 3 mm \times 3 mm \times 20 mm Lutetium-Yttrium Oxyorthosilicate (LYSO) scintillator crystals and a 10×10 MicroFC30035 SiPM

array. The 10×10 scintillating crystal array was one-to-one air coupled with the 10×10 MicroFC30035 SiPM array (bias voltage: 27.5 V). The detector was mounted in a light-tight encasement fabricated out of carbon fiber materials and a ²²Na source (20μ Ci) was shone on it. The FPGA was programmed to automatically perform event triggering, energy calculation, and list-mode event data generation for all of the individual SiPMs independently. The event data were transmitted to the host PC via the USB 2.0 port in real time. The energy spectra, photopeaks, and energy resolutions of all 100 individual crystals were measured and compared. There was no need to decode the individual crystals because they were one-to-one coupled with the individual SiPMs.

5) Crystal Decoding Performance: Two detector modules were constructed with SiPMs and crystals of different sizes to assess Pico-PET's crystal decoding performance and ability to read out from different types of SiPMs. The first module [Fig. 8(a)] was constructed with an 8×8 array of $1.5 \text{ mm} \times 1.5 \text{ mm} \times 5 \text{ mm}$ LYSO scintillator crystals air coupled to a part (4×4) of the 10×10 MicroFC30035 SiPM array. The surfaces of the individual SiPMs were protected by a glass layer of 0.35 mm in thickness. There is an air gap about 0.5 mm in thickness between the LYSO array and the surface of the SiPM array. The protective glasses and the air gap act together as the light guide to allow the light sharing between the SiPMs. The bias voltage of the SiPMs was set to 27.5 V. The FPGA was programmed to cluster 16 channels for event triggering, energy calculation, and list-mode event data generation.

The second detector [Fig. 8(b)] was constructed with a 30×30 array of very fine 0.445 mm $\times 0.445$ mm $\times 20$ mm LYSO scintillator crystals air coupled to a 2×2 array of 7.75 mm \times 7.75 mm linearly graded SiPMs (LG-SiPM, developed by Fondazione Bruno Kessler) [39], [40]. The 0.445-mm crystal pitch is the finest currently available. The bias voltage of the LG-SiPMs was set to 35 V. The LG SiPM implements the conventional resistor-based multiplexing network circuit inside the SiPM pixel. The event signal is split into four channels. Thus, every LG-SiPM output four channels of signals. Only four channels from one LG-SiPM were connected to the 100-channel Pico-PET electronics in this experiment. The energy measured from all four channels was added to generate valid event triggering, rather than triggering on four channels individually. The FPGA was programmed to cluster four channels for event triggering, energy calculation, and list-mode event data generation.

In this experiment, a ²²Na source (20 μ Ci) was shone on the detectors and the event data were sent to the host PC via the USB 2.0 port in real time. Crystal position decoding maps were generated using the traditional anger logic method [39], [41]. The quality of the position decoding maps was assessed qualitatively using profile analysis.

6) Readout Light Distribution in a Monolithic Scintillator: In a monolithic detector, the 3-D positions of the gamma interactions in a scintillator crystal are calculated from the distribution of scintillating photons on the surface of the crystal [15], [19], [42]–[44]. A piece of monolithic LYSO scintillating crystal (30 mm × 30 mm × 20 mm, see Fig. 9) was manufactured for this experiment to verify the ability of the Pico-PET system to read out 2-D distributions of scintillating photons. Five surfaces of the monolithic crystal were wrapped with a Vikuiti enhanced specular reflector (ESR). The last surface was air coupled

directly to the 10×10 MicroFC30035 SiPM array. The bias voltage of the SiPMs was set to 30 V. As before, a ²²Na source (20 μ Ci) was shone on the detector. The FPGA was programmed to cluster all 100 channels for event triggering, energy calculation, and list-mode event data generation. All 100 channels of energy readouts of valid gamma events were transmitted to the host PC simultaneously. The typical measurements for the 2-D light distributions were captured and reviewed.

III. Results

A. DC Current Measurements

Fig. 10(a) shows the transfer functions between the input dc currents and the measurements from all 100 channels. The 100 transfer functions are identical and have excellent linearity $(R^2 = 0.9999)$. Fig. 10(b) shows the slope distribution for the 100 transfer functions. The mean and the STD of the 100 slopes were 1.0276 and 0.0033, respectively. The uniformity over the 100 channels was excellent. Note that the measured currents were slightly larger (2.76%) than the true input dc current because the FPGA TTL voltage was slightly lower than the expected 2.5 V. This is something that can easily be calibrated when necessary.

B. SiPM V-I Curve Measurements

Fig. 11(a) shows the measured *V*-*I* curves from the six 10×10 MicroFC30035 SiPM arrays. The 100-channel Pico-PET system was able to measure dark currents as low as about 100 pA at the bias voltage of 20 V. The distribution of the measured breakdown voltages of the 600 SiPMs (mean \pm STD: 24.6 V \pm 0.089 V) is shown in Fig. 11(b). The measurements agree well with the values provided in the SiPM datasheet (typical value: 24.5 V; minimum value: 24.25 V, and maximum value: 24.75 V). Fig. 11(c) compares the measured dark currents with the values given in the datasheet. The range of the measured dark currents at overvoltages of 2.5 and 5.0 V was 200–300 nA and 900–1500 nA, respectively. These values agree reasonably well with the maximum dark currents listed in the datasheet (530 nA at 2.5 V and 2520 nA at 5V).

C. Energy of the Test Pulses

Fig. 12(a) shows the 200 plots of the input pulse widths versus the energies measured from a typical channel. Again, the 200 plots are identical and have excellent linearity. Variation in the 200 repeated measurements [see Fig. 12(b)] is smaller than ± 4 LSB. As calculated in (4), an LSB is equivalent to 0.5-pC electric charge. The STD of the variations is 1.36 LSB (0.68 pC). The distribution of the STDs of the variations of all 100 channels [Fig. 12(c)] ranges from 0.5 to 1.5 LSB or 0.25 to 0.75 pC. Fig. 12(d) and (e) shows the distribution of the normalized slopes *k* and offsets *b* for the 100 linear transfer functions. The results show that the 100 channels have excellent gain (slope) uniformity (difference < $\pm 0.5\%$). The maximum and average offsets in the energy measurements are 4.5 LSB (2.25 pC) and 7.7 LSB (3.85 pC), respectively. These quantitative results confirm Pico-PET's excellent accuracy, linearity, repeatability, and uniformity for pulse energy measurement.

D. One-to-One Coupled Detector

Fig. 13(a) shows a typical energy spectrum for ²²Na measured by a Pico-PET channel. The spectrum displays clearly and accurately both the 511-keV annihilation photopeak of the positron emitter and the photopeak of the 1.275-MeV gamma ray. An excellent full-width half-maximum (FWHM) energy resolution (10.5%) was measured on the 511-keV photopeak. Fig. 13(b) shows the distribution of the energy resolutions for all 100 discrete crystals in the 10×10 array. The mean and STD of the energy resolutions were 10.0% and 0.7%, respectively. Fig. 13(c) shows that the normalized position of the 511-keV photopeak varied in the range of 0.87 to 1.11, with an STD of 0.054. These results indicate that the 100-channel Pico-PET system is able to measure the energy spectrum of gamma events with high levels of accuracy, linearity, and uniformity.

E. Crystal Decoding Performance

Fig. 14(a) and (b) shows the crystal decoding map and a profile of the 8×8 array of 1.5 mm \times 1.5 mm \times 5 mm LYSO crystals. All of the 1.5-mm crystals are distinctly separate from each other in the decoding map. The profile analysis shows that all of the peaks have exceptional peak-to-village ratios (PTVRs), being all larger than 20.

Fig. 14(c) and (d) shows the crystal decoding map and a profile of the 30×30 array of 0.445 mm \times 0.445 mm \times 20 mm LYSO crystals. The decoding map covers one-quarter of the discrete crystals of the 30×30 array because only one LG-SiPM (four channels) was connected to the Pico-PET system. All the 0.445-mm crystals, except for some on the edges, are distinctly separate from each other in the decoding map. The profile analysis shows that here too, the peaks have excellent PTVRs (mean =b STD: 3.6 = b 0.51). These results indicate that the Pico-PET system has an excellent decoding ability that matches or outperforms most other readout electronics currently used in the research and industrial community [39], [45], [46].

F. Readout Light Distribution in a Monolithic Scintillator—Fig. 15(a) shows a typical energy spectrum for ²²Na measured with the monolithic LYSO crystal and 100-channel Pico-PET system. A good FWHM energy resolution (12.9%) was measured on the 511-keV photopeak. Fig. 15(b) shows 16 typical maps of the distribution of the scintillating photons generated by single gamma photons interacting with different parts of the monolithic scintillator. These results demonstrate the outstanding capacity of the Pico-PET system to read out 2-D distributions of photons.

IV. Discussion

SiPMs are widely used in light detection and ranging, automated test equipment, highenergy physics experiments, and nuclear medical imaging systems. A fundamental technical obstacle to applying large-scale SiPM arrays is the challenge of reading out a large number of detection channels, from hundreds to thousands or more. Apart from the cost, it is both difficult and not especially practical to use conventional electronics with discrete components to read out hundreds or thousands of channels. The bulky size of the electronics alone would prevent the connection of the individual SiPMs inside a compact array or

packing the detectors closely together. In 2016, a group at Lawrence Berkeley National Laboratory (USA) developed and released a general purpose open-source high-performance electronics system for nuclear imaging, called OpenPET electronics [45]–[47]. OpenPET adapted conventional front-end electronics using a high-performance ADC. A fully installed OpenPET module, consisting of a motherboard, eight analog boards (16 channels per board. 128 channels in total), and some debugging boards, is 30 cm \times 30 cm \times 44 cm in size, costs about U.S. \$30000, and consumes 100 W of power.

In this paper, we have successfully developed a novel 100-channel Pico-PET electronics system based on 1-bit sigma-delta modulation and an FPGA, with off-the-shelf low-cost components. Unlike the 128-channel OpenPET system, the Pico-PET system only requires one single FPGA board and one analog board. Almost all aspects of the front-end readout electronics, including signal splitting, event triggering, ADCs, and TDCs, are implemented inside the low-cost programmable FPGA. Thus, the cost (~U.S. \$800), power consumption (< 3W), and size (6 cm \times 6 cm \times 0.8 cm) of the Pico-PET system are significantly lower or smaller than they are for OpenPET.

Six different experiments were conducted to evaluate the performance of the Pico-PET system and assess its unique features. The experiments relating to dc current measurement confirmed the system's ability to measure 100 channels of dc current accurately, linearly, and uniformly. SiPM *V*-*I* curve measurements revealed its unique capacity to capture the *V*-*I* curves and measure the breakdown voltages and dark currents of 100 SiPMs accurately, uniformly, and simultaneously.

Note that the temperature dependence of the breakdown voltage, dark current, and gain [48] is one of the major downsides of analog SiPM detectors. None of the current ASICs and electronics systems developed for nuclear imaging, outside of Pico-PET, can measure the *V*-*I* curves, breakdown voltages, and dark currents of multiple SiPMs simultaneously. This core feature of the Pico-PET system enables the potential to monitor and compensate for temperature dependence in the breakdown voltage, dark current, and gain of multiple SiPMs simultaneously. More specifically, the gain G(V) of a SiPM is approximately [49]

 $G(V) \approx C_{\text{pix}} \cdot (V - V_{\text{bd}})/q_0$ (5)

where *V* and *V*_{bd} are the bias voltage and the breakdown voltage, respectively. C_{pix} and q_0 are the single pixel capacitance and the elementary charge, respectively. Equation (5) indicates that the gain of a SiPM is approximately proportional to the difference between the bias voltage and the temperature-sensitive breakdown voltage [50]. Thus, the differences of the gains among multiple SiPMs can be normalized conveniently by applying the calibration coefficients $c = 1/(V - V_{bd})$ for the individual SiPMs. The temperature compensation can also be implemented simply by measuring the breakdown voltages and updating the calibration coefficients periodically.

The experiments regarding pulse measurements showed that the Pico-PET system is able to measure the energies of 100 channels of pulse signals with excellent accuracy, linearity, and

uniformity. As expected, accurate energy spectra and outstanding energy resolution were achieved in experiments using a one-to-one coupled detector. These results indicate that Pico-PET is a powerful tool for applications, such as high-energy physics experiments and nuclear imaging system, where the accurate measurement of energy spectra are required.

Generally, there are two types of gamma detectors for nuclear medical imaging. The first uses discrete crystal arrays, where the locations of gamma interactions are decoded using lookup tables generated by segmenting the position decoding map [51], [52]. The second type is constructed with one piece of monolithic crystal. The locations of the gamma interactions here are calculated according to the distribution of scintillating photons measured on the surfaces of the crystal [15], [19], [42]–[44]. The experiments using detectors constructed with very fine discrete crystal arrays (0.445 mm) and the monolithic crystal demonstrated the extraordinary capacity of the Pico-PET system for both discrete crystal decoding and the measurement of light distribution. The experiments also demonstrated Pico-PET's excellent flexibility when reading out SiPMs provided by different manufacturers (SensL's MicroFC30035 and Fondazione Bruno Kessler's LG-SiPM), and when clustering different numbers of channels (1, 4, 16, and 100 for the four detectors shown in Figs. 7, 8(b) and (a), and 9, respectively) for event triggering and energy calculation.

The dead time for processing an ideal short pulse input is determined by the frequency of the sampling clock (f) and dynamic range (DR) requirements of the energy measurement

Dead Time = $2^{DR}/f$. (6)

For example, the dead time is 512 ns when DR = 9 bits and f = 1 GHz. That indicates that the maximum rate tolerance is about 2 MHz per channel in an ideal situation.

The timing performance is an essential performance of the readout electronics for nuclear imaging. In the current version of the Pico-PET electronics, the timing measurements are implemented using digital counters in the FPGA. The intrinsic timing resolutions are determined by the clock frequencies. A 1-ns timing resolution was achieved naturally when a 1-GHz system clock was adapted in the 100-channel 1-bit oversampled delta-sigma ADCs. That meets the requirements of many applications, such as the preclinical PET and clinical non-time-of-flight (TOF) PET.

However, a much better timing resolution is requested for the next generation of TOF PET [53], [54]. A low-cost, high-performance FPGA-based TDC [named nonuniform multiphase (NUMP) TDC] has been developed [32]. Four channels of NUMP TDCs have been implemented and an excellent (2.3 ps) timing resolution was measured with testing pulses. As a next step, we will implement 100 channels of FPGA-based TDCs in the Pico-PET electronics and perform experimental studies to evaluate the timing performances.

The Pico-PET electronics transfers the event data to the host device using a micro USB 2.0 port. The full-speed data combination rate was measured to be about 40 Mb/s. The size of the data package was fixed to 32 bits, while the format and length of the valid event data are

flexible and vary in different applications. For example, 32-bit event data, including 1 flag bit, 11 bits of energy, 12 bits of time stamp, and 8 bits of channel ID, were transferred to the host PC in the experiments with the one-to-one coupled detector. The maximum data transfer rate was about 1.25-M counts per second. In the experiments with a monolithic scintillator, 10032-bit data packages were occupied to transfer the light distribution measured from 100 channels for every valid event. Thus, the maximum data transfer rate was about 12.5-K counts per second.

V. Conclusion

Detector readout for numerous SiPM array channels is a fundamental technical problem that limits the potential for developing advanced nuclear imaging technology. A Pico-PET electronics system based on novel 1-bit Σ - modulation and an FPGA was developed to solve this long-standing problem. The cost, power consumption, and size of the Pico-PET system are about 30 × cheaper, 26 × lower, and 1000× smaller, respectively, than OpenPET, which is a commonly used open-source high-performance system for nuclear medical imaging. The Pico-PET system has some unique, essential, and sought-after features that are not available in any other ASICs and electronics systems for nuclear imaging. These include an ability to measure *V*-*I* curves, breakdown voltages, and dark currents for 100 SiPMs accurately, simultaneously, and in real time. Furthermore, the flexibility afforded by using an FPGA enables multiple channel clustering and intelligent triggering for different detector designs.

Pico-PET will make it much easier for small academic and industrial research groups to come up with new innovative ideas and approaches [55], [56]. This will significantly accelerate and expand the progress of research, technology, and application development in the field of nuclear imaging.

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(a) Pico-PET electronics system, (b) First-order, 1-bit oversampled delta-sigma ADC using an FPGA.





Pictures of (a) custom-designed FPGA board, (b) custom-designed analog board, and (c) assembled 100-channel Pico-PET electronics module.



Fig. 3. Firmware framework of the Pico-PET system.



Fig. 4.

Experimental settings for the dc current measurements.





Experimental settings for the SiPM V-I curve measurements.





Settings for the test pulse experiments.





 10×10 array of 3 mm \times 3 mm \times 20 mm LYSO scintillator crystals for the one-to-one coupled detector experiments.



Fig. 8.

(a) 8×8 array of 1.5 mm \times 1.5 mm \times 5 mm LYSO crystals. (b) 30×30 array of 0.445 mm \times 0.445 mm \times 20 mm LYSO crystals.



Fig. 9.

(a) 30 mm \times 30 mm \times 20 mm monolithic LYSO scintillating crystal, with five surfaces wrapped in Vikuiti ESR. (b) Monolithic LYSO crystal was air coupled to the 10 \times 10 MicroFC30035 SiPM array and mounted in a light-tight encasement manufactured out of black carbon fiber.



Fig. 10.

(a) Plots of the 100 transfer functions of the input and measured dc currents. The dc currents ranged from 1 nA to 3 μ A with a step of 1 nA. (b) Slope distributions for the 100 transfer functions.



Fig. 11.

Results of the SiPM *V-I* curve experiments, (a) Log-linear plot of the *V-I* curves for 600 SiPMs measured with the 100-channel Pico-PET system, (b) Distribution of the measured breakdown voltages of the 600 SiPMs (mean \pm STD: 24.6 V \pm 0.089 V). (c) Linear scale plot of the 600 *V-I* curves. The typical and maximum dark currents provided by the SiPM vendor are marked in the plot with horizontal lines.



Fig. 12.

Results of the test pulse experiments, (a) 200 curves of the input pulse widths versus the energies measured from one typical channel, (b) Variation in the measured energies of the typical channel in 200 repeated tests, (c) Distribution of the STDs of the energy measurements for the 100 channels. The distribution of (d) normalized slope k and (e) offset b of the linear transfer functions of the input pulse widths versus the measured energies. A LSB is equivalent to 0.5-pC electric charge.



Fig. 13.

(a) Typical energy spectrum, (b) Distribution of the energy resolution for the 100 discrete crystals, (c) Distribution of the normalized position of the 511-keV photopeak.



Fig. 14.

(a) Crystal decoding map for the 8×8 array of 1.5 mm \times 1.5 mm \times 5 mm LYSO crystals, (b) Profile of the crystal column marked by the yellow dashed box in decoding map a), (c) Crystal decoding map for the 30 \times 30 array of 0.445 mm \times 0.445 mm \times 20 mm LYSO crystals, (d) Profile of the crystal column marked by the yellow dashed box in decoding map c).



Fig. 15.

(a) Typical energy spectrum, (b) 16 typical maps of light distribution representative of single scintillation events measured by the 100-channel Pico-PET system.