

HHS Public Access

Author manuscript *IEEE Trans Biomed Circuits Syst.* Author manuscript; available in PMC 2020 August 01.

Published in final edited form as:

IEEE Trans Biomed Circuits Syst. 2019 August ; 13(4): 608-618. doi:10.1109/TBCAS.2019.2918761.

A mm-Sized Free-Floating Wirelessly-Powered Implantable Optical Stimulation Device

Yaoyao Jia [Student Member, IEEE],

School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA 30308, USA.

S. Abdollah Mirbozorgi [Member, IEEE],

Department of Electrical and Computer Engineering, University of Alabama at Birmingham, AL 35294, USA.

Byunghun Lee [Member, IEEE],

Department of Electrical and Computer Engineering, Incheon National University, Incheon, South Korea

Wasif Khan [Student Member, IEEE],

Department of Electrical and Computer Engineering, Michigan State University, East Lansing, MI 48824, USA

Fatma Madi,

Department of Physiology, Michigan State University, East Lansing, MI 48824, USA

Omer T. Inan [Senior Member, IEEE],

School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA 30308, USA

Arthur Weber,

Department of Physiology, Michigan State University, East Lansing, MI 48824, USA

Wen Li [Senior Member, IEEE],

Department of Electrical and Computer Engineering, Michigan State University, East Lansing, MI 48824, USA

Maysam Ghovanloo [Fellow, IEEE]

School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA 30308, USA

Abstract

This paper presents a mm-sized, free-floating, wirelessly-powered, implantable optical stimulation (FF-WIOS) device for untethered optogenetic neuromodulation. A resonator-based 3-coil inductive link creates a homogeneous magnetic field that continuously delivers sufficient power (>2.7 mW) at an optimal carrier frequency of 60 MHz to the FF-WIOS in the near field without surpassing the specific absorption rate (SAR) limit, regardless of the position of the FF-WIOS in a

large brain area. Forward data telemetry carries stimulation parameters by on-off-keying (OOK) the power carrier at a data rate of 50 kbps to selectively activate a 4×4 µLED array. Load-shift-keying (LSK) back telemetry controls the wireless power transmission (WPT) by reporting the FF-WIOS received power level in a closed-loop power control (CLPC) mechanism. LEDs typically require high instantaneous power to emit sufficient light for optical stimulation. Thus, a switched-capacitor based stimulation (SCS) architecture is used as an energy storage buffer with one off-chip capacitor to receive charge directly from the inductive link and deliver it to the selected µLED at the onset of stimulation. The FF-WIOS system-on-a-chip (SoC) prototype, fabricated in a 0.35-µm standard CMOS process, charges a 10 µF capacitor up to 5 V with 37% efficiency and passes instantaneous current spikes up to 10 mA in the selected µLED, creating a bright exponentially decaying flash with minimal wasted power. An *in vivo* experiment was conducted to verify the efficacy of the FF-WIOS by observing light-evoked local field potentials (LFP) and immunostained tissue response from the primary visual cortex (V1) of two anesthetized rats.

Keywords

Optogenetic switched-capacitor based stimulation; mm-sized; free-floating; implantable; inductive link; μ LED array

I. Introduction

OPTOGENETICS is becoming popular as an important technique in neuro-science. This technique activates or inhibits genetically-modified neurons that express light-sensitive opsin proteins, using light at certain wavelengths [1]-[4]. In comparison with traditional electrical stimulation, optical stimulation has several advantages, which include cell-type specificity, millisecond temporal precision, and rapid reversibility [1]-[4]. Therefore, optical stimulation has been widely utilized for brain research, particularly brain mapping, and expected to find clinical applications in various brain disorders that do not respond to medication, such as Parkinson's disease, epilepsy, psychological disorders, and even used in other organs, such as cardiac electrophysiology [4].

Laser is commonly used in optogenetic experiments, but it requires optical fiber or waveguide connectivity to deliver light from an external source to the target tissue [5]-[8]. The tethering effect, however, may bias the natural behavior of the animal subjects. It is not feasible in animal studies that involve multiple subjects either or those that are conducted in specific environments, such as tunnels [4]. LEDs have much smaller size, and can be directly integrated in the optical devices that are small enough to be carried by the animal subjects [9]-[15]. LED-based devices, whether in the form of detachable headstage or implant, eliminate the tethering effect of a stationary light source, facilitating *in vivo* experiments on freely behaving subjects [10]-[15]. The implantable module, under control of the external headstage, is implemented as a probe with integrated LEDs, penetrating deep into the brain or placed on the brain surface [10]-[13]. In some cases, optical fibers are used to guide the light from LEDs embedded in the headstage to the target brain area [14], [15]. However, the transcutaneous connection between the headstage and implantable module could cause infection and damage to the surrounding soft tissue, which may not be a major

concern in animal studies but a key concern in translation towards a clinical solution [16]. To prevent physical trauma of the transcutaneous connection, miniature implants are expected to minimize tissue damage by reducing foreign body reaction [16].

Recently, a few wirelessly-powered optogenetics approaches have been reported, demonstrating significant reduction in the implant size [17]-[21]. In [17], [18] the proposed optical stimulators, equipped with the energy-harvesting unit, either a coil [17] or a stretchable antenna on board [18], are wirelessly powered in the GHz band. The specific absorption rate (SAR) of electromagnetic (EM) field in the tissue, which mainly consists of water, at high frequencies is rather high [22]. Moreover, operation in these bands produces considerable radiation, which results in interference with other laboratory instruments or wireless devices in the environment [23].

To circumvent the challenges associated with wireless operation in GHz bands, the carrier frequency in [19] is limited to 13.56 MHz. However, in this case, the receiver (Rx) coil with a diameter of 9.8 mm becomes the main limiting factor in the device miniaturization. A photovoltaic power transfer strategy is proposed in [20] to wirelessly power the implant using infrared (IR) light. In this case, the overall power transfer efficiency (PTE) is quite low, and additional post-processing steps, needed in microfabrication of the silicon die and separation of the photovoltaic cells, could reduce the yield. In [21]. an ultrasonically-powered mm-sized implant enables both optical and electrical stimulations, at the cost of increasing size and weight of the external transmitter, which is too large to be carried by a behaving animal subject. Moreover, even though ultrasonic power transmission is immune to EM interference and offers good PTE in deeper tissues, it is quite sensitive to transducer misalignments and cannot penetrate bone/skull.

In this paper, we are demonstrating a mm-sized, free-floating, wirelessly-powered, implantable optical stimulation (FF-WIOS) device, building upon our preliminary results in [24]-[26], to address the abovementioned limitations. System functionality has been validated in vivo on anesthetized rats by observing light-evoked local field potentials (LFPs) and immunostained tissue response. The miniaturized version of the FF-WIOS device is built on polyimide substrate, which is verified *in vitro*. It is an important step towards the free-floating distributed neural interface concept. The main novelties can be summarized as 1) ensuring sufficient and constant power delivered to the load (PDL) at high PTE with the closed-loop power control (CLPC) mechanism, while staying well below the SAR limit, 2) assembling the FF-WIOS device with compact size and light weight, 3) designing the FF-WIOS system-on-a-chip (SoC) with high-level integration of front-end circuits and wireless power/data transmission related circuits for 16-ch wireless optical stimulation, 4) allowing end-users to configure the optical stimulation pattern, e.g. current level, frequency, and pulse width, and selectively turn on/off a μ LED of a 4×4 μ LED array. An overview of the system is given in Section II. Section III presents the detailed design of the FFWIOS SoC. Section IV describes the 3-coil inductive link specifically designed for this particular application. Section V discusses the benchtop measurement, in vitro, and in vivo results, followed by concluding remarks.

II. System Architecture

The conceptual view of the system that wirelessly powers and controls the mm-sized FF-WIOS is shown in Fig. 1. The FF-WIOS, which includes a SoC, surface-mount device (SMD) capacitors (0201), Rx coil, and µLEDs, all mechanically supported on a 100 µmthick polyimide substrate, and hermetically sealed with Parylene-C and polydimethylsiloxane (PDMS) for acute testing, is designed to be implanted on the surface of the subject's brain. The wire-wound Rx coil, L_{Rx} , which encompasses the SoC, forms a 3-coil inductive link with a transmitter (Tx) coil, L_{Tx} , which is part of the external headstage, and a passive high quality (Q)-factor resonator, L_{Res} , which encompasses one or more FF-WIOS implants roughly in the same plane. The headstage includes a class-E power amplifier (PA), controlled by a microcontroller (MCU) (CC2541, Texas Instruments), which delivers power from battery to the FF-WIOS through the 3-coil link at 60 MHz. To wirelessly control the FF-WIOS, stimulation parameters, which are set in a graphical user interface (GUI) running on a nearby PC, are sent to the headstage MCU via Bluetooth Low Energy (BLE), and then relayed to the FF-WIOS through the 3-coil link by on-off-keying (OOK) the power carrier signal. The rectified voltage on the FF-WIOS is also digitized, as a measure of the received power on the SoC, and sent back to the headstage via load-shiftkeying (LSK) to close the power control loop, and eventually send to the PC via BLE for real-time display.

Fig. 2 depicts the steps for microassembly of the FF-WIOS. In the current prototype, 18 µm of copper is patterned on both sides of the polyimide substrate with the diameter of 2.5×2.5 mm² to form interconnections between the wire-bonded SoC die, µLED pads, and SMD caps. The µLEDs ($220 \times 270 \times 50$ µm³, TR2227TM, Cree) were mounted on their pads using low melting point solder (144 Alloy Field's Metal) and then encapsulated using Parylene-C [9], [10]. The µLEDs are separated by 700 µm to ensure enough illumination field distinction. The FF-WIOS SoC was fixated in the center of the substrate for wire bonding, following which it was protected with ultraviolet-cured medical grade epoxy, while capacitors were mounted on the periphery of the SoC die using silver conductive epoxy (MG Chemicals 8331). The wire-wound Rx coil was then mounted around the SoC die and its terminals were connected to one of the SMD capacitors by silver conductive epoxy to form the *L_{Rx}C_{Rx}*-tank, resonating at the power carrier frequency. Finally, the FF-WIOS device is sealed with Parylene-C and PDMS.

III. FF-WIOS SoC Design

The overall block diagram of the FF-WIOS SoC is shown in Fig. 3. Major challenges in the design of FF-WIOS circuitry include small input power from L_{Rx} due to its weak couplings with L_{Tx} and L_{Res} and safety limit on the SAR [22]. On the other hand, instantaneous output power to µLEDs needs to be large enough for the resulting light intensity to surpass the optogenetic stimulation threshold [27]. High efficiency should also be maintained at every step from the PA to the µLED array to minimize heat generation. To address the issues, we adopted the wireless switched-capacitor based stimulation (SCS) circuit in [28]. and modified it for this particular application.

A built-in charger of a voltage double periodically charges a storage capacitor, C_{LED} from the inductive link. A charge control unit sets the target charging voltage at 5 V. During stimulation, C_{LED} is detached from the charging cell and delivers its stored charge to the selected µLED through positive and negative terminal selector multiplexers without loading the inductive link. A current limiter is added to limit the maximum current flowing through the target μ LED. To control the timing of C_{LED} charging/discharging, a timing control block is designed based on the Schmitt trigger in [29] and utilized for generating reference clocks, CLKs, and a stimulation enable signal, Stim, which pulse width and frequency are adjustable [25]. In the power management, the voltage doubler, following $L_{Rx}C_{Rx}$ -tank, generates a DC voltage, V_{DBR}. A bandgap reference generator uses a classic topology to generate V_{BGR} [29], which is used by a cap-less low dropout regulator (LDO) to generate the supply voltage, V_{DD} . The bias generator block also uses the same V_{BGR} to generate other reference voltages and currents. ADC level shifter shifts the high level voltage of Stim from V_{DD} to V_{DBR} , resulting in a control signal, $Stim_H$, for enabling/disabling the built-in charger of the voltage doubler. In the forward data telemetry, a pulse-position-modulated clock/data recovery (PPM-CDR) circuit recovers synchronized clock/data from the OOK modulated coil voltage, V_{COIL}, setting a 12-bit shift register through a serial-to-parallel (S2P) converter with 10-bit pre/post-amble data [25], [28]. LSK back telemetry is adopted for CLPC of the FF-WIOS by sensing V_{DBR} [28], [30].

The 60 MHz power carrier induces a V_{COIL} across the $L_{Rx}C_{Rx}$ -tank, which is rectified and regulated by the voltage doubler and cap-less low dropout regulator (LDO) blocks, respectively, as shown in Fig. 4a. The built-in charger is controlled by the *Stim* input, which turns P_4 on and N_2 off when it is lowered. During stimulation, Stim = `1`, the charger is disabled to avoid V_{DD} drop, while C_{LED} is connected to the stimulation sites as shown in Fig. 4e. We adopt dynamic body biasing with two pairs of auxiliary transistors, automatically connecting the body voltage of P_1 and P_5 to the highest potential [28]. To reduce the number of off-chip components, a cap-less LDO is utilized to generate a stabilized $V_{DD} = 1.8$ V for the rest of SoC.

Fig. 4b shows the schematic diagram of the OOK-based forward data telemetry block, adopted from [28]. In the OOK demodulator, V_{COIL} is low-pass filtered by an envelope detector, following which OOK pulses are recovered by a hysteresis comparator, A_I , when compared with V_{REF2} , to provide the PPM signal, S_{PPM} . In the PPM-CDR block, S_{PPM} is converted to clock, CLK, using a frequency divider. CLK controls the charging and discharging of C_3 , which generates a triangular waveform, V_{PPM} . If positioning ratio among three consecutive S_{PPM} pulses is 4:1, VPPM exceeds V_{REF3} during CLK = `1`, leading to DATA = `1`. Otherwise, DATA = `0`, if the positioning ratio is 1:4. In the S2P, the recovered data, DATA, is shifted by CLK into a buffer. Once the pre/post-amble data bits ($D_1 \sim D_5$, $D_{18} \sim D_{22}$) are matched with a pre-defined 10-bit value, a flag, *StimEN*, will be raised and then the data bits ($D_6 \sim D_{17}$) will be saved in registers to set the stimulation parameters.

LSK back telemetry is adopted for CLPC of the FF-WIOS. This is a key mechanism for practical implementation in the face of headstage (basically L_{Tx}) and brain motion artifacts, as well as uneven brain surface morphology, e.g. gyri and sulci, in larger species. Considering requirements for the CLPC stability in [31]-[34], we have chosen a 160 Hz

clock, CLK_{LSK} , to control the timing of back telemetry pulse, BT, which results in a BT data rate of 160 bps. In Fig. 4c, the pulse width of $BT(1 \ \mu s \text{ or } 2 \ \mu s)$ is decided by the number of delay cells that are engaged in generating this pulse. The resistive divider, R_4 and R_5 , sets the maximum V_{DBR} at 4.2 V. When the divided V_{DBR} exceeds the bandgap reference voltage, V_{BGR} , BT pulses are generated to short L_{Rx} by closing P_{14} switch, resulting in increasing L_{Rx} Q-factor, as well as the voltage across and current through L_{Tx} .

Fig. 4d shows the schematic diagram of the clock generator for the timing of charging and stimulation. A control signal, *PL*, at the output of a hysteresis comparator, A_4 , controls the timing and amplitude of V_{C4} by charging C_4 in the phase of PL = '0'. Once V_{C4} reaches V_{REF4} , *PL* is set to '1 ' to discharge C_4 in a short period, generating a single narrow *PL*, which is converted to a reference clock, CLK_{REF} , through a frequency divider. In Fig. 4e, a μ LED is selected from the 4×4 μ LED array by specifying the positive/negative terminals of the μ LED through a pair of 4:1 multiplexers. The current limiter, consisting of a 3-bit programmable current sink with binary-weighted transistors, can adjust the maximum current limit and the light output of the activated μ LED. The current sink is controlled by 3 pairs of digital control signals, *CL0-CL2* and $\overline{CL0}$ - $\overline{CL2}$.

IV. 3-coil Inductive Link Design and Optimization

A 3-coil inductive link model with surrounding tissue layers of a rodent model was constructed in HFSS (ANSYS, Cecil Township, PA) for the coil optimization. In Fig. 5, L_{TX} is placed above the head, while L_{Res} and L_{RX} are implanted above and under the skull, respectively. Using a high Q-factor L_{Res} can significantly improve and homogenize the EM field over the area encompassed by L_{Res} . To complete the HFSS model, L_{Res} and L_{RX} are coated with 50 µm PDMS and 5 µm Parylene-C for bio-compatibility. A simplified equivalent circuit model of the inductive link is shown in Fig. 5 as well.

The power conversion efficiency (PCE) of passive AC-DC converters, such as rectifiers and voltage doublers, which use diode-connected transistors, degrades at higher frequencies due to the parasitic capacitance [35], [36]. Considering the effects of surrounding tissue and PCE of the voltage doubler that loads L_{Rx} [37]-[40], Fig. 6 shows an algorithm, which generates the optimized coil specifications and operating frequency that would maximize PTE×PCE. Because PTE is obtained by taking into account the loading effect of the voltage doubler, which is reflected through the PCE, it is fine to simply multiply PTE and PCE at each frequency. Other considerations are the application and fabrication constraints, which are the input parameters to the algorithm, including the distance between L_{Tx} and L_{Res} , D, L_{Rx} diameter, d_{Rx} , d_{Rx} , L_{Res} diameter, d_{Res} , and PDL. The equivalent load, R_L , was set to 4.6 k Ω based on a maximum PDL of 2.7 mW at 5 V_{peak} across L_{Rx} , which is imposed by the process.

Design procedure starts with L_{Rx} optimization, which effective area is maximized to increase the magnetic flux passing through it. L_{Rx} is wire wound around the FF-WIOS die to achieve the highest Q-factor without considerably increasing the diameter of the FF-WIOS. AWG 34 magnet wire is chosen for this purpose, resulting in d_{Rx} of 1.6 mm and wire thickness, w_{Rx} , of 0.16 mm. The number of turns, n_{Rx} , is then fine-tuned for maximizing the

receiver power reception susceptibility, Rx-PRS, which indicates the efficiency of L_{Rx} power reception under a given magnetic field exposure [37], [38]. To determine n_{Rx} , we need to strike a balance between Rx-PRS and geometrical parameters of L_{Tx} and L_{Res} as a function of frequency to maximize PTE. Therefore, n_{Rx} is chosen after determining the carrier frequency of the 3-coil inductive link

 L_{Res} is designed based on the size of the target cortical area. In our application, we expect to observe light-evoked neural activities from the primary visual cortex (V1) of a rat following the FF-WIOS optical stimulation. According to [41], approximately 0.5-5 mm lateral of skull midline overlays V1 in one side lobe. As a result, d_{Res} , should be larger than 10 mm to encompass the V1 areas in both left and right lobes. With a certain margin of error, L_{Res} is implemented with $d_{Res} = 11.6$ mm. The wire thickness, w_{Res} , and the number of turns, n_{Res} , were selected considering the limited space on the rat skull for L_{Res} , its Q-factor, and coupling coefficient between L_{Res} and L_{Rx} . The result was a single-turn coil made of 0.4 mm (AWG 26) magnet wire.

The distance between L_{Tx} and L_{Res} , D is determined by the thickness of various tissue layers, depending on the anatomical position of the FF-WIOS. For the rodent model, we considered the thickness of skin and fat to be 5 mm at most. The optimal size of L_{Tx} is directly related to D and is calculated for maximum coupling between L_{Tx} and L_{Res} using equation (4) in [38]. L_{Tx} is made of 0.8 mm (AWG 20) magnet wire for high Q-factor. It is relatively easier to fine-tune L_{Tx} parameters compared to those of L_{Rx} and L_{Res} . To find the best the number of turns, n_{Tx} , PTE of the 3-coil link is simulated from 10 MHz to 160 MHz for 1-and 2-turn L_{Tx} in Fig. 7. The PTE with 2-turn L_{Tx} is lower across the entire frequency range, and its peak occurs at lower frequency. As a result, L_{Tx} is designed as a single-turn coil.

The PCE of the voltage doubler is yet another key factor in determining the overall efficiency of the power delivery path. In Fig. 7, the PCE of the voltage doubler drops sharply as the frequency increases because of parasitic capacitors of the diode-connected transistors. The best operating frequency was decided based on the peak value of PTE×PCE. The results of PTE×PCE with both 1-turn and 2-turn L_{Tx} are divided by the peak value of PTE×PCE for normalization. The peak value of the normalized PTE×PCE appears at 60 MHz with the 1-turn L_{Tx} . Thus, 60 MHz is selected as the carrier frequency of the 3-coil inductive link, with $n_{Rx} = 6$.

Fig. 8a shows the directional power flux density, i.e. the rate of power transfer per unit area, using Poynting vectors. The power density varies from 1 to 5×10^3 W/m² when the input power level is set at 12.86 mW to deliver a target PDL of ~2.7 mW to the FF-WIOS. The threshold marked on the vertical column indicates the required power flux density to deliver PDL 2.7 mW. The area where L_{Rx} is to be located is above the threshold, indicating sufficient received power. L_{Res} and L_{Rx} are concentrically aligned with L_{Tx} in this model. In terms of PTE vs. horizontal misalignment, this arrangement is considered the worst-case scenario for L_{Rx} , as demonstrated in [38]. Since the 3-coil inductive link provides sufficient PDL in the worst-case scenario, and the Poynting vector in Fig. 8a shows higher EM power density close to the perimeter of L_{Res} , the entire area within L_{Res} is indeed covered with

sufficient PDL. Concerning the exposure to EM field and heat generation in the tissue, Fig. 8b presents the HFSS simulation of the local SAR for different tissue layers under the same power source setting. Simulation results show that the maximum local SAR is ~2.78 W/kg, which is well below the safety limit of 20 W/kg [22].

V. Experimental results

A. 3-Coil Inductive Link Implementation

A 3-coil inductive link was implemented using the optimized geometries from Section IV. The PTE of the inductive link and the PCE of the voltage doubler were measured, respectively, and compared with simulations in Fig. 9, as a function of frequency. The PTE×PCE was also calculated, normalized, and added in Fig. 9. Measurements have good agreement with simulation results. The lower PTE in measurements can be attributed to the tissue effect on L_{Res} . Its high Q-factor is affected by the large contact area with the surrounding tissue and makes it sensitive to detuning. The difference between the measured and simulated PCEs could be due to process variation and parasitic effects of the measurement instruments. The parasitic inductance and capacitance from the probes cause distortion in the measured waveforms at higher frequencies, resulting in further reduction in the measured PCE. The key point here is that the optimal carrier frequency to achieve maximum PTE×PCE is still at 60 MHz, as expected from simulations and design target. After fine tuning the optimal inductive link parameters *in vitro* using fresh tissue from sheep head, they were determined and summarized in Table I.

B. Bench-top Characterization

The FF-WIOS SoC was fabricated in the TSMC 0.35- μ m 4M2P standard CMOS process, occupying a 1 × 1 mm² footprint including pads, as shown in Fig. 10a. With additional test pins, the die area is 1.1×1 mm². Fig. 10b shows a prototype headstage, which consists of two stacked PCBs, together with L_{Tx} , fitting in a 15×15×15 mm³ cube. The headstage, made of commercial off-the-shelf (COTS) components, not only drives L_{Tx} by its PA but also operates the CLPC [42]. The envelop detector block in the headstage, which has been explained in [42], is used to recover the *BT* pulses. The resulting signal is then detected by the headstage MCU. Similar to [31], [32], the headstage decreases the PA supply voltage, V_{PA} , when it detects the *BT* pulses. Otherwise, V_{PA} is continually increased by default at an adjustable rate. In steady state, V_{PA} bounces within a range to stabilize V_{DBR} . The CC2541 MCU applies an upper bound to V_{PA} to avoid any sudden increase.

Fig. 11a shows the measurement results of the forward data telemetry. V_{COIL} is OOKdemodulated to generate S_{PPM} , which is converted to synchronized 50 kbps *CLK* and *DATA* by PPM-CDR. In the close-up view, S_{PPM} with pulse position ratio of 4:1 generates *DATA* = '1'. On the contrary, when the positioning ratio is 1:4, *DATA* = 'O'.

Fig. 11b shows the CLPC operation when the headstage is moved manually from D = 10 mm to 5 mm and then back to 10 mm. As the headstage gets closer to the FF-WIOS, *BT* pulses are generated when V_{DBR} is larger than a certain threshold, indicating that there is more than enough power available to the FF-WIOS. In response, voltage across L_{Tx}

increases, and the CLPC starts reducing V_{PA} to compensate for this perturbation. V_{PA} decreases by 1 V in 8 steps, resulting in the step size of 0.125 V. It takes ~60 ms for V_{DBR} to return back to 4.2 V. As the headstage moves back to its original location, V_{PA} starts to increase in the absence of *BT* pulses. During the short switching period of L_{Rx} , the FF-WIOS SoC is powered by the stored charge in C_L , and V_{DD} remains stable at 1.8 V.

Fig. 11c shows the charging and discharging of the 10 μ F C_{LED} to generate optical stimulation with 2 ms pulse width at 10 Hz. The μ LED current. I_{LED} is limited to 10 mA. Once stimulation starts, C_{LED} discharges in the target μ LED with a decaying exponential current. V_{DBR} shows a slight drop (0.45 V), which is much less than V_{CLED} (voltage across C_{LED}), and remains above the minimum level (2.6 V) required for V_{DD} not to be affected by its variations. The emitted light from μ LED (0.5 × 1×0.4 mm³, LB QH9G, OSRAM) during stimulation pulse was collected by the photodetector (Newport 883-SL) of an optical power meter (Newport 1835-C). The normalized output light (NOL) expectedly follows the stimulation current variation but with a slight delay. After each stimulation, C_{LED} is recharged back to the target voltage within 30 ms.

Following startup, as shown in Fig. 12a, it takes ~50 ms for V_{DBR} and V_{CLED} to stabilize at their steady-state target voltages. Before this, V_{DD} and V_{BGR} have already been stabilized within 20 µs at 1.8 V and 1.2 V, respectively. The µLED current under 4 different settings was measured from the voltage across a 10 Ω current-sensing resistor in series with the µLED. In Fig. 12b, I_{LED} increases from 2.5 mA to 10 mA in a 2.5 mA step according to the design specifications. The µLED output light during a stimulation pulse is also measured at each current level with 2 ms pulse width at 10 Hz, as shown in Fig. 12c. To validate measurement results with the µLED datasheet, we normalized the output light. The output light at each µLED current is divided by the peak value of the light intensity (4.8 mW/mm²) under 5 mA current for normalization. Fig. 12d shows that normalized output light intensities under different currents match with specifications of the µLED [43]. A summary of the FF-WIOS SoC characterizations is presented in Table II.

C. In Vivo Experiment Design

In vivo experiments were conducted to verify the efficacy of the FF-WIOS to optically evoke neural activities in the V1 of anesthetized rats. One male and one female adult rat (Sprague Dawley, 600-650 g) were tested based on our established protocols approved by the Institutional Animal Care and Use Committee (IACUC) at Michigan State University [10]. Using the stereotaxic surgery protocol in [10], adeno-associated virus (AAV) that carries optogenetics opsin (AAV-hSyn-hChR2 (H134R)-mCherry; UNC Vector Core) was injected bilaterally into the rat's V1. Post injection, the rats were housed in the animal facilities for 4 weeks till the V1 neurons express channelrhodopsin-2 (ChR2) [10]. Then the animals were subjected to *in vivo* experiments.

A modified FF-WIOS system was used in these experiments to facilitate the acute animal studies. The FF-WIOS board (14×8 mm²), shown in Fig. 13, consisted of an FF-WIOS SoC bonded on the top side, a 2 ×2 µLED array (0.5 × 1 ×0.4 mm³, LB QH9G, OSRAM) assembled on the bottom side, and L_{Rx} soldered at the back-end. A class-E PA delivered power wirelessly to the FF-WIOS board through the optimized 3-coil inductive link at 60

MHz. The FF-WIOS board was placed over the skull of the rats with two μ LEDs aligned over each side of V1 lobe. More specifically, μ LED₁ and μ LED₂ were above the left V1, while μ LED₃ and μ LED₄ were above the right V1.

Under anesthesia, unilateral optical stimulation was performed on the left V1 by selectively driving μ LED₁ with user-defined parameters, while LFPs were simultaneously recorded through a tungsten electrode, which penetrated into the left V1 cortical layers. The LFP recordings were amplified and digitalized through a commercial 32-ch Intan system (RHD2132), and then uploaded to a PC for data analysis using a MATLAB Chronux toolbox.

Fig. 14 shows LFPs recorded at the depths of 100 μ m, 500 um, and 1 mm, following light stimulation directed with a pulse train of 2 ms pulse width, 2.5 Hz frequency, and 10 mA stimulation current. Clear light-evoked LFPs were observed at the ~100 μ m depth, in response to the optical stimulation, whereas the LFP variation decreases as the recording depth increases. This result reveals that the optical stimulation applied by the FF-WIOS can evoke the neurons in superficial layers.

At the effective stimulation depth of 100 μ m, LFPs were measured when the μ LED light intensity is above and below the threshold of 1 mW/mm² [27]. The stimulation pulses have 2 ms pulse width at 2.5 Hz with 10 mA and 2.5 mA current limits, corresponding to the light intensity of ~10 mW/mm² and ~0.95 mW/imn², respectively. Spontaneous LFPs were also recorded as a baseline when μ LEDs were completely off. We expected to observe light-evoked LFPs, i.e. synchronized with above-threshold stimulation, being distinguishable from the LFPs uncorrelated with below-threshold stimulation.

LPF recordings recorded at different light intensity are compared in Fig. 15a over a time span of 50 s. The stimulation flags in the close-up view of 1 s long LFP recordings indicate the occurrence of a stimulation pulse. Photoelectric artifacts induced from light stimulation came along with the LFPs [3], [4]. The recorded LFPs with below-threshold stimulation is not evoked as compared to the spontaneous LFPs, while with 10 mA stimulation current, the recorded LFPs show significantly larger variations, suggesting that the above-threshold stimulation can effectively evoke neural activity.

The Hilbert transformation was applied to extract the instantaneous phases of 200 trials of LFP recordings within a frequency range of 1-25 Hz [10], [44]. In Fig. 15b, colors indicate the instantaneous phase of each trial. The 200 trials are aligned to the concurrence of the stimulus and stacked. The Y axis of the Hilbert transformation in Fig. 15b is the number of trials. Since each trial lasts 0.4 s, the X axis of the Hilbert transformation indicates the time duration of the stimulation trials. The instantaneous phase of individual trial is color coded, aligned to the concurrence of the stimulus, and stacked as shown in Fig. 15b. Expectedly, the spontaneous LFPs with random phases did not show phase synchrony. Very short phase synchrony was observed at the 2.5 mA stimulation current, which could be the effect of photoelectric artifacts. In contrast, strong and reliable phase-locked synchronization was observed across 200 trials within a time window of ~100 ms following the optical stimulation at 10 mA current (p < 0.05, Wilcoxon signed rank test).

Furthermore, 200 trial LFPs were averaged and mapped onto a time-frequency graph of color-coded, normalized power spectral density (PSD) distribution [10], [45]. Fig. 15c shows the PSD results in a 1-300 Hz frequency range, where a significant increase in PSD was observed in a short time window of ~100 ms following the optical stimulation of 10 mA. In contrast, stimuli at 2.5 mA only caused a slight increase in PSD as compared to the PSD of the spontaneous LFPs, which can be mainly attributed to the photoelectric artifacts.

In addition to LFPs, immunochemical analysis was performed to identify the increased expression of c-Fos as an indirect measure of light-evoked neuronal activity induced by optical stimulation [10], [46]. In this experiment, the left V1 lobes of both rat #1 and #2 were stimulated optically for 45 mins, with 2 ms pulse width, 2.5 Hz pulse rate, and 10 mA stimulation current, while the right V1 was untouched as a control. Fig. 16 shows the fluorescent microscope images of the post-processed brain tissue with a thickness of 50 µm. Green fluorescence spots indicate cells expressing c-Fos, while orange spots are m-cherry stained cells that are expressing optogenetic opsins (ChR2). The tissue analysis results show a significant increase in the c-Fos expression under 10 mA stimulation, implying elevated neural activity. In contrast, only a slight increase in the c-Fos expression was observed at the 2.5 mA current, most likely representative of background activity since both of the rats' eyes were open during testing [10]. Moreover, the overlapping of the cells expressing both m-cherry and c-Fos reveals that the same transfected cells express increased activities induced by the above-threshold optical stimulation.

D. Miniature FF-WIOS Prototype and In Vitro Experiment

Fig. 17 shows the *in vitro* setup using tissue layers in a cube cut out of a sheep head, including brain, skull, fat, and skin for preliminary evaluation of the system operation. The headstage, powered by a 100 mAh rechargeable LiPo battery, weights 4.2 g. The assembled FF-WIOS and L_{Res} are encapsulated with 5 µm Parylene-C and ~50 µm PDMS before implantation. The FF-WIOS implant, with dimensions and weight of $2.5 \times 2.5 \times 1.5$ mm³ and 15 mg, respectively, is placed on the surface of the brain, while L_{Res} is placed above the skull but under the scalp, well aligned with and 2 mm away from the FF-WIOS implant. Between L_{Tx} and L_{Res} is the skin and fat layers of scalp with a total thickness of 5 mm. The headstage is placed above the skin and concentrically aligned with L_{RES} . In this setup, the headstage delivered sufficient power to operate the FF-WIOS by driving a selected µLED (blue color), while the MCU established BLE link with PC and OOK modulated the power carrier, resulting in 97.6 mW drawn from the 3.7 V battery.

VI. Conclusion and Future Steps

We have presented a wirelessly-powered free-floating implantable optical stimulation device. With its compact size and light weight, the FF-WIOS is expected to minimize tissue damage and therefore enable efficient chronic wireless optical stimulation. Thanks to the 3- coil inductive link design, the FF-WIOS is efficiently powered, while staying well below the SAR limit. Wireless data transmission between the headstage and FF-WIOS is established with OOK modulation of the power carrier. The CLPC mechanism is also utilized to ensure stable power delivery to the FF-WIOS. The SCS architecture implemented in the FF-WIOS

SoC provides high instantaneous current for effective optical stimulation without putting too much burden on the inductive link. The circuit topology for each block is chosen for power/ area efficiency and design simplicity to achieve a compact and reliable SoC design. It is possible to further reduce power consumption using ultra-low power circuit designs, e.g. the relaxation oscillator design in [47]. We have verified the functionality of the entire system *in vitro* and *in vivo* on sheep head and rat models, respectively. We targeted the left V1 of two anesthetized rats with wireless optical stimulation, while observing light-evoked LFPs and immunostained tissue responses. Table III benchmarks the FF-WIOS against state-of-art optical implants in the literature. The FF-WIOS is competitive in terms of implant size, weight, and stimulation capability. The near-field 3-coil link combined with SCS charge storage is a safe and efficient strategy that can offer sufficient PDL for optical stimulation. Moreover, CLPC can handle misalignments much better than ultrasound and focused EM field at high frequencies. The FF-WIOS also benefits from high-level integration of 16-ch stimulation and provides the end user with full control over the stimulation parameters.

The FF-WIOS prototype used for *in vitro* experiments is the miniaturized version of the FF-WIOS prototype used *in vivo*. By the miniaturized version of FF-WIOS, we are referring to the prototype built on polyimide substrate. We are now working towards testing this miniaturized version *in vivo*, first on an anesthetized rats and then on freely behaving subjects within the EnerCage-HC environment [33], [34]. Since the miniaturized version has smaller Cree μ LEDs assembled, the resolution of optical stimulation applied using this prototype can be improved. It is also possible to couple a fiber onto the μ LED to further focalize LED light onto a specific brain area at the cost of being more invasive [48]. This miniaturized version of the FF-WIOS is built on polyimide substrate, which is flexible and biocompatible. It is an important step towards the free-floating distributed neural interface concept.

From the neuroscience perspective of alignment with a particular target in the brain, a combination of multiple FF-WIOS distributed within the region of interest, and each FF-WIOS being capable of driving a 4×4 µLED array with 700 µm pitch, give the neuroscientist sufficient flexibility, redundancy, and leeway to try multiple adjacent channels or nearby devices to find the optimal target. In rodents, surgery procedure involves creating small holes in the skull that match the size of FF-WIOS at the target locations. After placing the device, the hole will be sealed by dental cement. This approach would preserve key feature of the FF-WIOS to be free-floating with no anchor onto the skull or brain tissues, and minimize damage to the surrounding tissue. Due to small intracranial space and thinness of the skull bone, L_{Res} will be placed above the skull but under the scalp to power the FF-WIOS devices underneath through the bone. L_{Res} is stationary and covers the target brain area. Therefore, no accurate alignment is needed when one or more FF-WIOS are to be located within L_{Res} , and as we have demonstrated before [38], every one of them can receive sufficient power regardless of their position.

Acknowledgments

This work is supported in part by NSF awards ECCS-1407880 and ECCS-1408318, and NIH award 1R21EB018561.

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Fig. 1.

A simplified conceptual representation of the free-floating, wirelessly-powered, implantable optical stimulation (FF-WIOS) device being wirelessly powered and controlled by a battery-powered headstage, which is in turn controlled via Bluetooth Low Energy (BLE) by a PC running the GUI.



Fig. 2.

The fabrication and micro-assembly process of the FF-WIOS device.

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Fig. 3. A simplified system architecture of the FF-WIOS SoC.



Fig. 4.

Schematic diagrams of the (a) voltage doubler with built-in charger and the cap-less LDO, (b) forward data telemetry with OOK modulation, (c) LSK back telemetry, (d) clock generator for the timing of stimulation and charging, (e) current limiter and stimulation output stage.







Fig. 6.

The flowchart of algorithm for the 3-coil inductive link optimization.



Fig. 7.

HFSS simulation results for PTE of the 3-coil inductive link vs. power carrier frequency and n_{Tx} , simulated PCE of the voltage doubler as a function of frequency, and normalized PTE×PCE vs. frequency and n_{Tx} .







Fig. 9.

PTE of the 3-coil inductive link and PCE of the voltage doubler in both simulation and measurement as a function of frequency, and normalized PTE×PCE based on the measurement results.



Fig. 10.

(a) The micrograph of the FF-WIOS SoC and (b) implementation of the prototype headstage with the Tx coil.





Measured results of (a) forward data telemetry, (b) back telemetry for CLPC, and (c) charging and stimulation.

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(a) Starting up transients of the power management block, (b) measured μ LED current at 4 stimulation current settings, and normalized output light as a function of (c) time and (d) μ LED current.



Fig. 13.

In vivo experimental setup with its block diagram and anatomical location of the FF-WIOS board, electrode, and μ LEDs on the rat brain.



Fig. 14. LFP recordings at different depths through the tungsten electrode.

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Fig. 15.

LFP analysis in terms of (a) amplitude variation, (b) instantaneous phases, and (c) normalized PSD with above threshold stimulation (left), below threshold stimulation (middle), and no stimulation (right).





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Fig. 17.

In vitro measurement setup using the sheep model with a close-up view of the FF-WIOS and the resonator.

Table I

Measurement of 3-Coil, Inductive Link Specifications at 60 MHz

Coil	Tx	Resonator	Rx
Inductance (nH)	26	21.2	50.1
Resistance (Ω)	0.04	0.1	0.85
Quality factor (Q)	246	79	22
Diameter (mm)	14.3	11.6	1.6
Number of turns	1	1	6
Wire gauge	AWG 20	AWG 26	AWG 34
Separation (mm)	D _{Tx-Re}	$e_s = 5$ D_{Res}	$R_{R_X} = 2$
PTE		21%	
Carrier frequency (f)		60 MHz	

Table II

Measurement of FF-WIOS SoC Specifications

Ove	rall system
ASIC area	1 mm ²
Power with stimulation	1 mW (Average)
Power without stimulation	300 µW
Voltage doubler efficiency	43 %
Switch capacit	tor-based stimulation
Target voltage	5 V
Charging efficiency	37 %
Charging time	30 ms
$C_s / C_{LED} / C_L$	$10~\mu F$ / $10~\mu F$ / $10~\mu F$
Optical stim. efficiency	62.5 %
Stimulat	ion parameters
Stimulation frequency	2.5 Hz ~ 10 Hz, 2 bits
Pulse width	0.5 ms ~ 2 ms, 2 bits
Current limiter	250 μA ~ 1 mA, 2.5 mA ~10 mA
Forward and	back data telemetry
Data bits	12 bits
Pre/post-amble bits	10 bits
PPM data rate	50 kbps
LSK data rate	160 bps
BT pulse width	1 or 2 µs, 1 bit

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Device weight 20-50 220 30	5 9.8 mm diameter, 1.3 mm thickness 1.3	i×1.3 ×(0.6-1.0)	2×3×6.5	2.5×2.5×1.5 (in vivo) 8×14×2 (in vivo)
	0 30	2.3	78	15 mg (in vitro) 0.5 g (in vivo)
Stim. proper Ctrl No No No	No	No	Yes	Yes
In vivo experiments Yes Yes Yes	s Yes	No	Yes	Yes

2

** Power eff. = rectifier eff. \times LDO eff.

*** Optical eff: output light power divided by input power.