

## Field-effect at electrical contacts to two-dimensional materials

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Received: 30 March 2021 / Revised: 4 June 2021 / Accepted: 9 June 2021

### ABSTRACT

The inferior electrical contact to two-dimensional (2D) materials is a critical challenge for their application in post-silicon very large-scale integrated circuits. Electrical contacts were generally related to their resistive effect, quantified as contact resistance. With a systematic investigation, this work demonstrates a capacitive metal-insulator-semiconductor (MIS) field-effect at the electrical contacts to 2D materials: The field-effect depletes or accumulates charge carriers, redistributes the voltage potential, and gives rise to abnormal current saturation and nonlinearity. On one hand, the current saturation hinders the devices' driving ability, which can be eliminated with carefully engineered contact configurations. On the other hand, by introducing the nonlinearity to monolithic analog artificial neural network circuits, the circuits' perception ability can be significantly enhanced, as evidenced using a coronavirus disease 2019 (COVID-19) critical illness prediction model. This work provides a comprehension of the field-effect at the electrical contacts to 2D materials, which is fundamental to the design, simulation, and fabrication of electronics based on 2D materials.

### KEYWORDS

field-effect, electrical contact, two-dimensional materials, nonlinearity, in-memory-computing

## 1 Introduction

The emergence of artificial intelligence and 5G communication requires electronics with greater computing performance and higher energy efficiency. With their abundance and the rich variety of electronic properties, the family of atomically thin two-dimensional (2D) materials is a promising choice in the next generation of very-large-scale-integration (VLSI) technology [1–4]. Electronics devices based on atomically thin 2D materials such as transition metal sulfides, black phosphorus, and graphene, have been intensively studied during the past decade. 2D material-based electronics show great potential for building both traditional von Neumann computing architecture and state-of-art non-von Neumann in-memory-computing architecture [5–17]. However, the inferior electrical contacts greatly hinder the performance of 2D material-based electronic devices [18–22]. The inferior electrical contacts are generally related to their significant resistive effect at the interface, quantified as the contact resistance [13]. Multiple elaborate methods have been developed to lower the contact resistance through engineering the metal-2D material interface, such as low work function metallization [23], defect and impurity control [24, 25], doping [26–29], phase change transition [30], and the recent van der Waals contacts [31–33]. Herein, continuous improvement of the electrical to 2D materials is required, and full fundamental

comprehension of the electrical contacts to 2D materials is substantially needed.

In previous researches, attention has been generally focused on the resistive effect at the electrical contact interface. In this research, we investigate the electrical contacts to 2D materials from a bottomed physical perspective and demonstrate that for the atomically thin 2D materials, the electrical contacts contribute not only the resistive effect but also a significant capacitive metal-insulator-semiconductor (MIS) field-effect. The capacitive MIS field-effect can deplete or accumulate the charge carriers, redistribute the voltage potential, and give rise to unusual current saturation and nonlinearity. Based on a thorough understanding, one can selectively decide whether to eliminate such field-effect or use it. On one hand, engineering the electrical contact configuration can eliminate the field-effect and significantly improve the driving current of the 2D material based transistors. On the other hand, the nonlinearity introduced by the electrical contact to 2D materials can efficiently improve the perceptron ability of the non-von Neumann in-memory-computing circuit, as demonstrated by a COVID-19 critical illness prediction model. The principle in this research generally applies to a wide range of 2D materials. The comprehension of the field-effect at electrical contacts facilitates the design, simulation, and fabrication of electronics based on 2D material.

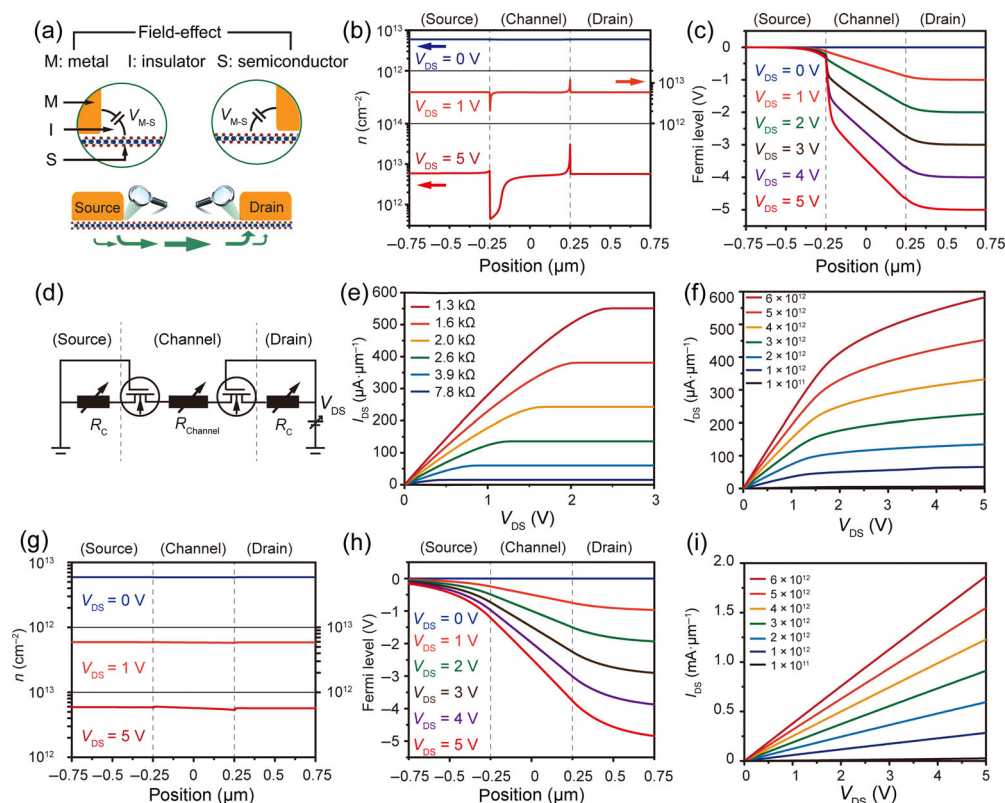
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## 2 Results and discussion

### 2.1 Principle of the capacitive MIS field-effect at electrical contact

For concision, we first expound on the general principle of the capacitive MIS field-effect. Figure 1(a) shows a simplified 2D material channel (monolayer MoS<sub>2</sub>, for example) with two contacting metallic electrodes. As shown in the inset, the sidewall of the metallic electrode, the insulating dielectric (or vacuum), and the semiconducting channel form a fringe metal-insulator-semiconductor (MIS) structure. At the ends of the channel, the distance between the metallic sidewall and the channel is small (Ångstroms to nanometers). The capacitive coupling between the metallic sidewall and the channel can be very effective. Therefore the voltage drop between the metallic sidewall and the channel (which is  $V_{M-S} = I_{DS} \cdot R_C$ , where  $I_{DS}$  is the current and  $R_C$  is the contact resistance) can create a significant MIS field-effect. As shown in Fig. S1(a) in the Electronic Supplementary Material (ESM), we simulated the simplified device (monolayer MoS<sub>2</sub>, n-type, donor density  $6 \times 10^{12} \text{ cm}^{-2}$ , intrinsic mobility  $400 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$ , channel length 500 nm, and contact resistance  $2R_C = 1.3 \text{ k}\Omega$ ) with technology computer-aided design (TCAD). Figure 1(b) and Fig. S1(b) in the ESM show the simulated electron density distribution in the channel. For  $V_{DS} = 0 \text{ V}$ , the electron density is inconsistent with the donor doping density. With applied  $V_{DS} = 1 \text{ V}$ , carriers deplete/accumulate at the source/drain end of the channel. For increased  $V_{DS}$  up to 5 V, the depletion/accumulation is further strengthened. The depletion/accumulation is due to the field-effect coupled through the MIS structure, where the voltage drop  $\pm V_{M-S}$  is applied between the source/drain electrode sidewall and the channel, respectively. The fully depleted region is non-conducting,

correspondingly, a considerable part of voltage potential drops on the depletion region, as shown in Fig. 1(c). Considering this capacitive MIS field-effect, we plot the equivalent circuit as shown in Fig. 1(d). Besides the source/drain contact resistors ( $R_C$ ) and the channel resistor ( $R_{Ch}$ ), the circuit consists of two extra parasitic MIS field-effect transistors. The gates of the transistors correspond to the sidewalls of the electrodes. The  $I_{DS}-V_{DS}$  curves of the equivalent circuit with various contact/channel resistances ( $2R_C = R_{Ch}$ ) are shown in Fig. 1(e), which are in good accordance with the TCAD simulated  $I_{DS}-V_{DS}$  curves of the constructed devices with various doping densities, as shown in Fig. 1(f). The current increases linearly with small  $V_{DS}$  and saturates with large  $V_{DS}$ . The current saturation results from a negative feedback mechanism: With increased  $V_{DS}$ , the field-effect at the Source end can fully deplete the carriers through the atomically thin 2D material, dramatically decrease the conductivity, prevent the  $I_{DS}$  from increasing, and in turn cause apparent current saturation. We further carry comparative simulations by replacing the source/drain with the virtual zero-thickness electrodes, as shown in Fig. S1(c) in the ESM. In this case, the fringe capacitive MIS field-effect coupling is absent. Consequently, we do not see the carrier depletion/accumulation at the ends of the channel, as shown in Fig. 1(g) and Fig. S1(d) in the ESM. The voltage drop redistributes equally, and the  $I-V$  curves maintain good linearity, as shown in Figs. 1(h) and 1(i). The comparative simulations confirm the field-effect originated from the MIS structure. The zero-thickness virtual electrodes neglect the apparent field-effect at the electrical contact to 2D materials, which should be used with extra caution in TCAD simulations. Additionally, the field-effect for devices with various metal electrode thicknesses and channel thicknesses (layer number) is also investigated, as shown in Fig. S2 in the ESM.

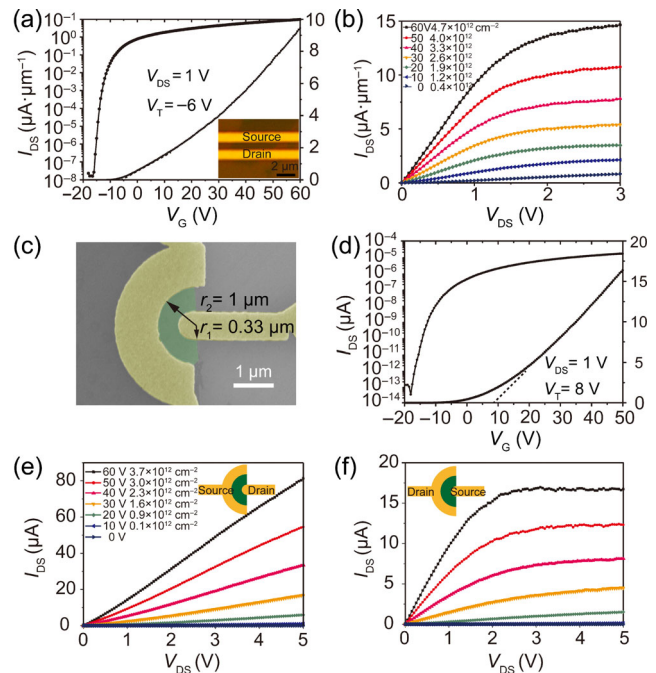


**Figure 1** Principle of the capacitive MIS field-effect. (a) Capacitive MIS field-effect of electrical contacts coupled through the MIS structure. (b) Electron density and (c) voltage potential along the MoS<sub>2</sub> channel. (d) Equivalent circuit of devices considering the capacitive MIS field-effect. (e)  $I_{DS}-V_{DS}$  curves of the equivalent circuit. The legends are the contact/channel resistance ( $2R_C = R_{Ch}$ ). (f)  $I_{DS}-V_{DS}$  curves of the TCAD simulated device, the legends are the doping density ( $\text{cm}^{-2}$ ). (g) Electron density, (h) the voltage drop distribution, and (i)  $I_{DS}-V_{DS}$  curves of the device without the capacitive MIS field-effect.

The above analysis has proposed a field-effect at the electrical contacts to 2D materials, which can significantly deplete/accumulate carriers, change the voltage potential distribution, and give rise to apparent current saturation. For clarification, the above simulations have used the simplified device geometry with only one channel and two electrodes. However, the principle is universal and applies to practical 2D material devices with more complex geometries, as will be shown below. A similar analysis also applies to Schottky contacts, where the field-effect widens the Schottky barrier width at the contact area, blocks the field/thermal-field emission current, and causes a similar current saturation, as we specify detailedly in Section S3 in the ESM.

## 2.2 Capacitive MIS field-effect at the electrical contacts to monolayer MoS<sub>2</sub>

So far, we have theoretically proposed the significant capacitive MIS field-effect of the electrical contacts to ultra-thin 2D material channels. The question is, in practical cases, does this field-effect have a real impact on 2D material electronic devices? To address this question, we experimentally fabricated the back gate monolayer MoS<sub>2</sub> field-effect transistors (FETs). Here we used Ti as the contacting metal with the gate-dependent contact resistance ranging from 20 to 400 kΩ·μm [22]. The transfer curves and output  $I$ - $V$  curves are shown in Figs. 2(a) and 2(b), respectively. We note that the  $I$ - $V$  curves are linear at small  $V_{DS}$  and the current saturates at large  $V_{DS}$ , which is in accordance with the simulation results in Figs. 1(e) and 1(f). Similar current saturation is observed not only with Ti/MoS<sub>2</sub> contacts [19, 34], but also with Ni/MoS<sub>2</sub> contacts (see Fig. S4 in the ESM) [35], Au/MoS<sub>2</sub> contacts [8], Ti/WSe<sub>2</sub> contacts [36], Ag/WSe<sub>2</sub> contacts [36], van der Waals VSe<sub>2</sub>/WSe<sub>2</sub> contacts [21], etc. In all the above cases, we find  $V_G - V_T \gg V_{DS}$  ( $V_G$  is the gate voltage and  $V_T$  is the threshold voltage), and therefore the current saturation should not result from the common pinch-off of the channel. Previous studies have attributed such abnormal current saturation to several possible mechanisms such as carrier velocity saturation [35, 37], self-heating effect [34], or Schottky barriers [19], while the field-effect at the electrical contact has not been recognized. To address the origin of the abnormal current saturation, we designed and fabricated asymmetric FETs with an arc channel, as shown in Fig. 2(c). The inner radius  $r_1$  and the outer radius  $r_2$  are 0.33 and 1.0 μm, respectively. Therefore the contact resistance is asymmetric, with  $R_{C1}$  prominently larger than  $R_{C2}$  ( $R_{C1}:R_{C2} \approx r_2/r_1 = 3$ , considering the relatively small transfer length) [20]. The transfer curve of the device is shown in Fig. 2(d). The extracted effective carrier mobility is  $12 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$ . Two sets of  $I$ - $V$  curves were obtained comparatively: In Fig. 2(e), the outer electrode was used as the source, the inner electrode as drain, and the  $I$ - $V$  curves are linear; while in Fig. 2(f), the inner electrode was used as the source, the outer electrode as drain, and the current starts to saturate with  $V_{DS} \sim 2 \text{ V}$ . This is direct evidence that the current saturation is closely related to the electrical contacts, rather than the channel. When the inner electrode is the source, the voltage drops at  $I_{DS} \cdot R_{C1}$  is relatively large, the field-effect depletes the electrons effectively and causes current saturation. When the outer electrode is the source, the voltage drop at  $I_{DS} \cdot R_{C2}$  is relatively small, which is not large enough to fully deplete the electrons or cause current saturation. If the current saturation was caused by the carrier velocity saturation or self-heating effect, the current saturation behavior in Figs. 2(e) and Fig. 2(f) would be identical. If the current saturation was caused by the switching of the Schottky barrier [19], the current saturation should happen when the inner/outer electrode is used as the



**Figure 2** Capacitive MIS field-effect of electrical contacts to monolayer MoS<sub>2</sub>. (a) Transfer curve and (b)  $I$ - $V$  curves of the back gate monolayer MoS<sub>2</sub> FET. The inset in (a) is the microscope image of the device, and the channel length is 0.8 μm. (c) SEM image of the asymmetric MoS<sub>2</sub> back gate FET with arc channel. (d) Transfer curve of the asymmetric MoS<sub>2</sub> back gate FET. (e)  $I_{DS}$ - $V_{DS}$  curves of the asymmetric MoS<sub>2</sub> back gate FET with the outer/inner electrode as the source/drain respectively. (f)  $I_{DS}$ - $V_{DS}$  curves of the asymmetric MoS<sub>2</sub> back gate FET with the inner/outer electrode as the source/drain, respectively.

drain/source respectively, which is contrary to the above observations. The mere Schottky barrier (without considering field-effect) dominated  $I$ - $V$  curves should present increased differential conductance at large  $V_{DS}$ , due to the lowered Schottky barrier height and enhanced field/thermal-field emission with increased  $V_{DS}$  [38–40], as shown in Fig. S3(a) in the ESM.

Based on the above analysis, it is shown that the capacitive MIS field-effect of the electrical contacts can dominate the electrical characteristics of practical monolayer MoS<sub>2</sub> devices, giving rise to current saturation at large  $V_{DS}$ . Note that the contacting angle between the metal and the channel might be slanted, as shown in Section S5 in the ESM. The field-effect coupling with the slanted angle can be more robust and give rise to more apparent carrier depletion/accumulation, voltage potential redistribution, and current saturation, as shown in Section S6 in the ESM. We also note that the current saturation is closely related to the degeneration of the device, as shown in Section S7 in the ESM, probably because the surface of Ti is oxidized into TiO<sub>x</sub> with a high dielectric constant ( $\sim 18$ ) and further strengthens the capacitive MIS field-effect [41].

## 2.3 Capacitive MIS field-effect at the electrical contacts to graphene

The concept of capacitive MIS field-effect for electrical contacts is general and should apply to a wide range of ultra-thin channel devices. Here we show how the capacitive MIS field-effect of the electrical contacts affects the characteristics of graphene FETs. Unlike semiconducting 2D materials with sizeable bandgaps, graphene is semi-metallic with zero bandgap. Considering the capacitive MIS field-effect, for the n-type graphene channel, electrons deplete at the source end and

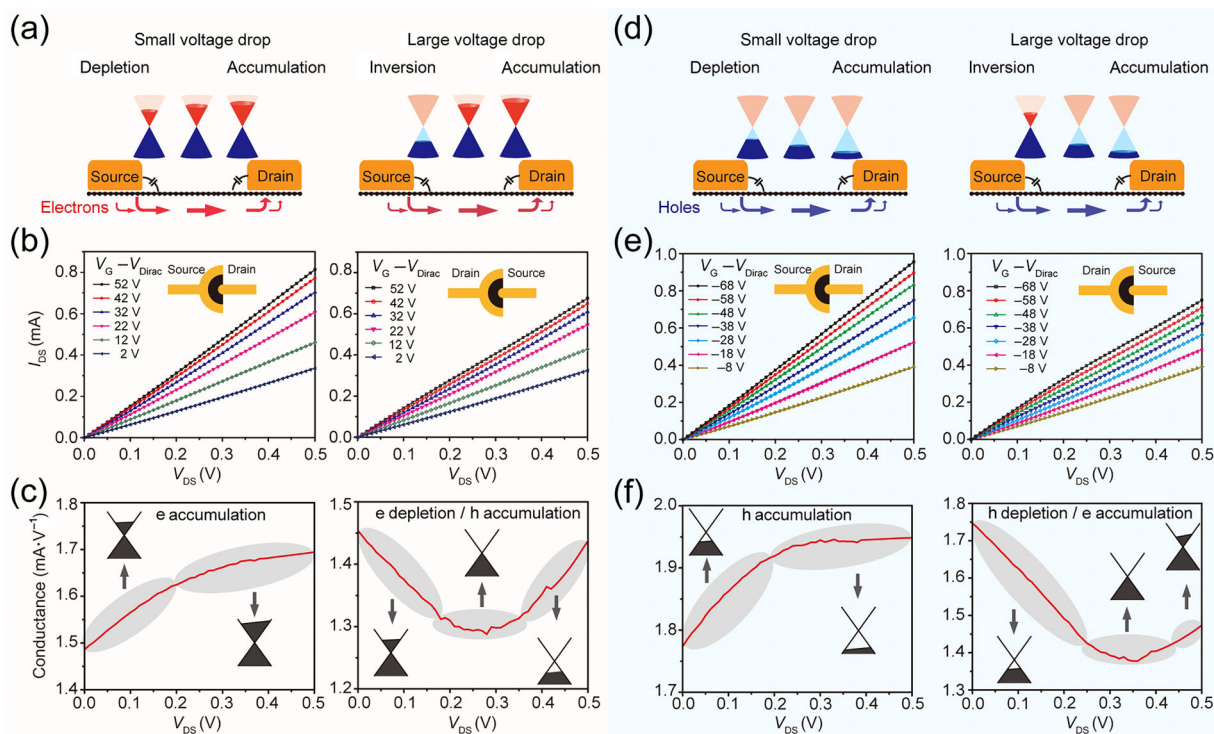


accumulate at the drain end at a small voltage drop (Fig. 3(a) left). The source end may further reverse at a large voltage drop, and holes accumulate due to the bandgap absence (Fig. 3(a) right). We fabricated the arc channel graphene FETs with  $r_1$  of 0.5  $\mu\text{m}$  and  $r_2$  of 1.5  $\mu\text{m}$  (Fig. S8(a) in the ESM). The Dirac point extracted from the transfer curve is 8 V (Fig. S8(b) in the ESM), and the extracted effective carrier mobilities for electrons and holes are 1,089 and 675  $\text{cm}^2\cdot\text{V}^{-1}\cdot\text{s}^{-1}$ . We compared the  $I$ - $V$  curves with the outer/inner electrode as the source or drain alternated. The current is larger with the outer/inner electrode as the source/drain electrode (Fig. 3(b), left), and the derived differential conductance continually increases with  $V_{\text{DS}}$  (Fig. 3(c), left,  $V_{\text{G}} - V_{\text{Dirac}} = 52$  V, where  $V_{\text{Dirac}}$  is the Dirac point voltage). In contrast, the current with the inner/outer electrode as the source/drain electrode is smaller (Fig. 3(b), right, compared in Fig. S8(c) in the ESM), and the derived differential conductance first decreases and then increases with  $V_{\text{DS}}$  (Fig. 3(c), right). Furthermore, the inversion point of  $V_{\text{DS}}$  is monotonically dependent on the gating, as shown in Figs. S8(e) and 8(g) in the ESM. The above observation is in good agreement with the principle of the capacitive MIS field-effect for the electrical contacts: The inner electrode with larger contact resistance dominates the outer electrode; when the inner electrode is used as the drain, the electrons accumulate and the conductance continually increases; while when the inner electrode is used as the source, the electrons firstly deplete and then the holes accumulate with increased  $V_{\text{DS}}$ ; consequently, the conductance firstly decreases and then increases. The identical analysis is applied to the p-type channel graphene, and similar results are observed, as shown in Figs. 3(d)–3(f), and Figs. S8(d), S8(f), S8(h) in the ESM. Therefore, the capacitive MIS field-effect of the electrical contacts applies to a wide range of 2D materials, including both semiconducting 2D materials and semi-metal 2D materials.

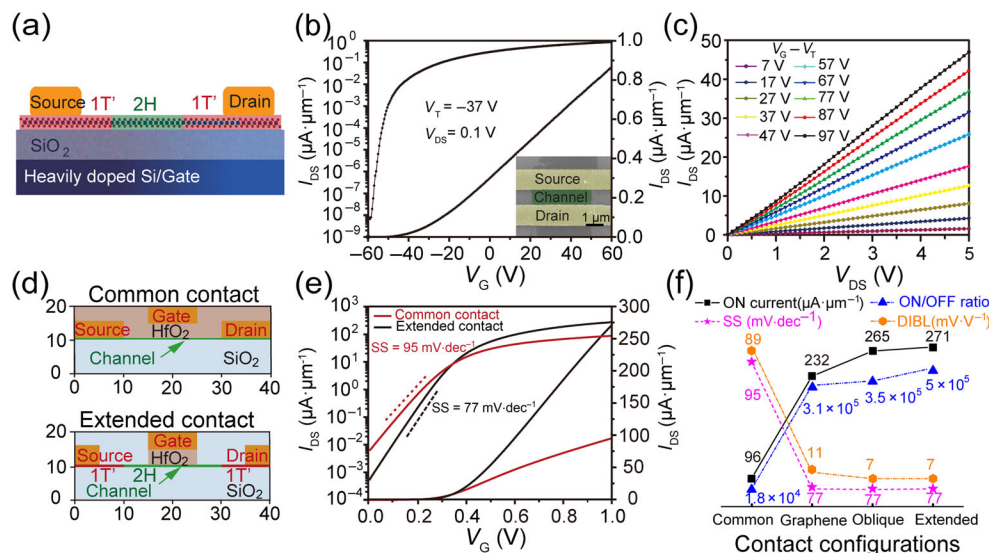
## 2.4 Engineering the electrical contact configuration

The comprehension of field-effect at the electrical contact to 2D materials leads to an interesting question of whether to eliminate it or use it, depending on different application scenarios. For 2D material-based transistors, the field-effect at electrical contact saturates the ON current and hinders the devices' driving ability. The field-effect is critically dependent on the configuration. An elaborately designed electrical contact configuration should be able to eliminate the capacitive MIS field-effect effectively. As shown in Fig. 4(a) and Fig. S9 in the ESM, extended electrodes are realized by the butyl lithium induced 2H/1T' phase change transition, so that the sidewall of the metallic electrode is kept far away from the semiconducting channel. Such configuration eliminates the capacitive MIS field-effect, and subsequently, the current is not saturated, as shown in Figs. 4(b) and 4(c). Therefore, the efforts to improve the electrical contact to 2D materials should concentrate on not only the interface between the metallic electrode and 2D materials (to lower the contact resistance), but also the contact configuration (to eliminate the field-effect).

Using TCAD simulations, we further studied electrical monolayer MoS<sub>2</sub> short-channel transistors (10 nm gate length, 20 nm source/drain distance) with various contact configurations. The contact resistance was uniformly set to 800  $\Omega\cdot\mu\text{m}$ . Figure 4(d) shows two comparative transistors: The first transistor (upper) is with the common contact configuration, and the second transistor (lower) is with the optimized contact configuration, including the extended electrodes and the low dielectric spacer, to suppress the coupling through the capacitive MIS field-effect. The transfer curves of the two transistors are compared in Fig. 4(e). The red lines are from the transistor with the common contacts, and the black lines are from that with the extended electrode contacts. Compared to the common contact configuration, the extended electrode increases the



**Figure 3** Capacitive MIS field-effect of the electrical contacts to graphene. Principle of the capacitive MIS field-effect for metal–graphene contacts, the depletion, accumulation, and inversion of the graphene channel: (a) n-region; (d) p-region.  $I_{\text{DS}}-V_{\text{DS}}$  curves of the graphene FET with the outer/inner electrode as the source/drain (left), and the inner/outer electrode as the source/drain (right): (b) n-region; (e) p-region. The differential conductance of the graphene FET with the outer/inner electrode as the source/drain (left), and the inner/outer electrode as the source/drain (right): (c) n-region ( $V_{\text{G}} - V_{\text{Dirac}} = 52$  V); (f) p-region ( $V_{\text{G}} - V_{\text{Dirac}} = -68$  V).



**Figure 4** Electrical contact configuration engineering. (a) Schematic of the MoS<sub>2</sub> FET with the extended source/drain electrode. (b) Transfer curve and (c) output curves of the FET in (a). The inset in (b) is the SEM image of the device, and the channel length is 0.9  $\mu\text{m}$ . (d) Schematic of the short channel monolayer MoS<sub>2</sub> FET with the common contact configuration (upper) and with the optimized contact configuration with extended electrodes/low dielectric spacer (lower). The contact resistances are both set to be 800  $\Omega\cdot\mu\text{m}$ . (e) Transfer curves of the FETs with the common contact configuration (red) and the extended contact configuration (black). (f) ON currents, ON/OFF ratios, SSs and DIBLs of four short channel MoS<sub>2</sub> FETs, with the common contact configuration, the graphene contact configuration, the oblique angle contact configuration, and the extended contact configuration, respectively.

ON current from 96 to 271  $\mu\text{A}\cdot\mu\text{m}^{-1}$  and the ON/OFF ratio from  $1.8 \times 10^4$  to  $5 \times 10^5$ , and suppresses the subthreshold swing (SS) from 95 to 77  $\text{mV}\cdot\text{dec}^{-1}$ , and the drain-induced-barrier-lowering (DIBL) from 89 to 7  $\text{mV}\cdot\text{V}^{-1}$ . Therefore, the optimized contact configuration eliminates the capacitive MIS field-effect effectively and improves the device performance significantly. We further propose other engineered contact configurations, such as the contacts with the oblique contacting angle or the contacts using graphene as the extended electrode [42], as shown detailedly in Fig. S10 in the ESM. The device performances with various contact configurations are summarized in Fig. 4(f) and Table 1. All of the three optimized electrical contact configurations dramatically improve the performance of monolayer MoS<sub>2</sub> short-channel transistors.

## 2.5 Enhancing perception ability of artificial neural network (ANN) circuits

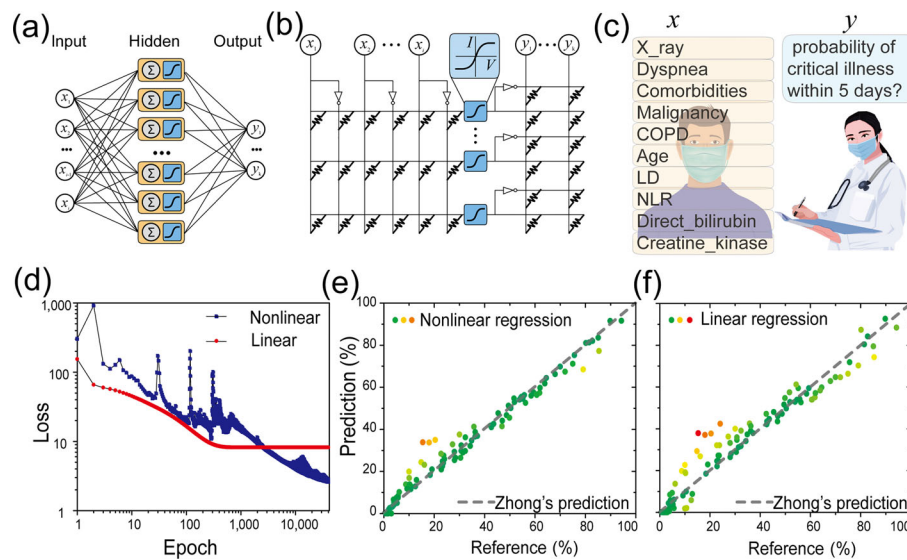
ANN implanted in in-memory-computing circuits has great advantages in parallel computing acceleration and energy efficiency [43–46]. For ANN, neurons with nonlinear activation functions are essential. In the mathematical theories of ANN, the universal approximation theorem has demonstrated that given appreciate squashing activation function (for example, S-shape functions), a three-layer ANN perceptron consisting of an input layer, one hidden layer with an arbitrary number of neurons, and an output layer (Fig. 5(a)) can approximate any Lebesgue integrable function [47–49]. The S-shape tanh and sigmoid activation functions, which feature the saturating nonlinearity, are among the most commonly used activation functions in ANN. Analog/digital conversions with additional

activation processors can achieve such nonlinearities, but they consume the extra chiplet/die area and energy, and increase the complexity of the design and fabrication process [50, 51].

In the above discussion, this work has demonstrated that the field-effect of the electrical contact to 2D materials can introduce saturating nonlinearity in  $I$ - $V$  curves. One can easily obtain a tanh-like or sigmoid-like  $I$ - $V$  curve with the MoS<sub>2</sub> or other 2D material based devices, as shown in Fig. S11 in the ESM. A three-layer in-memory-computing ANN perceptron circuit diagram is shown in Fig. 5(b). Such hybrid circuits can be achieved with the compact 3D integration shown in Fig. S12 in the ESM, where MoS<sub>2</sub> channels are nonlinear neurons, and random access memories are synapses. We simulated the ANN perceptron to approximate a coronavirus disease 2019 (COVID-19) early-stage prediction dataset model, as shown in Fig. 5(c). The inputs are ten clinical indexes, and the output is the predicted probability that the patient develops to critical illness within 5 days. Such prediction is essential for the triage treatment of COVID-19 patients, as detailedly described in an earlier paper by Zhong et al. [52]. Here we use 3,000 derivate patients' clinical indexes (input) and Zhong's prediction (output) for the training of the perception. As shown in Fig. 5(d), with the nonlinearity introduced, the ANN reaches a lower loss than the linear regression in 45,000 epochs. The regressed critical illness probability of 100 patients shows significantly improved accuracy than the linear regression model, with the average error decreased from 4.71% to 2.87%, as shown in Figs. 5(e) and 5(f). Therefore, the electrical contact introduced nonlinearity can significantly improve the accuracy of the in-memory-computing integrated ANN perceptron with

**Table 1** Devices' performance with various contact configuration

Contact configuration	Common		Graphene		Oblique		Extended	
	Value	Ratio	Value	Ratio	Value	Ratio	Value	Ratio
ON current ( $\mu\text{A}\cdot\mu\text{m}^{-1}$ )	96	–	232	2.4	265	2.8	271	2.8
SS ( $\text{mV}\cdot\text{dec}^{-1}$ )	94.6	–	77	0.81	76.9	0.81	77	0.81
ON/OFF ratio	$1.8 \times 10^4$	–	$3.1 \times 10^5$	17	$3.5 \times 10^5$	19	$5 \times 10^5$	28
DIBL ( $\text{mV}\cdot\text{V}^{-1}$ )	89	–	11	0.12	7	0.08	7	0.08



**Figure 5** Enhancing perceptron ability in ANN circuits with nonlinearity. (a) Schematic of a three-layer ANN. (b) Circuit diagram of the ANN in-memory-computing. (c) Schematic of the COVID-19 early-stage prediction model. COPD: Chronic obstructive pulmonary disease. LD: Lactate dehydrogenase. NLR: Neutrophil/lymphocytes ratio. (d) Loss in the training process. The predicted probability (e) with and (f) without nonlinearity in the ANN circuits. The dotted reference lines are from Zhong et al.'s model described in their earlier paper [52].

compact device geometry compared to normal transistor devices working in the saturation region.

### 3 Conclusion

This work proposes and demonstrates a field-effect at the electrical contacts to 2D materials, which has long been neglected in previous studies. The field-effect can deplete and accumulate the carriers, change the carrier densities and types, and introduce current saturation and nonlinearity. Based on such comprehension, on one hand, we call for attention to the optimization of the electrical contact configuration for 2D material transistors, which increases the current driving ability of devices. On the other hand, we demonstrate that the nonlinearity introduced by the field-effect at the electrical contact can enhance the perception ability of the hybrid ANN circuits. The principle in this work is general and applies to a wide range of 2D material devices. The full comprehension of the electrical contacts to 2D materials is fundamental to the simulation, design, and fabrication of 2D material-based electronic devices.

### Acknowledgements

We thank Prof. H.-S. Philip Wong from Stanford University and Analysis & Testing Center, Beijing Institute of Technology for the support of this work. We thank Prof. Zhiyong Zhang and Prof. Qing Chen from Peking University for useful discussion. Yao Guo thanks Dr. I-Ting Wang for the valuable suggestions. This work was supported the National Natural Science Foundation of China (No.11804024).

**Electronic Supplementary Material:** Supplementary material (results of the simulation and SEM) is available in the online version of this article at <https://doi.org/10.1007/s12274-021-3670-y>.

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