

Slot-Die Coated Triple-Halide Perovskites for Efficient and Scalable Perovskite/Silicon Tandem Solar Cells

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Cite This: *ACS Energy Lett.* 2022, 7, 3600–3611

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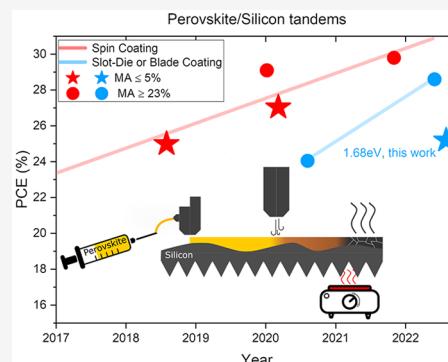
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ABSTRACT: Wide bandgap halide perovskite materials show promising potential to pair with silicon bottom cells. To date, most efficient wide bandgap perovskites layers are fabricated by spin-coating, which is difficult to scale up. Here, we report on slot-die coating for an efficient, 1.68 eV wide bandgap triple-halide (3halide) perovskite absorber, $(\text{Cs}_{0.22}\text{FA}_{0.78})\text{Pb}(\text{I}_{0.85}\text{Br}_{0.15})_3 + 5 \text{ mol } \% \text{ MAPbCl}_3$. A suitable solvent system is designed specifically for the slot-die coating technique. We demonstrate that our fabrication route is suitable for tandem solar cells without phase segregation. The slot-die coated wet halide perovskite is dried by a “nitrogen (N_2)-knife” with high reproducibility and avoiding antisolvents. We explore varying annealing conditions and identify parameters allowing crystallization of the perovskite film into large grains reducing charge collection losses and enabling higher current density. At 150 °C, an optimized trade-off between crystallization and the PbI_2 aggregates on the film’s top surface is found. Thus, we improve the cell stability and performance of both single-junction cells and tandems. Combining the 3halide top cells with a 120 μm thin saw damage etched commercial Czochralski industrial wafer, a 2-terminal monolithic tandem solar cell with a PCE of 25.2% on a 1 cm^2 active area is demonstrated with fully scalable processes.



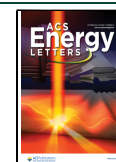
Tandem solar cells, combining a tunable wide bandgap halide perovskites top cell (around 1.68 eV) with a silicon bottom cell, surpassed the Auger recombination limited single-junction silicon solar cells’ efficiency^{1,2} and have recently reached a world record power conversion efficiency (PCE) of 29.8%.³ Both the state-of-the-art halide perovskites single-junction solar cell (25.7% PCE on 0.1 cm^2)⁴ and the perovskite/silicon tandem solar cell (29.8% PCE on 1 cm^2)³ are manufactured by spin coated perovskite layers. However, for spin-coating, the homogeneity of the thin film on large scale is always a main challenge.⁵ When the active area increases further, it becomes more difficult to control the film thickness from the substrate center toward the edges. In addition, 90% of the halide perovskites precursor and antisolvents are spun off and wasted during the spin-coating process, which is not economically and environmentally viable for mass production.⁶ In addition, antisolvents deposited during spin-coating are typically used to achieve high-quality thin films: they interact with halide perovskites precursors to trigger crystallization of halide perovskites out of the wet film.

However, the film formation is still not fully understood and is difficult to control for larger areas. Besides achieving high PCEs close to 30%, the major challenge for perovskite/silicon tandems thus remains to transfer these high PCEs to commercial, large area, mass-produced silicon bottom cells. These have thicknesses $\leq 140 \mu\text{m}$ and a rough surface (mean arithmetic height, $S_a \approx 0.7 \mu\text{m}$) due to the saw damage etching (SDE) process. Spin-coating halide perovskites on such wafers is difficult. The thick perovskite film by slot-die coating (SDC) and insufficient thin wafer absorption need to be further investigated for the optimal current matching condition of the tandem solar cells.

Received: July 1, 2022

Accepted: September 6, 2022

Published: September 27, 2022



There are several printing techniques available for upscaling, such as blade coating, inkjet printing, and slot-die coating. The blade coating technique is limited by the automation process and ink supply, while inkjet printing is limited by the slow deposition speed and potential nozzle clogging. Equipped with an automatic coating table and continuous ink supply syringe with 100 μm coating slits for the slot-die head, the slot-die coater platform has advantages such as easy operation, easy cleaning, and accurate thickness. To control the drying process of the coated wet film, N_2 gas flows from so-called N_2 -knives have been widely used to induce film formation^{7–9} similar to the antisolvent drip in spin-coating. Therefore, slot-die coating with N_2 gas quenching, which is also possible to be upscaled for roll-to-roll production, is a promising candidate for scalable deposition and low cost.^{7,10,11} Another important aspect is to find a wide bandgap halide perovskites ink composition that leads to high crystal quality, no phase segregation, high thermal stability, and high PCE for these coating processes. The state-of-the-art halide perovskites solar cells have been summarized in [Figure S1](#) and [Table S1](#) for the coating techniques of spin-coating and printing in general. Two composition groups have been widely developed for the perovskite/silicon tandem applications, namely triple-cation perovskites comprising cesium (Cs^+), methylammonium (MA^+), and formamidinium (FA^+) as the A-cations^{12,13} and double-cation perovskites containing Cs^+ and FA^+ .^{14,15} The MA^+ free perovskites have the advantages of long-term and thermal stability while the performance is a shortcoming due to the limited photovoltage of these halide perovskites materials.^{16,17} According to optical simulations pairing halide perovskites with silicon in a tandem layer stack, the optimal perovskite bandgap is around 1.70 eV;¹⁸ thus, groups working on tandems have increased the bandgap from 1.63 eV toward 1.68 eV in past efforts. Cs^+ and Br^- are widely utilized to increase the bandgap, but cation and halide segregation have been the main limitation with maximum amounts of 25% Cs and 20% Br reaching 1.68 eV.¹⁹

Finally, considering the solvent system, the combination of DMF and DMSO has been the most popular system for most of the published compositions for spin-coating due to the high solubility.²⁰ However, without the usage of antisolvents, DMSO has been detrimental in scaling to large area deposition assisted by N_2 quenching. There are also two works reported for tandem solar cells fabricated by printing techniques.^{8,9} However, the bandgap, phase stability, and high MA concentration have been the bottlenecks that limit further development. Although 2-ME (2-mercaptoethanol) has been widely used for slot-die coating,^{21,22} the low solubility of Cs^+ and Br^- hinders their addition to precursor inks and, hence, the bandgap tunability. So far, ink systems with low methylammonium⁺ (MA^+) content and wide bandgap compositions for printing methods are rarely reported but are essential for the upscaling purpose of perovskite-based tandem solar cells.

Here we present a suitable ink system and film drying process for improved perovskite film quality with high photoluminescence quantum yield when using a so-called triple-halide perovskite with top cell optimized bandgaps.¹⁷ Additionally, we have integrated the halide perovskites into a tandem architecture with industrial silicon bottom cells, demonstrating scalable industrially relevant perovskite silicon tandem solar cells. The triple-halide perovskites contains chloride (Cl^-) to reduce the amount of bromide (Br^-) needed for top cell optimized bandgaps,¹⁷ also to improve the surface

morphology and to induce a surface passivation.²³ We targeted to include 5 mol % additional MAPbCl_3 into the well-investigated double-cation perovskites $\text{Cs}_{0.22}\text{FA}_{0.78}\text{Pb}(\text{I}_{0.85}\text{Br}_{0.15})_3$ (1.63 eV) to achieve a bandgap of 1.68 eV with no phase segregation and excellent optoelectronic properties. By optimizing the drying and annealing conditions from 100 to 170 $^\circ\text{C}$ for 20 min, the absolute as well as transient photoluminescence (PL) shows obvious improvements in quasi-Fermi level splitting (QFLS) and charge carrier lifetimes. In addition, the film morphology as a function of annealing conditions and the optimized crystallization enable large grain size reducing charge collection losses and, thus, achieving higher current density in solar cells. With annealing at 150 $^\circ\text{C}$, an optimized trade-off between sample crystallinity and the detrimental formation of large PbI_2 aggregates on the film's top surface is found. Thus, supreme stability and performance of slot-die coated perovskite single-junction cells toward a stabilized power output of 19.4% are achieved, which is the highest yet reported for halide perovskites with top cell optimized bandgaps. Finally, by integrating the optimized perovskite absorber layers' fabrication with silicon bottom cells made of commercial Czochralski (Cz) thin wafers (around 120 μm) and no extra chemical or mechanical surface polishing, a two-terminal monolithic tandem solar cell with a PCE of 25.2% on a 1 cm^2 active area is demonstrated with fully scalable processes. Our findings open the way for efficient perovskite/silicon tandem solar cells with optimized bandgap and scalable fabrication routes via slot-die coating.

Ink and Film Properties. We start with an investigation of halide perovskites' film formation and material properties for slot-die coating, so-called "triple-halide" perovskites with N_2 gas quenching, aiming at investigating with electrical and optical measurements on how the drying method and annealing temperature influence the formation of perovskite films. A spin coated triple-halide (3halide) perovskite with antisolvent treatment is used as reference for comparisons because of its excellent performance.¹⁷ The used 3halide perovskite precursor composition is $\text{Cs}_{0.22}\text{FA}_{0.78}\text{Pb}(\text{I}_{0.85}\text{Br}_{0.15})_3$ (denoted as Cs22Br15) + 5 mol % MAPbCl_3 . The detailed coating description can be found in the [Supporting Information](#) and in [Figure S3](#). A direct comparison of the PL intensity and peak position stability between spin coated (annealed at 100 $^\circ\text{C}$) and slot-die coated (annealed at 150 $^\circ\text{C}$) films on ITO coated glass covered with the self-assembled monolayer (SAM) molecules 2PACz²⁴ ([2-(9H-carbazol-9-yl)ethyl]phosphonic acid) can be found in [Figure S2](#) as a function of time. The spin coated double-cation host composition Cs22Br15 has also been utilized as reference. Under 1 sun illumination intensity, all three samples show no obvious peak shift. However, under 10 suns illumination, although no secondary peak is observed, an observable red shift of the PL emission is detected for both deposition methods (possibly enhanced by the small 0.4 mm^2 spot¹²). We found that, for both spin coated and slot-die coated samples, a slight red shift of the PL is observed indicating some light-induced phase-segregation. However, this perovskite composition is still desirable to be investigated for the solar cell under 1 sun illumination. In addition to the photostability, the low MA^+ concentration used here for the 3halide perovskite can also prevent degradation under high-temperature treatments, which might be necessary for, for example, screen-printed silver electrodes' curing formation in future upscaling.²⁵ To transfer the recipe to slot-die coating, a new solvent system has been

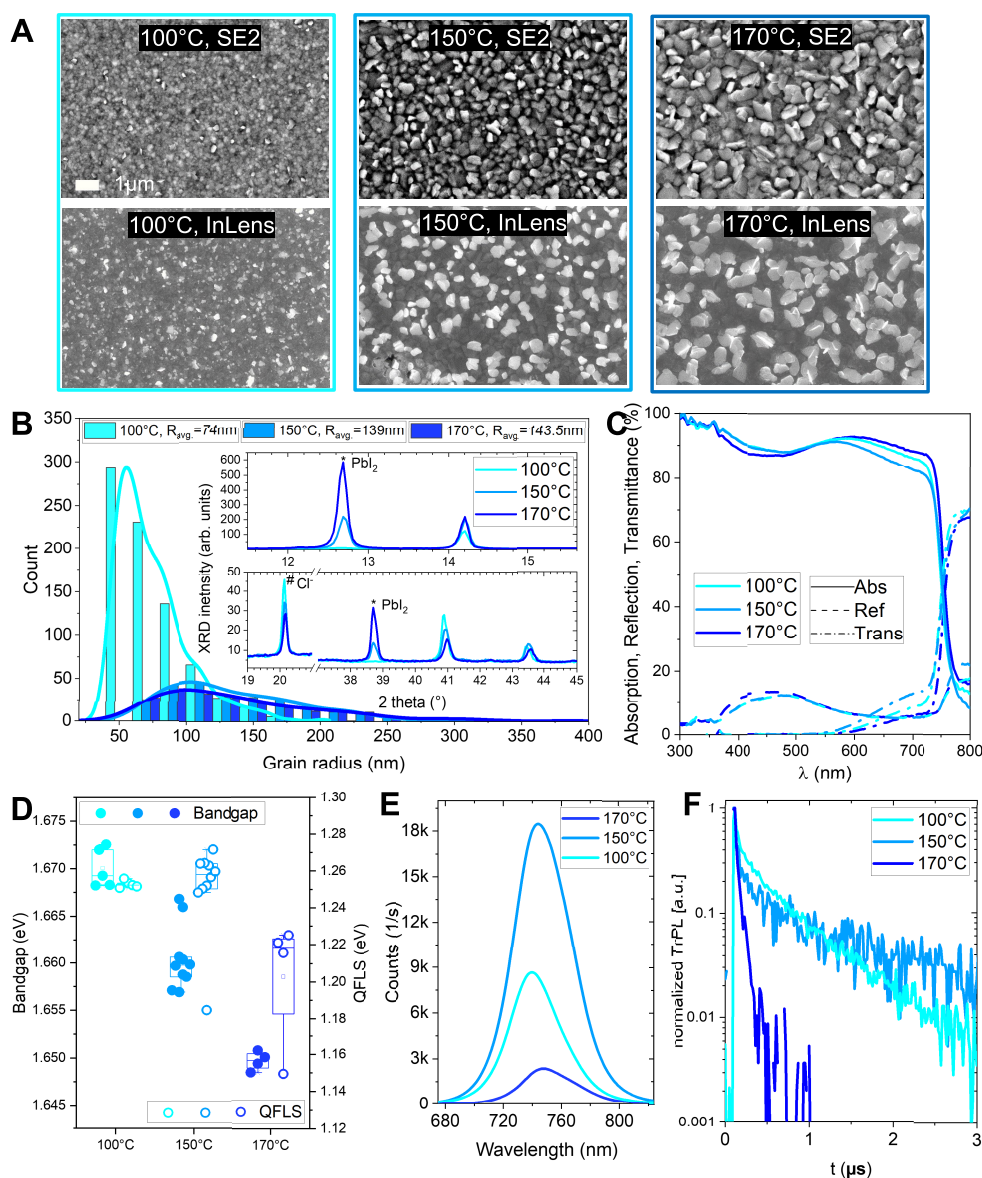


Figure 1. Characterizations of slot-die coated perovskite films on indium tin oxide (ITO) coated glass substrates covered with hole transporting materials (here self-assembled monolayer molecule “2PACz”) prepared at various annealing temperatures as indicated. (A) Top-view SEM images showing the perovskite domain size and surface composition distribution; the scale bar in part A applies to all SEM images. The first row is acquired by the secondary electron detector (SE2) showing the halide perovskites topography with high lateral resolution. The second row was recorded using the InLens detector (SE1) which enhances the surface compositional contrast. (B) Grain radius distribution as derived from the first row in panel A. The mean area equalized grain radius (R_{avg}) indicates an increased grain size for higher annealing temperatures. Inset: Angular X-ray diffraction (XRD) patterns, rescaled to allow for better comparison of peak positions. (C) UV-vis spectra to show the shift of the absorption edge with annealing temperatures. (D) Photoluminescence (PL) measurement statistics show that samples annealed at 150 °C have the highest calculated QFLS. (E) Corresponding PL spectra. (F) Time-resolved photoluminescence (trPL) transients, showing the slowest nonradiative recombination for a film annealed at 150 °C.

developed in this study: the DMF (dimethylformamide)-based solvent both has high solubility of precursors for the Cs^+ , FA^+ -based double-cation perovskites and is without precipitation during the coating process. In order to stabilize the intermediate stage between the gas quenching and post annealing, *N*-methyl-2-pyrrolidone (NMP) has been utilized to maintain the large intermediate wet film time scale with a wide processing time window between the gas quenching stage and the annealing stage in our work.²⁶

Here we will specifically focus on the properties of annealed halide perovskites films. After slot-die coating of the precursor

solutions on substrates covered with indium tin oxide (ITO) and hole selective SAMs from our previous studies,²⁴ the printed and N_2 -knife dried perovskite films were annealed at different temperatures (100, 150, and 170 °C) for 20 min, aiming for optimized film formation. Occasionally, we observed that the slot-die coated films annealed at 100 °C turn yellowish after exposure for 12 h to ambient air while the film annealed at 150 °C kept the black color for at least 5 days in 60% relative humidity ambient air. At the same time, for the antisolvent treated spin coated perovskite film which has been annealed at 100 °C, the color remains black in the same

ambient condition. The different vulnerability to moisture and oxygen is a first indication that a different perovskite composition and morphology have been formed. Meanwhile, the remaining solvate phases could also be formed due to the remaining solvents by the gas quenching process. In previous reports, numerous studies and investigations about the phase instability of FAPbI₃ were reported.^{27,28} Several methods were developed to keep the FAPbI₃ in the α -phase, such as increasing the annealing temperature or the incorporation of Cs and additives such as methylammonium chloride (MACl) and formamidinium chloride (FACl) into the precursor solution.^{29,30} However, all investigations were carried out by spin-coating with antisolvents treatment. Thus, we hypothesized that the crystallization and wanted perovskite phase formation could also proceed differently under N₂ quenching conditions. Therefore, we increased the annealing temperature further toward 170 °C, which was the boundary temperature to enter the α -FAPbI₃ crystal structure,²⁷ and investigated the film's surface morphology by using scanning electron microscopy (SEM) with both the secondary electron detector (SE2) and InLens detector (SE1), which are more sensitive to surface height topography and to surface composition reflected by the gray contrast, respectively. The apparent "grain" sizes of the SEM images in the top row of Figure 1A were analyzed with a modified algorithm applied to generate the equivalent grain radius distribution (see Experimental Procedures in the Supporting Information for further information) in Figure 1B.³¹ The algorithm calculates the size of the grain area, and then the same size circular area represents the calculated grain area. At last, the circular radius can be summarized in a histogram for easiness of comparison. This analysis shows clearly that higher annealing temperatures significantly enlarge the domain size from an average of 74 nm for 100 °C annealing to 139 nm for 150 °C and finally to 144 nm for 170 °C. In the next step, we examined the crystal properties by XRD, as shown in the inset of Figure 1B. A detailed comparison between the 100 °C annealed spin coated reference sample and the 150 °C annealed slot-die coated sample is also listed in Figure S4. The characteristic halide perovskites 2θ peak (14.1°, 100) intensity doubled when the temperature increased from 100 to 150 °C, which implies the enhanced crystallinity.³² The XRD pattern showed no further change at 14.1° when the temperature increased from 150 to 170 °C. The peaks at 12.7° and 38.7° correspond to the (001) PbI₂ diffraction plane, and the 20.1° and 40.8° peaks indicate the existence of Cl⁻ species (110) in the lattice.¹⁷ This proves the decreased inclusion of Cl⁻ within the halide perovskites lattice for slot-die coating, while secondary PbI₂ phases are generated during the 150 and 170 °C annealing steps. Meanwhile the Br-rich grains are also observed by energy-dispersive X-ray (EDX) in Figure S5, which is caused by the fast crystallization process from solution.³³ The lower amount of Cl⁻ is expected to reduce the bandgap, which is confirmed in Figure 1D and E.¹⁷ We speculate that the higher thermal budget (170 °C) induces a lower methylammonium chloride (MACl) concentration during the 20 min annealing process. The PbI₂ is clear by the XRD peaks at 12.7° (2.7-fold increase) and 38.7° (2.3-fold increase) in Figure 1B. The peak intensity starts to rise rapidly when the temperature is higher than 150 °C. The slot-die coated 3halide film shows no phase segregation that would be visible in the XRD pattern in the range between 15° and 15.5°, a finding that has been discussed in previous reports.¹⁷

In comparison with the slot-die coated 3halide film, the spin coated 3halide film shows no extra PbI₂ signal under XRD inspection.¹⁷ The degassing from the surface and bulk might lead to a gradient formation expected with higher annealing temperatures. It was important to find out whether the PbI₂ excess is located at the surface or in the bulk. Hence, a cross-sectional EDX was measured for samples annealed at 100, 150, and 170 °C (Figures S7–S9). Apart from an increased bromide concentration toward the surface, there is no direct evidence of an uneven gradient between I⁻ and Pb²⁺ from the cross-sectional EDX images. Thus, from these measurements there is no indication that the halide perovskites have decomposed into PbI₂ with high volume fraction. Furthermore, grazing incidence XRD (GIXRD) is utilized to investigate the perovskite surface with high sensitivity (see Figure S10) by comparing the intensity ratio of the diffraction peaks at 12.7° (PbI₂):14.1° (halide perovskites). For higher incidence angles, the measurements become more sensitive to the perovskite bulk film crystallographic information. At an incidence angle of 3°, the intensity ratio decreases for all samples, and the sample annealed at 170 °C still gave the highest value. This suggests that the secondary phase of PbI₂ mainly is situated on the perovskite thin film top surface and that the relative amount increases with annealing temperature. This observation is consistent with the increased amount of flake-like features with higher brightness observed for samples annealed at higher temperatures in SEM images.

The absorption spectrum (UV–vis) shows the optical property of the slot-die coated films (Figure 1C). With a 600 nm slot-die coated perovskites thin film, the absorption of more than 80% of the incident light between the wavelengths of 300 and 740 nm proves that these films can convert photons into charge carriers effectively. To measure the bandgap and estimate the quasi-Fermi level splitting (QFLS) of the perovskite (see Experimental Procedures in the Supporting Information), we utilize the absolute photoluminescence of the halide perovskites deposited on glass/ITO/SAM(2PACz) layer stacks as shown in Figure 1D. The bandgap decreased linearly with the temperature increment from 1.68 eV for 100 °C to 1.66 eV for 170 °C annealing. Despite the intermediate bandgap at 1.67 eV with 150 °C annealed samples, the QFLS shows the highest value among the annealing conditions at 1.26 eV, indicating low nonradiative recombination for 150 °C annealing. The spin coated 3halide reference sample (annealed at 100 °C) reaches 1.27 eV. The Gaussian shape fit of the slightly asymmetrical PL spectral result indicates a slightly lower bandgap compared with the bandgap derived from the external quantum efficiency (EQE) later.³⁴ The PL spectra shown in Figure 1E are also another direct evidence for the bandgap shift, as seen from the peak emission.

Time-resolved photoluminescence (trPL) of halide perovskites films was measured for the three annealing conditions on ITO coated glass with 2PACz as the hole transport layer and is displayed in Figure 1F. The fast initial decay can be attributed to fast hole extraction into the ITO.³⁵ The more slowly decaying component at longer times is dominated by nonradiative charge carrier recombination through defects,³⁶ which is slowest for the 150 °C annealing condition. Furthermore, the trend in carrier lifetimes for different annealing temperatures behavior also correlates well with the discussed QFLS and PLQY (photoluminescence quantum yield) in Figure 1D and E. This further proves the reduced nonradiative recombination for 150 °C annealed samples.

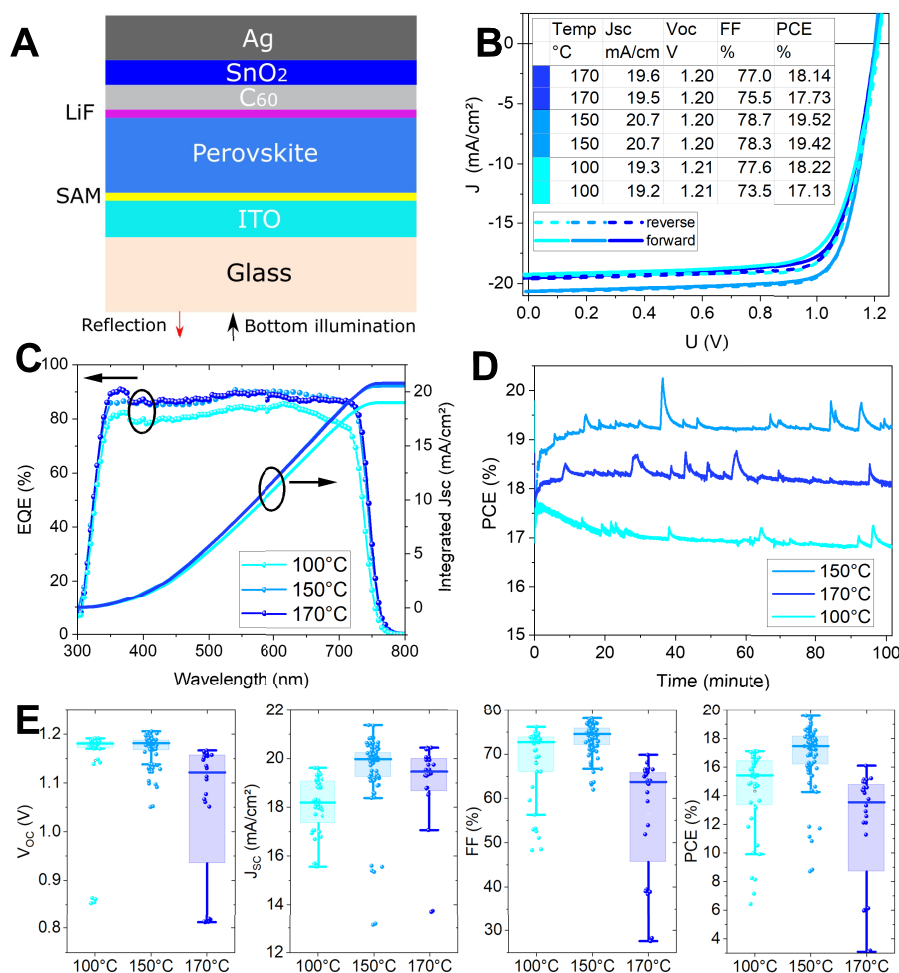


Figure 2. Solar cell characteristics of single-junctions for annealing temperatures of 100, 150, and 170 °C. (A) Schematic of the single-junction device with illumination through the glass and an active area of 0.16 cm². (B) *J*–*V* curves of the best solar cells for each annealing temperature. Solid lines: forward scan (from *V*_{oc} to *J*_{sc}); dashed lines: reverse scan. Inset table: corresponding device parameters. (C) External quantum efficiency spectra (EQE, left axis) and integrated short circuit current density (right axis) for the cells in panel B. (D) Continuous maximum-power-point (MPP) track of the best solar cells for 100 min at 25 °C. (E) Box plot graphs of characteristic solar cell device parameters for investigated annealing temperatures. The data set contains 74 solar cells in total.

However, the 170 °C annealed sample shows the fastest nonradiative recombination although it exhibits the largest domain size, which contradicts our expectation of recombination happening mostly at the grain boundaries.³⁷ To find out the reasons behind the misalignment of the grain size and PL lifetime for 170 °C annealed samples, energy-dispersive X-ray (EDX) analysis was applied to examine the compositional distribution from both cross-sectional and top-view images. The elemental images in Figure S5 reveal that Br atoms were the only chemical element that was not evenly distributed in the film. Further, we collected the averaged compositional intensity across the top surface over a large area for each composition. From this, it is apparent that the chlorine content decreases with increasing annealing temperature (see also Table S2). In contrast, the lead (Pb) concentration increases with temperature. For iodine (I), the same trend can also be observed. The EDX elemental intensity distribution (Figure S6) is composed from two parts when we investigate the whole energy scanning from 0 eV to 4 keV: low energy 0.4 keV (280 nm penetration depth), which carries a larger portion of

information for the surface, and 3.9 keV (3720 nm penetration depth), which can reflect the bulk property within a large volume. There is no clear difference for the 3.9 keV scanning region, which hints at similar bulk properties. The intensity of the 0.4 keV scanning region gives an observable difference and implies a surficial I[−] concentration difference. Thus, we conclude that the excess I[−] is mainly found on the halide perovskites' top surface.

Highly Efficient Wide Bandgap Halide Perovskite Single-Junction Solar Cells. To investigate the solar cell performance, we first compare slot-die coated single-junction solar cells with the following layer stack: either BCP (bathocuproine) or SnO₂ between the fullerene (C₆₀) and Ag (or Cu) contacts. The former is typically used for opaque p-i-n solar cells, while the latter is needed for top contacts in tandem solar cells. There are no observable performance differences. Thus, we utilize SnO₂ in single-junctions that can later serve as a buffer layer for the sputtered indium zinc oxide (IZO) electrode required for tandem application. Figure 2A shows the corresponding device configuration, and Figure 2B

Table 1. Ideality Factor (n_{id}) and Pseudo Fill Factor (pFF) for Halide Perovskite Films on Various Substrates Calculated from Intensity-Dependent PL Measurements^a

Sample	n_{id}	J_{EQE} (mA/cm ²)	pV_{oc} (V)	pFF (%)	pPCE (%)	V_{oc} (V)	FF (%)	$\Delta FF = pFF - FF$, $\Delta V_{oc} = pV_{oc} - V_{oc}$
a. SC on 2PACz/ITO	1.29	20.5	1.29	88.2	23.3	1.25	81.0	7.2, 0.04
b. SDC on 2PACz/ITO	1.27	20.5	1.26	87.2	22.7	1.22	78.0	9.2, 0.04
c. SDC on 2PACz/ITO/silicon	1.26	20.5	1.25	87.5	22.5			

^aThe halide perovskites were coated on quartz glass and ITO coated glass by spin-coating or slot-die coating. The measurement data and the curve fits are displayed in Figure S11. In addition, the best fill factor (FF) from solar cell devices and the difference between the pseudo and solar cell factors (ΔFF) and pseudo V_{oc} vs V_{oc} (ΔV_{oc}) are presented.

shows the best $J-V$ data under the investigated annealing conditions. In combination with Figure 2D (showing the maximum power point tracking) and Figure 2E (showing statistical data of the performance parameters for 74 solar cells), they reveal the overall trend and explain how the annealing temperature impacts on the full solar cell performance. The current density of the sample annealed at 100 °C shows only 18 mA/cm² on average. Although the absorption coefficients are approximately in a similar level among all annealing temperatures shown in Figure 1C, the poor charge collection from the low EQE signal for 100 °C annealed films in the complete wavelength between 350 and 700 nm proves the high recombination caused by the small grain size and, hence, large limitation, presumably due to recombination in grain boundaries.³² In Figure S15, the 11 h maximum-power-point (MPP) track also shows the instability caused by the small grain size with dramatically decreased current density. Hence, annealing at 100 °C, which has been commonly used for spin-coating, cannot be easily transferred to trigger correct crystallization under SDC and N₂-knife drying conditions. In Figure 2C, the calculated EQE derivative from 150 °C shows the bandgap at 1.68 eV, which is desirable to pair with the silicon solar cell. The 150 °C annealed sample shows the highest V_{oc} , fill factor (FF), and J_{sc} and, thus, the highest efficiency for all temperatures investigated here: the best 150 °C sample stabilized at 19.4% efficiency, with V_{oc} and J_{sc} values of 1.2 V and 20.65 mA/cm², which are the highest reported for slot-die coated 1.68 eV top cell optimized perovskite solar cells. In Figure 2D, the MPP tracks measured for 100 min in inert atmosphere are shown. These stability tests also demonstrate that 150 °C is the optimal temperature for annealing with no difference between the $J-V$ scan and the MPP track. The spikes in the MPP track are caused by sudden changes of the glovebox pressure during operation and do not reflect a sample characteristic. The 300 h shelf-lifetime also demonstrates the good stability in Figure S16. When the annealing temperature increases from 150 °C further to 170 °C, the V_{oc} and FF decrease significantly. The $J-V$ curve in Figure S12 demonstrates that this is caused by higher series resistance. The accumulated PbI₂ platelets might provide high resistance and strong recombination pathways, as we have seen from the absolute and transient PL above. It is conceivable that, when the 170 °C samples undergo two subsequent $J-V$ scans with a waiting time of 2 min light soaking, the ions accumulate and build up between the deteriorated perovskite/C₆₀ interface as an additional internal field which hinders the carrier extraction and, subsequently, the J_{sc} is reduced, as seen in Figure 2B.³⁸ The XRD measurement which has been discussed previously can give a deeper interpretation. Although the general Pb²⁺ and I⁻ distribution is kept even in the bulk layer (Figure S7), the surface-near PbI₂ platelets are the most probable reason for the higher series resistance although there

is no excess PbI₂ involved in the preparation stage in our fabrication. Meanwhile, the V_{oc} reduction is in line with the 40 mV reduction of the QFLS which we have discussed in the last section, although the bandgap only decreases by 10 mV in the comparison between the 100 and 150 °C annealing conditions.

The best slot-die coated single-junction solar cells developed here with annealing at 150 °C reach FF values around 78%, while spin coated halide perovskites of the same composition can reach FFs above 81%.¹⁷ To better understand the limited FFs of slot-die coated solar cells, we prepared three kinds of samples on the quartz glass: spin coated 3cation¹² perovskites as reference, spin coated 3halide perovskites (3halide SC), and slot-die coated 3halide perovskites (3halide SDC). The aims are to compare both the coating methods and two recipes to evaluate the halide perovskites quality, interface-near and bulk defects, and its impact on PL behavior. We measured trPL and found that the spin coated 3halide film shows the longest carrier lifetime of close to 3 μ s, as seen in Figure S13. Although the spin coated 3halide shows smaller grain size than the slot die coated 3halide, as seen in Figure S14, photogenerated carriers in the slot-die coated 3halide film decay with a lifetime of only 0.7 μ s. This obvious difference in PL lifetime between spin and slot die coated 3halide films can be interpreted by the limited bulk quality and can explain the lower FF for slot-die coated samples.

From another viewpoint, the diode ideality factor (n_{id}) and potential fill factor (pFF) are widely used to evaluate the dominant recombination pathways in semiconductors.^{39,40} We further analyzed various films from spin-coating and slot-die coating on ITO and ITO/silicon substrates with intensity-dependent PL to extract pseudo fill factors.³⁹ In Table 1, data for these metrics are collected from intensity-dependent PL measurement, according to fits shown in Figure S11. Comparing spin-coating and slot-die coating (Table 1, a vs b), the halide perovskites film shows almost no difference in ideality factor, with values below 1.3 indicating a dominance of Shockley–Read–Hall recombination through traps and defects within the band gap, while the PLQY of SC halide perovskites is still higher than the SDC one which leads to the higher pV_{oc} . Comparing the coating techniques between SC and SDC, the existence of PbI₂ suggests a potentially stronger nonradiative recombination with lower photoluminescence property. When we compare the ΔFF values, which are derived by the pFF and real solar cell $J-V$, it can clearly illustrate the different halide perovskites films induced by the spin-coating and slot-die coating methods. The ΔFF values reflect that the series resistance is still higher for the slot-die coated film, which aligns with our previous discussion. Further investigation is still necessary, and this also highlights that those potential halide perovskites film improvements need to be studied with the slot-die coating process presented here for further optimized

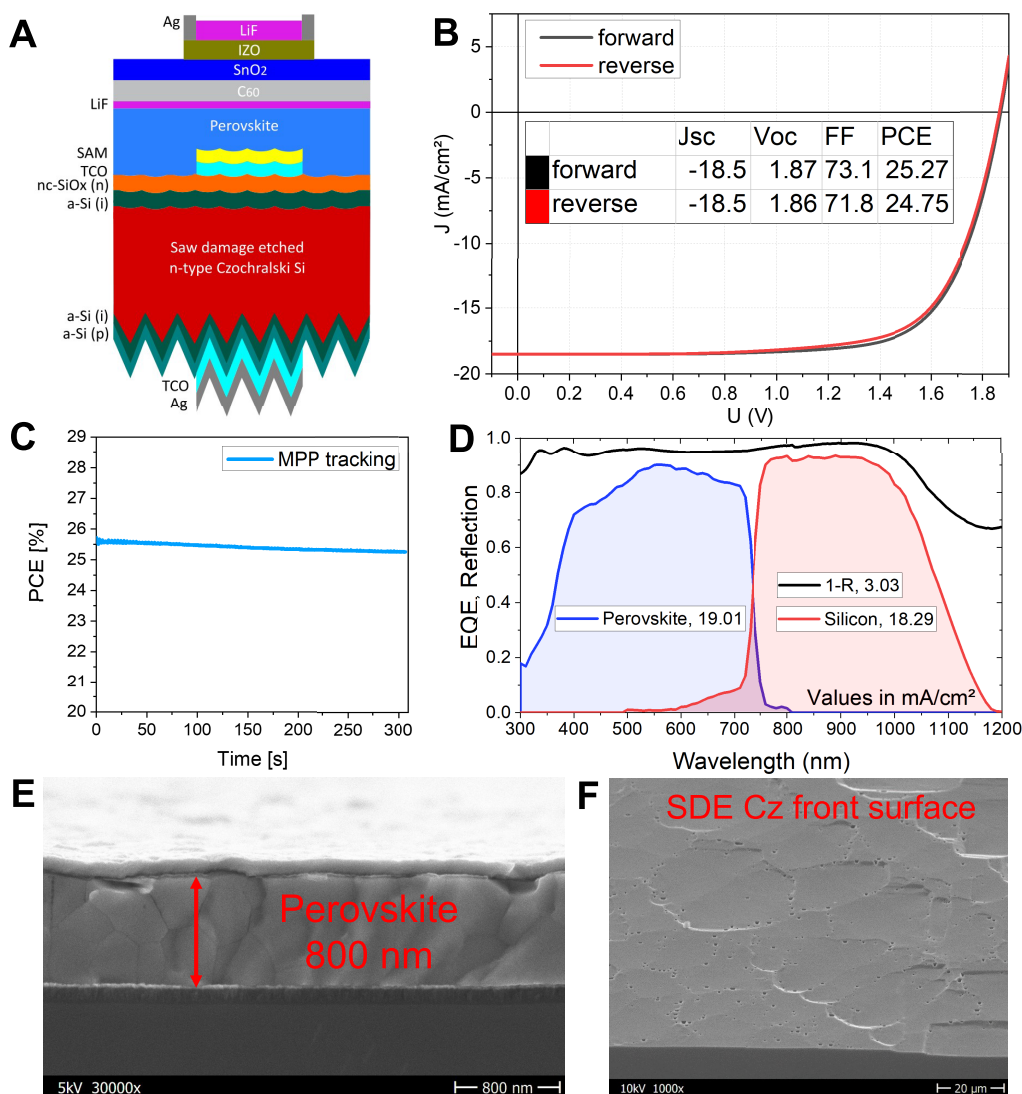


Figure 3. (A) Schematic of the two-terminal monolithic tandem layer stack developed in this work on a 120 μm saw damaged etched Czochralski silicon wafer with a rough front surface. The detailed layer stack is described in the [Supporting Information](#). (B) J - V curves of the champion slot-die coated perovskite/silicon tandem solar cell. Red line: reverse scan (from V_{oc} to J_{sc}); black line: forward scan. (C) MPP tracking over 300 s with stabilized PCE at 25.2%. (D) EQE and UV-vis (1-R, black line) measurements for the tandem solar cell. (E) Cross-sectional SEM images of a slot-die coated top cell. (F) 30° tilted SEM image of a saw damaged etched Czochralski silicon wafer front surface.

crystallization. When the substrates have been compared between ITO glass and ITO coated silicon substrates (Table 1, b vs c), no obvious difference exists for the perovskite film apart from the lower photoluminescent emission. In the PL setup, the illumination occurs on the perovskite side of the sample. A white reflectance holder for substrates is utilized as the same function of the integrate sphere to reflect the PL signal from the ITO glass or silicon substrates. Therefore, the lower PL signal on the substrate is likely caused by the reabsorption of the silicon bottom cell.⁴¹

Finally, 150 °C annealing is the optimal condition for SDC 3halide perovskites, and it will be further utilized in the tandem solar cell section.

Integration of Slot-Die Coated Triple-Halide Perovskites into Silicon-Based Tandem Solar Cells. We now turn to the integration of slot-die coated perovskites top cells on silicon bottom cells for two-terminal (2T) monolithic tandem solar cell integration. During slot-die coating of silicon cells, it is unavoidable that the perovskite precursor solution

flows around the wafer's edge and reaches its back side, leading to destruction, indicated by discoloration of the Ag rear contact and decreased cell performance. Therefore, to improve the reproducibility of slot-die coated tandem solar cells, we investigated the reaction between the halide perovskites solution and the back contact materials in detail. The results are presented from [Figure S19](#) to [Figure S21](#) in the Supporting Information. Briefly, it appears that the lead halide solution reacts with the Ag electrode to form silver halides,⁴² which are nonconductive and difficult to dissolve. To avoid these reactions and improve reproducibility, 25 nm SiO_x has been added on the back side of the silicon wafer before slot-die coating to protect the back silver electrode. After the whole fabrication process, 1% HF solution dipping has been utilized to remove the SiO_x protection layer. With this, we could utilize the wide bandgap 3halide perovskites in tandem solar cells and investigate the losses in comparison with the standard spin coated tandem solar cell. In [Figure S21C](#), a direct comparison of tandem cell performance before HF cleaning and after the

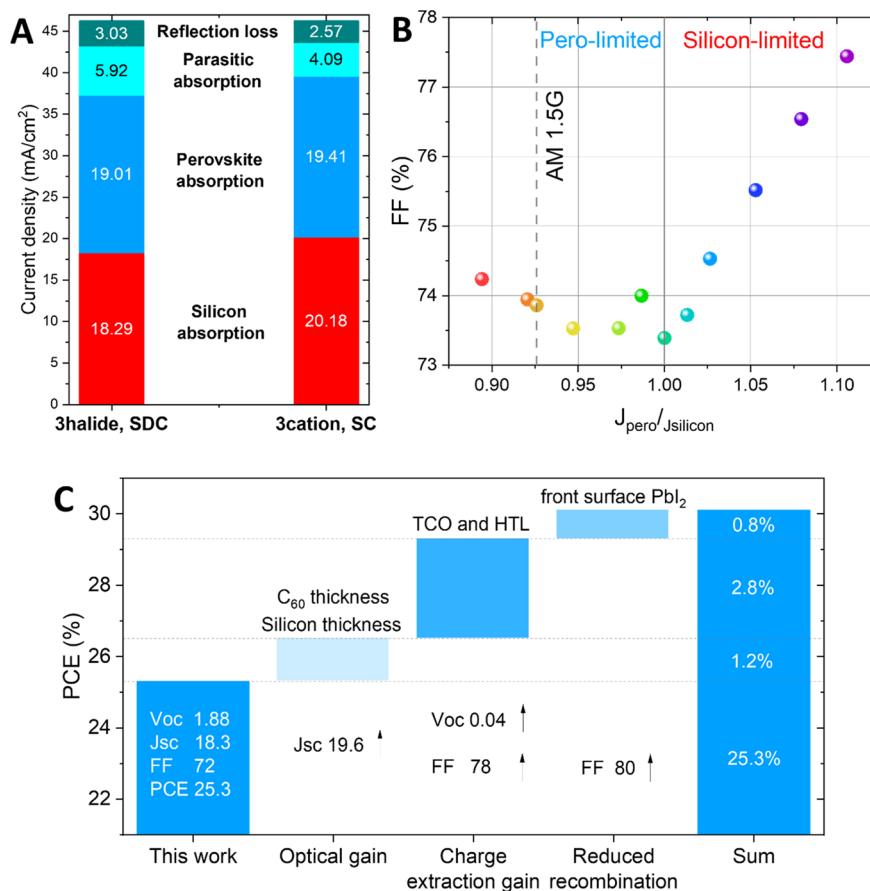


Figure 4. Efficiency loss analysis of slot-die coated perovskite silicon tandem solar cells. (A) Summary of current density losses in SDC perovskite/silicon tandem solar cell (this work) as extracted from Figure S18 in comparison to reported champion values using spin-coating and nonindustrial thick wafers.¹² (B) Fill factor as a function of illumination with varied spectrum (red-rich to blue-rich) to induce current mismatch and thus determine the fill factor limitation of the bottom and top cells.⁴³ (C) Loss analysis and roadmap toward 30%: a summary of optical and electrical losses for both the perovskite top and silicon bottom cell and the feasible solutions for further studies.

HF cleaning process is shown. Before the cleaning process, a degraded silver electrode with silver halide residues is attached directly on the metal chuck. After the cleaning process, additional silver metal paste is brushed on the cleaned region.

A schematic illustration of the slot-die coated tandem devices is shown in Figure 3A. The best tandem solar cell performance using the industrial, thin Cz wafer is shown in Figure 3B. This tandem solar cell reaches a PCE of 25.2% (V_{oc} = 1.87 V, J_{sc} = 18.5 mA/cm², and FF = 73.1%) with an active area of 1 cm² (see photographs of the active area layout in Figure S17). The corresponding stabilized PCE from the MPP tracks after 300 s is 25.2%, as shown in Figure 3C. To the best of our knowledge, the V_{oc} of 1.87 V is the highest yet reported value in a perovskite/Si tandem with a non-spin-coated perovskite absorber.

The tandem EQE spectra shown in Figure 3D validate our measured current density from the J - V curves (Figure 3B). In Figure 3D, the integration of the tandem EQE spectra gives a 19.01 mA/cm² photocurrent for the perovskite absorber and a 18.29 mA/cm² photocurrent for the silicon absorber. In order to have a clear comparison of the optical losses, in Figure S18, the spin coated 3cation on a thick FZ wafer is utilized as the reference EQE, which is deduced from ref 12 and compared in detail below. As seen in Figure 3F, the surface roughness of the SDE Cz bottom cell is rather large; however, the slot-die

coating process has successfully covered the rough SDE surface when using an absorber thickness of over 800 nm (Figure 3E).

The best tandem solar cell developed in this work is still performing worse than the best reported tandem cells produced by spin-coating to fabricate the halide perovskites on thick floatzone (FZ) wafers. In order to shed light on the reasons for this difference, we perform a loss analysis here. In Figure S18, a direct comparison is made between the spin coated halide perovskites (so-called “3cation” with the following composition: Cs_{0.05}(FA_{0.77}MA_{0.23})_{0.95}Pb(I_{0.77}Br_{0.23})₃) on FZ wafers with a thickness of 250 μm¹² and the best slot-die coated 3halide perovskites on 120 μm thin Cz silicon bottom cells from this work. There are three main distinct differences: First, with 23 nm C₆₀ for the SDC tandem instead of 18 nm C₆₀ for SC, parasitic absorption between 300 and 600 nm is higher, as discussed previously.¹⁷ This C₆₀ thickness used here is needed for enhancing solar cell stability and to make sure to cover this nonflat perovskite surface more conformally. Second, the thicker SDC halide perovskites film has a higher photocurrent and, with that, less transmission to the silicon bottom cell. As seen in the wavelength range from 600 to 730 nm, the halide perovskites gained 0.28 mA/cm² photocurrent as compared to the spin coated film due to the 800 nm slot-die coated halide perovskites layer compared with the 600 nm spin coated layer thickness. Third, the insufficient absorption in the NIR wavelength (900–1200 nm) regime is

caused by the thin silicon wafer.⁴⁴ In addition, the detailed comparison for the two investigated layer stacks in Figure 4A and Figure S18 shows that parasitic absorption, being the difference between (1-reflection) and the sum of the perovskite and silicon EQE, is higher for the SDC cell, which could originate from higher absorption within the contact layers (the thicker C_{60} thickness with higher absorption). In the NIR range, the difference originates likely from the transparent conductive oxide (TCO) layers or the silicon bottom cell. Overall, by increasing the silicon wafer thickness of the bottom cell to above 200 μm or applying suitable front surface texture for light trapping and reduced parasitic absorption, we estimate a current matching value at around 19.6 mA/cm^2 , which would further enhance the PCE of the slot die coated tandem solar cell by 1.2% absolute (see Figure 4B).

In addition to the optical losses, we have also analyzed the FF limitation by measuring the cell performance under different spectral illumination conditions while keeping the same overall intensity as shown in Figure 4B. The measurement method has been discussed previously.⁴³ Although the characterized sample shows lower perovskite photocurrent due to thin perovskite thickness, the mismatch trends still explain the tandem FF limitation. The generated current ratio has been utilized to saturate either the perovskite top cell or silicon bottom cell so that the specific subcell can no longer be FF limiting under specific illumination conditions. By increasing the infrared illumination for the silicon subcell, the perovskite-limited region reflects the perovskite dominated FF. On the contrary, by increasing the blue illumination for the perovskite subcell, the perovskite-limited region reflects the silicon dominated FF. With the asymmetrical FF shape of controlled current mismatch, it can be concluded that the FF limitation of the top cell still needs to be considered as an overall tandem FF limitation. Thus, the tandem FF of the best slot-die coated perovskite silicon tandem solar cell presented here is still limited by the perovskite top cell. As we have described here in the last section, the low charge carrier lifetime and PbI_2 accumulated between the perovskite/ C_{60} interface are likely the root issues to be investigated in a further study in more detail to improve the top cell fill factor further.

With the full tandem stack, we perform intensity-dependent $J-V$ ($\text{suns}-V_{\text{oc}}$) measurements,³⁹ which are summarized in Figure S22. A pseudo fill factor of 80% is measured which forecasts the potential tandem performance. In comparison with the recently published 84% pFF of perovskite/silicon tandems constructed by selective electroluminescence (EL) measurements,³ the 4% pFF difference can be explained by limitations of the intrinsic perovskite bulk material which needs to be further improved with the targets of long lifetime and potential shunts reduction. In comparison between the $\text{suns}-V_{\text{oc}}$ and $J-V$ measurement in Figure 3B with 8% FF difference (averaged between the forward and reverse scans) and by the slopes of the V_{oc} intersect point, significant serial resistance and transport losses are still present in slot-die coated perovskite/silicon tandem solar cells presented here.

Higher fill factors could potentially be achieved when replacing the SAM molecule 2PACz with Me-4PACz,¹² which has been known to improve charge extraction. The successful deployment of Me-4PACz for SDC 3halide could give significant PCE improvement by higher FF. Assuming the same improvements in FF here for slot die coated tandems and being able to transfer the single-junction V_{oc} into the tandem solar cells, a gain of 2.8% absolute efficiency is feasible.

Finally, the perovskites and electron transport layer (ETL) interface with accumulated PbI_2 induce strong recombination loss and likely high resistance which should be investigated and be avoided in future research by proper interlayer or passivation strategies. Getting rid of this layer can further improve the FF to 80%, which has been proven for the spin coated 3halide single-junction (Figure 4C). Assuming a final tandem solar cell V_{oc} of 1.92 V, potentially 30.1% PCE could be generated by optimizing the slot-die coated top cell with our current knowledge. Further research and development, especially regarding the perovskites/ C_{60} interface loss, will further improve the realistic efficiency potential and thus close the gap between slot die coated and spin coated perovskite/silicon tandem solar cells in the future.

We report on an efficient, wide bandgap triple-halide perovskite, with the composition of $(\text{Cs}_{0.22}\text{FA}_{0.78})\text{Pb}(\text{I}_{0.85}\text{Br}_{0.15})_3 + 5 \text{ mol } \% \text{ MAPbCl}_3$, specifically for slot-die coating by developing a suitable solvent system. We demonstrate that, with these halide perovskites, our fabrication route enables a bandgap of 1.68 eV, which is suitable for tandem solar cells and without the phase segregation that is typically observed for high Br loadings. The slot-die coated wet perovskite films were dried using a stream of nitrogen (N_2) from an “ N_2 -knife” with high reproducibility and avoiding the need to use antisolvents. We find parameters allowing crystallization of the perovskite film into large grains reducing charge collection losses and, thus, enabling higher current density for the tandem solar cell. By annealing at 150 $^\circ\text{C}$, an optimized trade-off between crystallization and the detrimental formation of PbI_2 aggregates on the film's top surface is found. Thus, we improve the cell stability and performance of halide perovskites single-junction cells toward a stabilized power output of up to 19.4%, and a two-terminal monolithic tandem solar cell with a PCE of 25.2% on a 1 cm^2 active area is demonstrated with fully scalable processes. This tandem efficiency is reached for wafers with a thickness of around 120 μm , not yet enabling the full photocurrent potential in the NIR wavelength regime compared to thicker FZ wafers ($>250 \mu\text{m}$) that are typically used in the literature. Finally, we show a detailed comparison between spin coated and slot-die coated perovskite films. For the solar cells, we present the loss mechanisms as well as guidelines for further improving the printed films. With that, we highlight the high potential for slot-die coating as a fabrication route for scalable and industrially relevant perovskite/silicon tandem solar cells.

■ ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acsenergylett.2c01506>.

Experimental procedures, material and device fabrication, device characterization, PL spectra, XRD and GIXRD data, EDX of surface and cross section, intensity-dependent PL measurements, trPL measurements, SEM, PCEs for aged devices, TLM measurements, rear side protection, and intensity-dependent pseudo $J-V$ measurement (PDF)

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Author Contributions

S.A. and K.X. planned the experiments, coordinated the work, and prepared the figures; K.X. fabricated the perovskite single-junction and perovskite/silicon tandem cell and performed the photovoltaic characterizations (J – V , EQE, UV–vis, SEM, suns- V_{oc}). Z.W.P. fabricated and optimized the silicon bottom cells and optimized the silicon part. K.X. and A.A.-A. conducted and analyzed the PL experiments. E.K. helped to optimize the tandem devices. H.H., J.A.M., F.A., and J.H.Z. conducted and analyzed the PL experiments with pseudo J – V and FF- V_{oc} loss analysis (intensity-dependent V_{oc} and QFLS). P.T. conducted the optical simulation. O.S. advised on perovskite crystallization monitoring. J.D. advised on slot-die coating. D.A.-R. and F.R. conducted the EDX measurement. S.A., La.K., and E.U. supervised the projects. All authors contributed to data interpretation and manuscript writing.

Notes

The authors declare no competing financial interest.

Data and Materials Availability. All data are available in the main text or the Supporting Information.

ACKNOWLEDGMENTS

We thank M. Gabernig, C. Ferber, S. Severin, T. Lušky, H. Heinz, K. Jacob, and C. Klimm from Helmholtz-Zentrum Berlin (HZB) for technical assistance. Special thanks go to P. Wagner, who fabricated the silicon bottom cells for this study. Intensity-dependent PL measurements were carried out at University of Potsdam with the support of F. Lang and M. Stolterfoht. Funding was provided by the Federal Ministry of Education and Research (BMBF) through Young Investigator Group Perovskite Tandem Solar Cells within the program “Materialforschung für die Energiewende” (grant no. 03SF0540) as well as the project PEROWIN (grant no. 03SF0631) and by the Helmholtz Association within the projects HySPRINT Innovation lab and the EU Partnering project TAPAS. E.U. and J.D. acknowledge financial support from the Federal Ministry of Education and Research (BMBF) in the Program “NanoMatFutur” (grant no. 03XP0091). We further acknowledge HyPerCells, a joint graduate school of the University of Potsdam and the Helmholtz-Zentrum Berlin. In addition, we acknowledge the Federal Ministry for Economic Affairs and Energy (BMWi) for funding the project PrEsto (grant no. 03EE1086C).

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