

Fig 1A

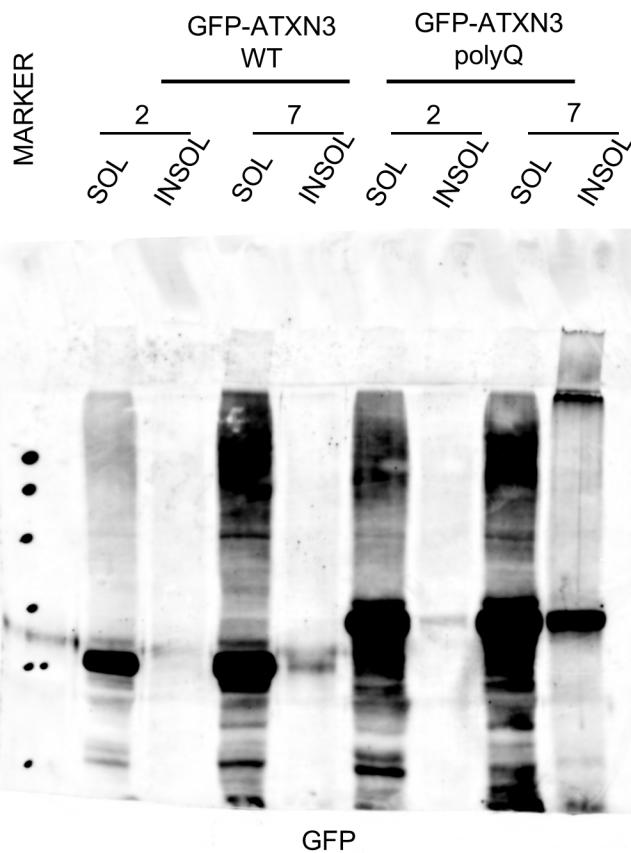


Fig 1B

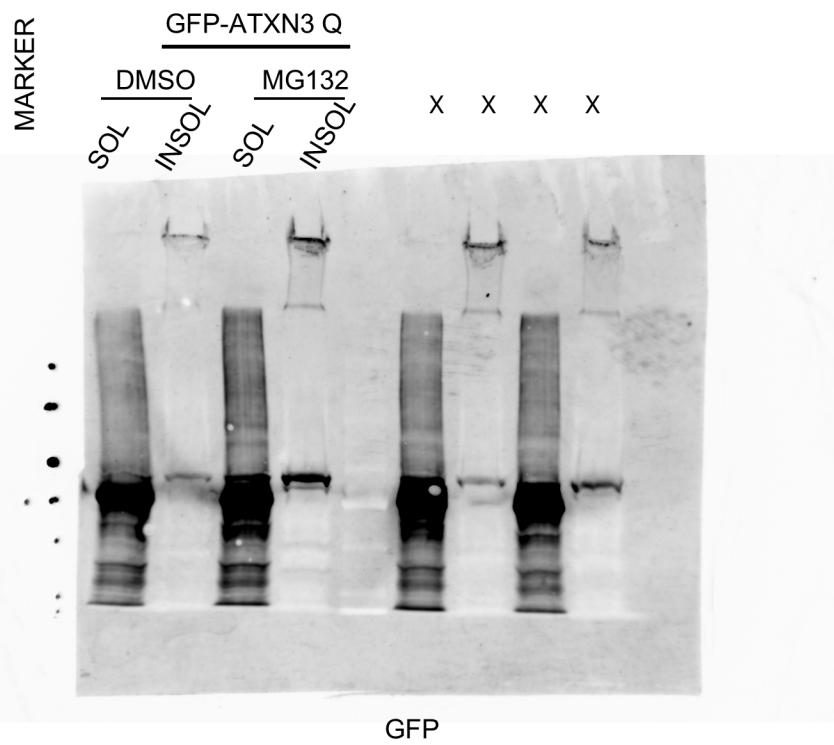


Fig 2A

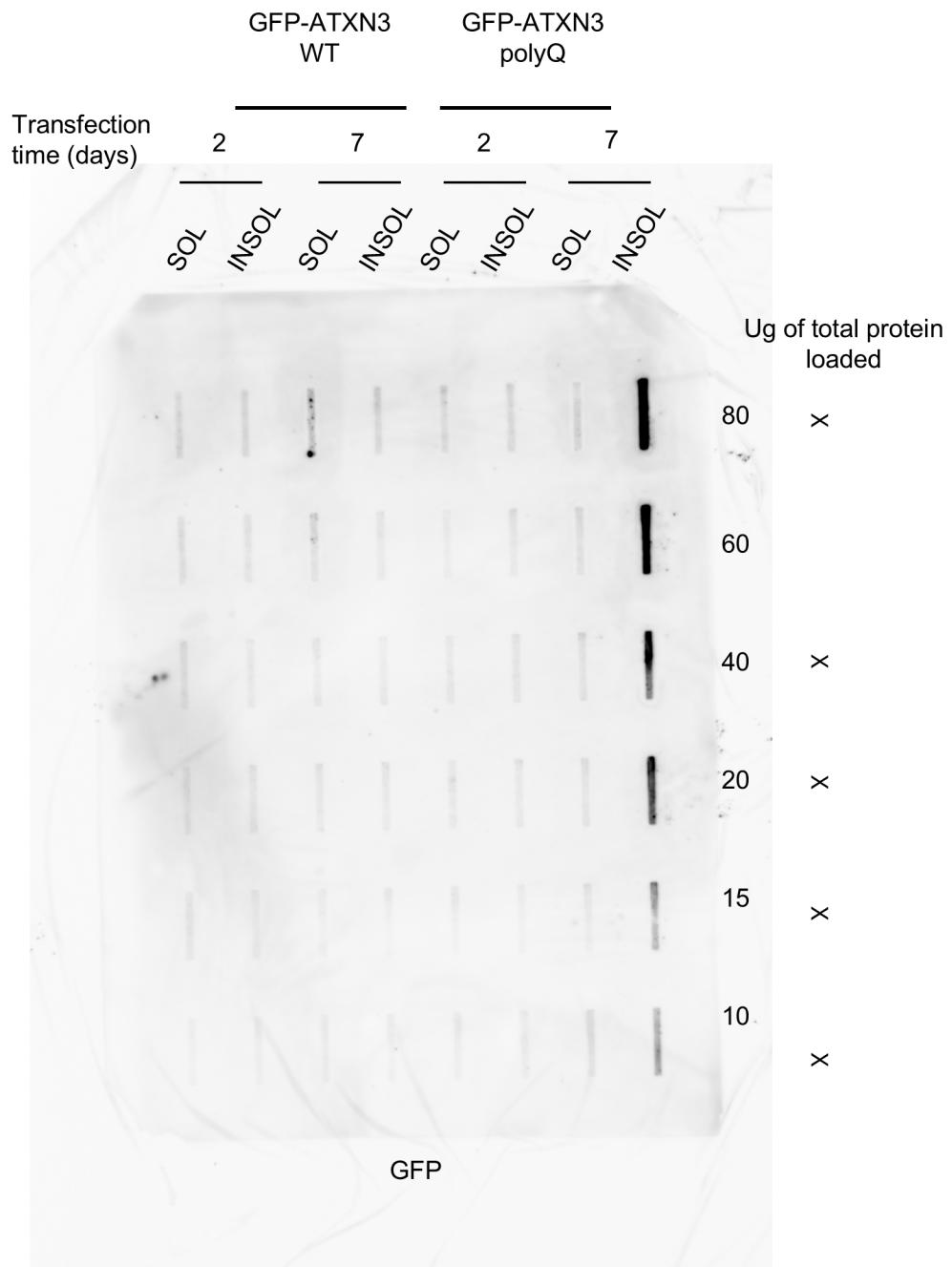


Fig 2B

GFP-ATXN3  
polyQ

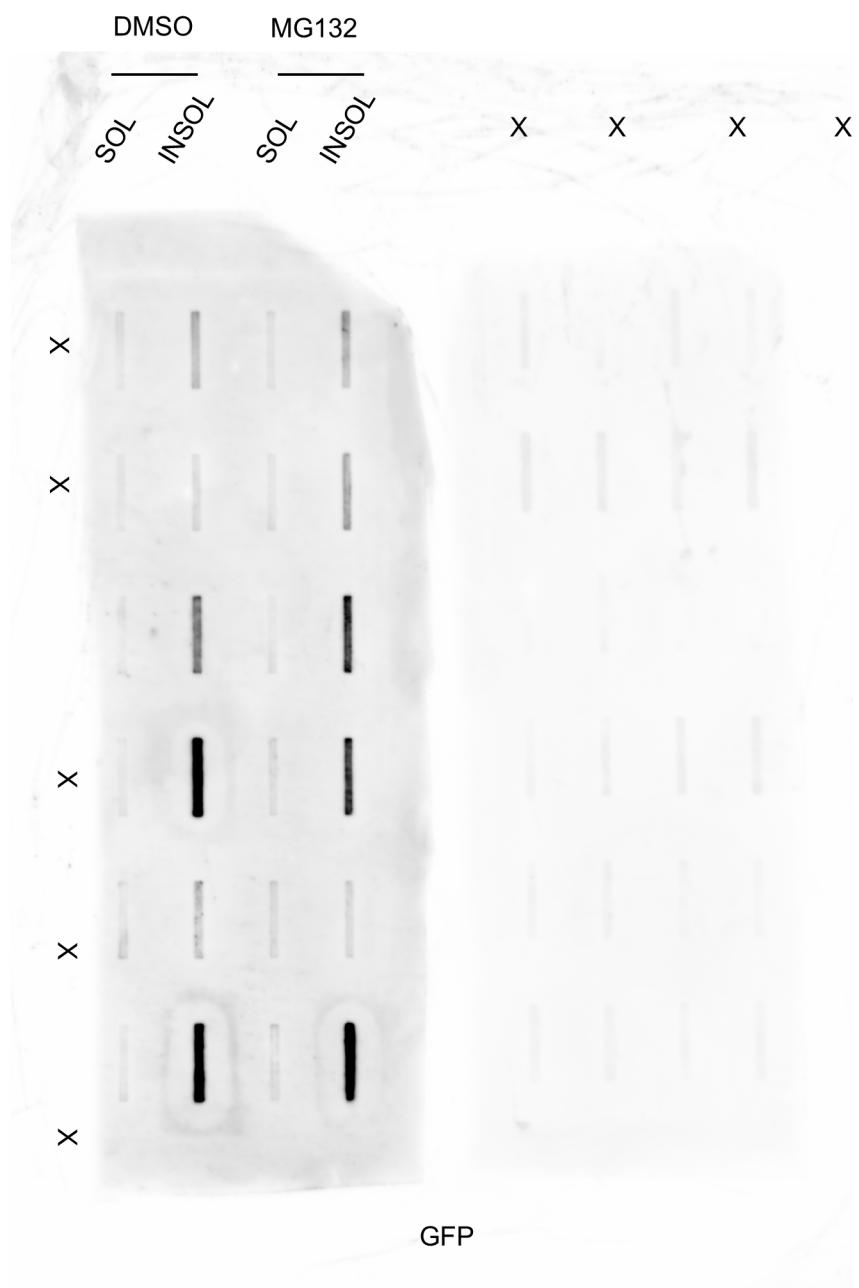


Fig 3A

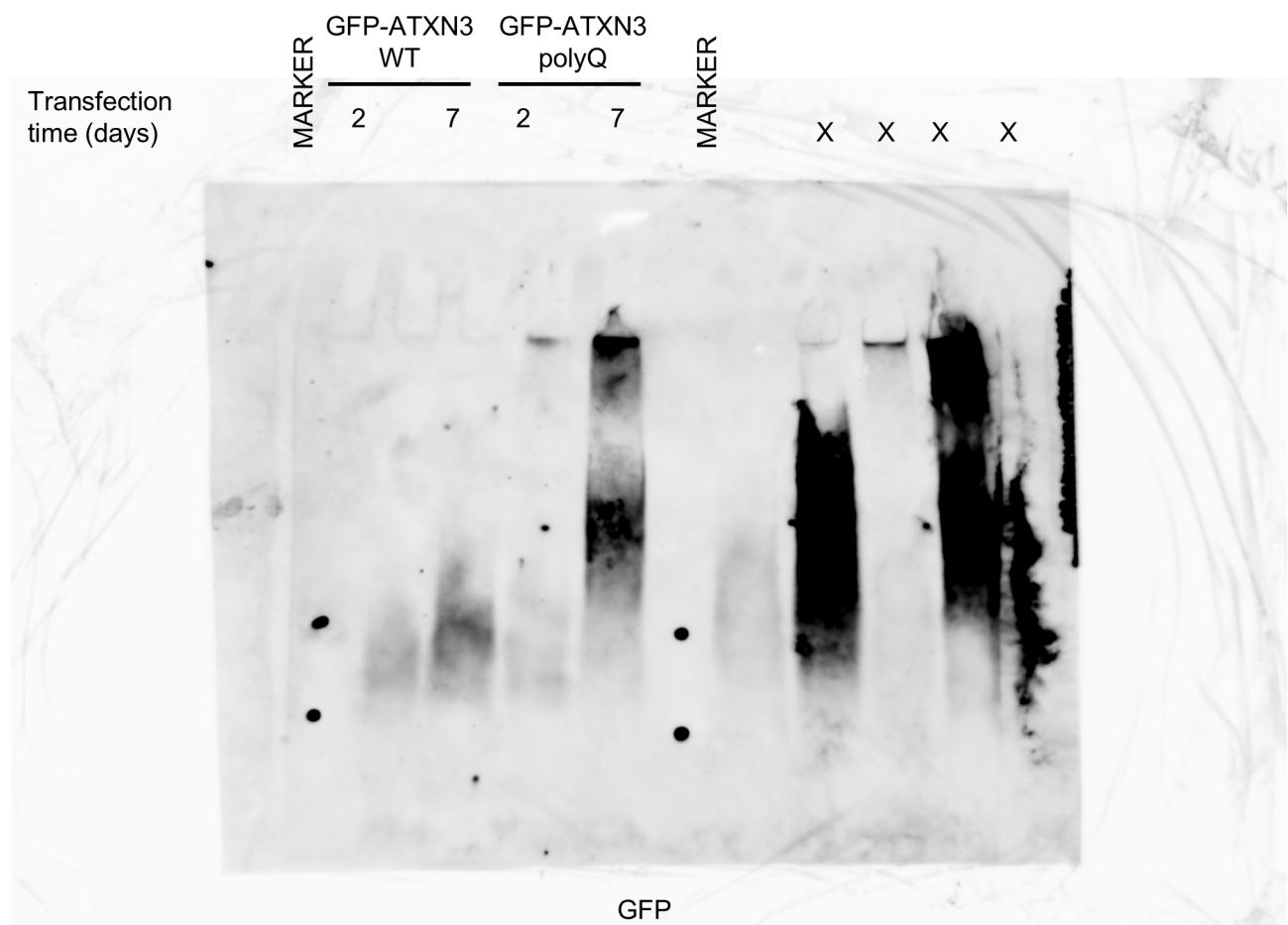
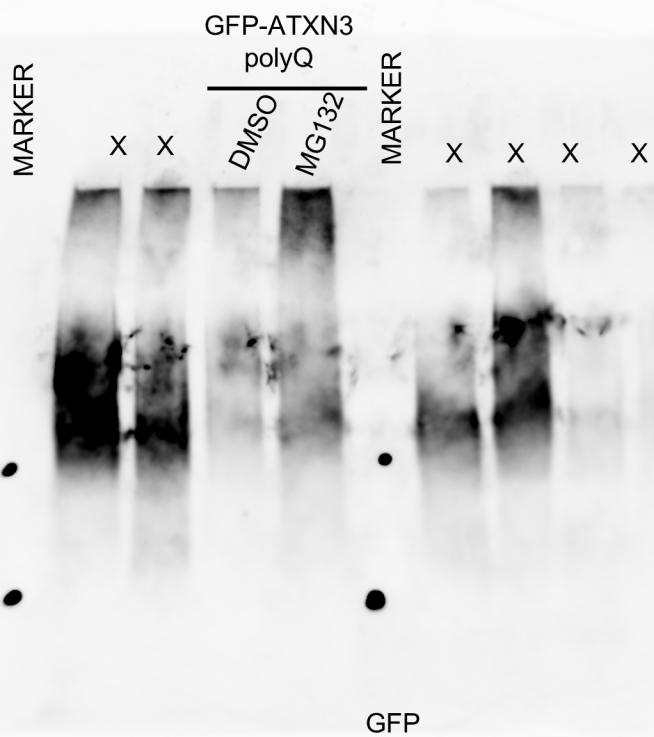
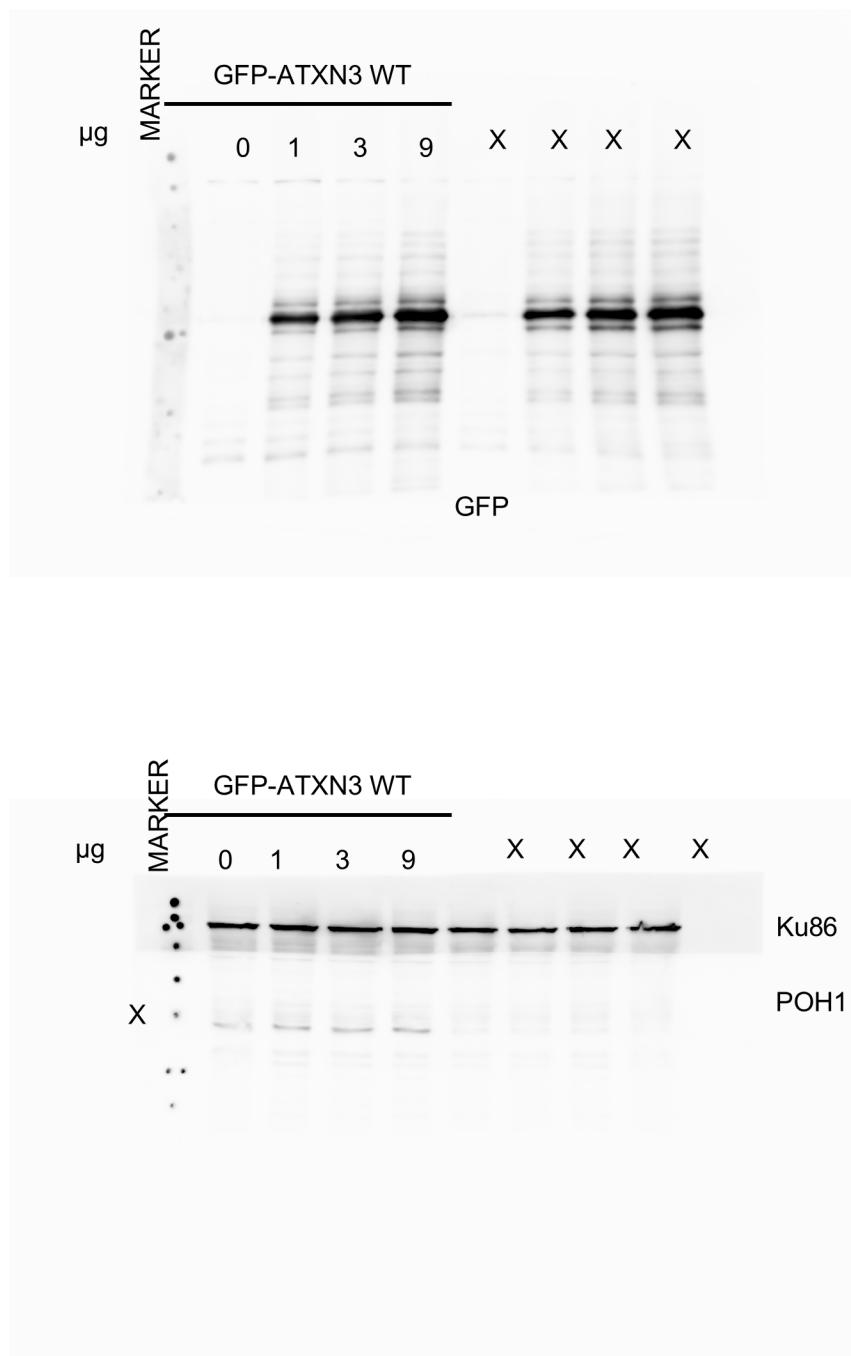


Fig 3B

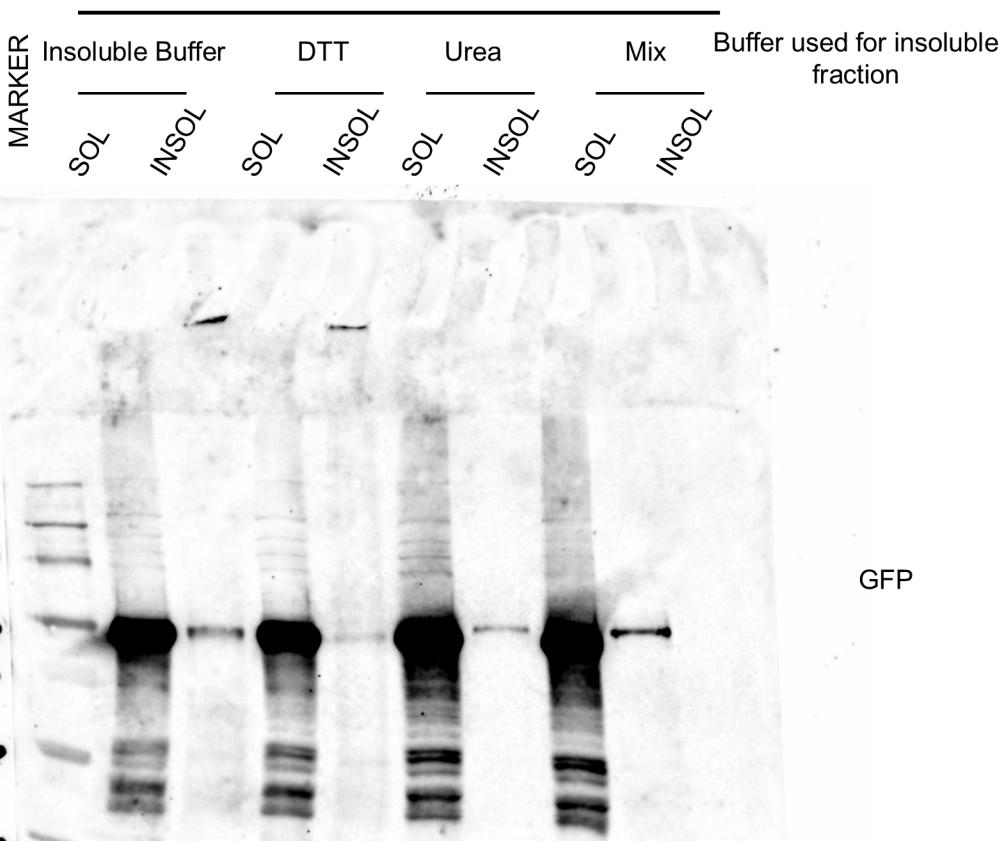


# S1 FigA

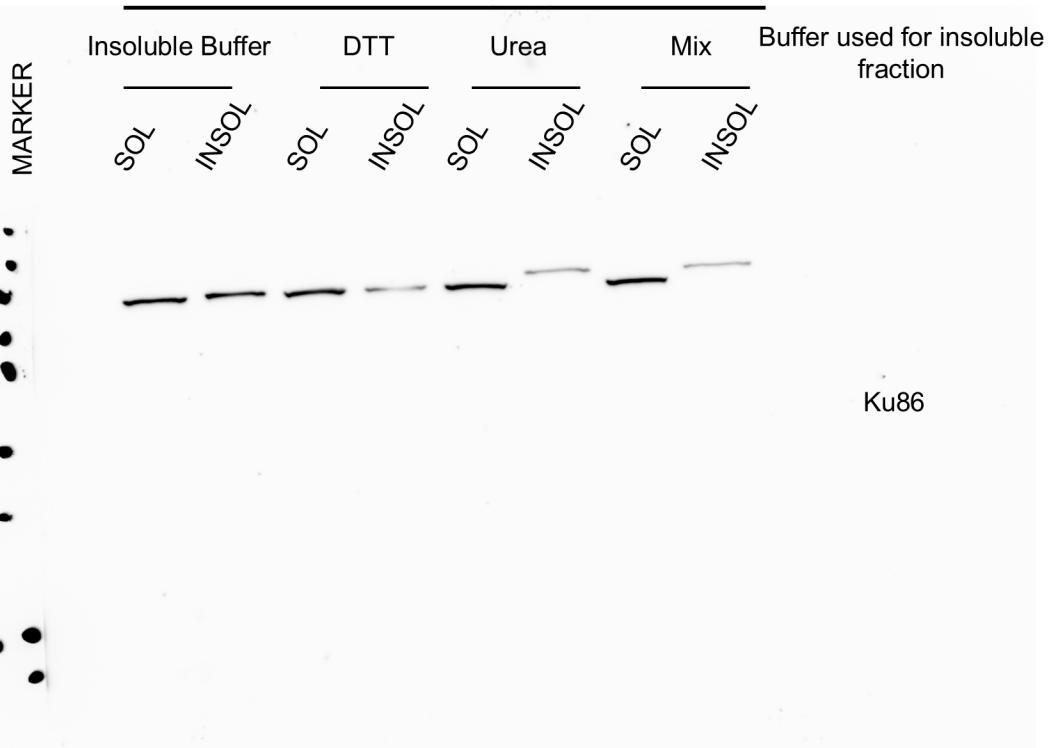


# S1 FigB

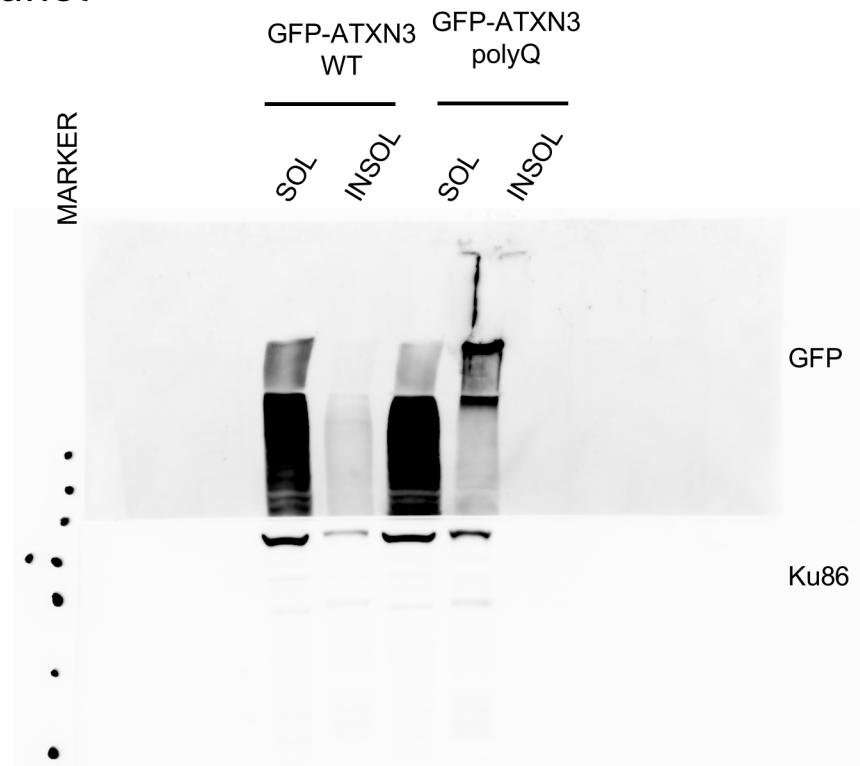
## GFP-ATXN3 polyQ



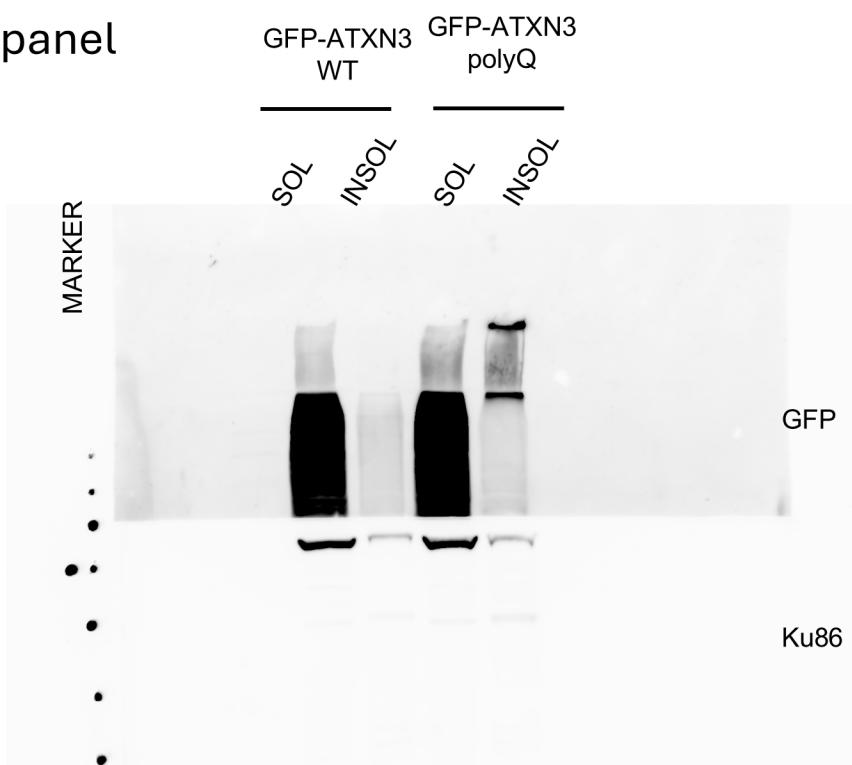
## GFP-ATXN3 polyQ



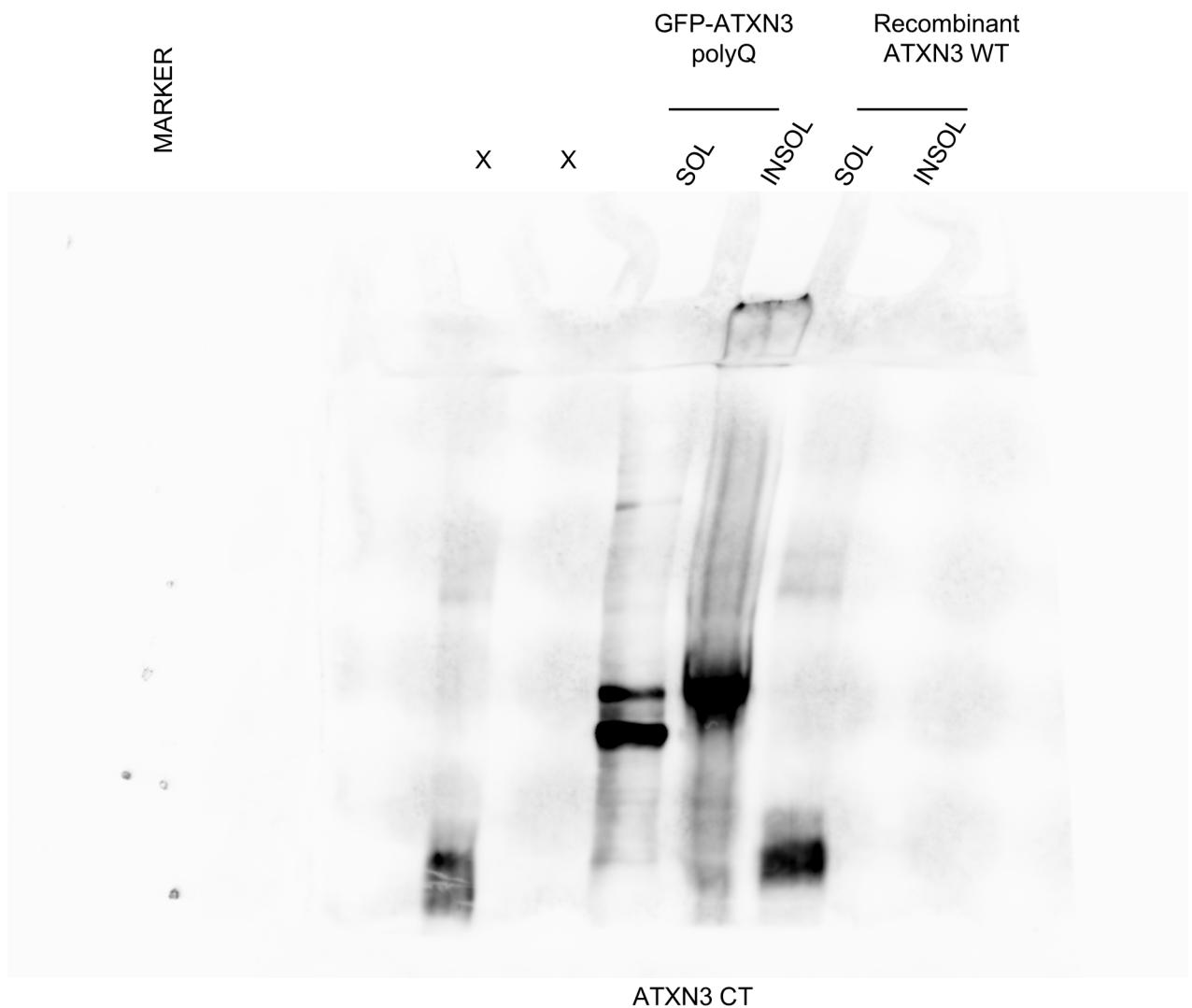
S1 FigC left panel



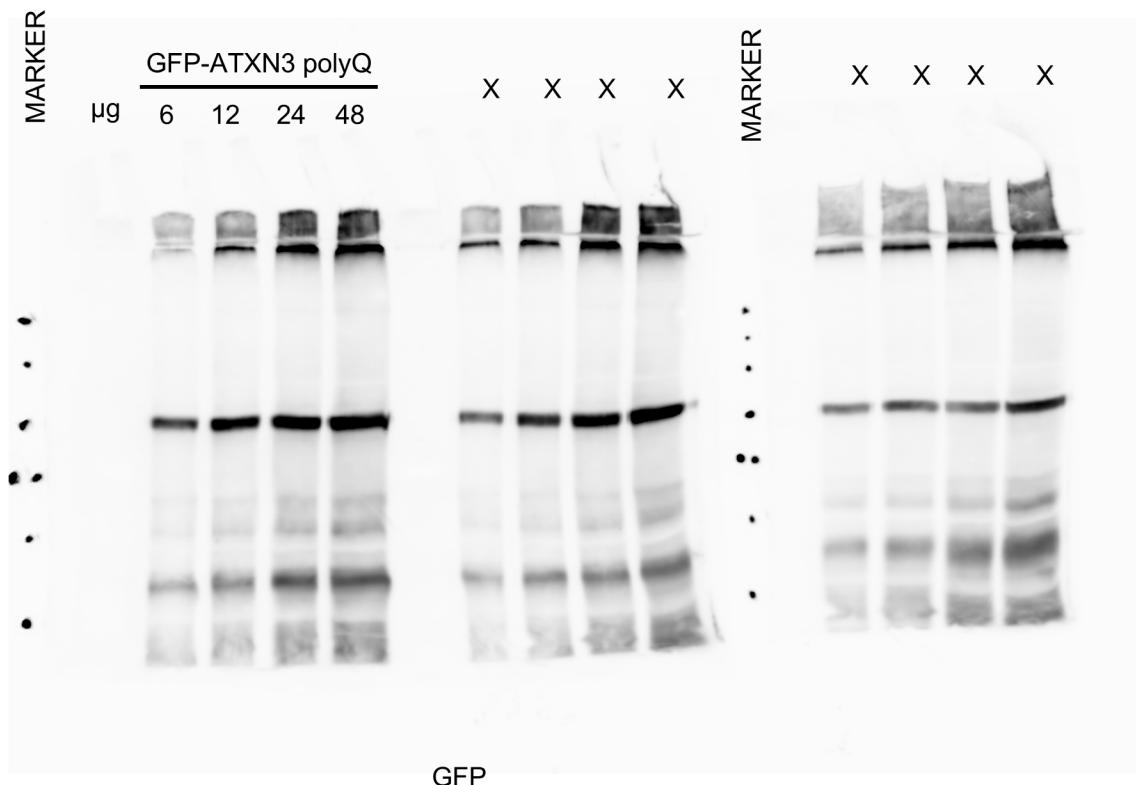
S1 FigC right panel



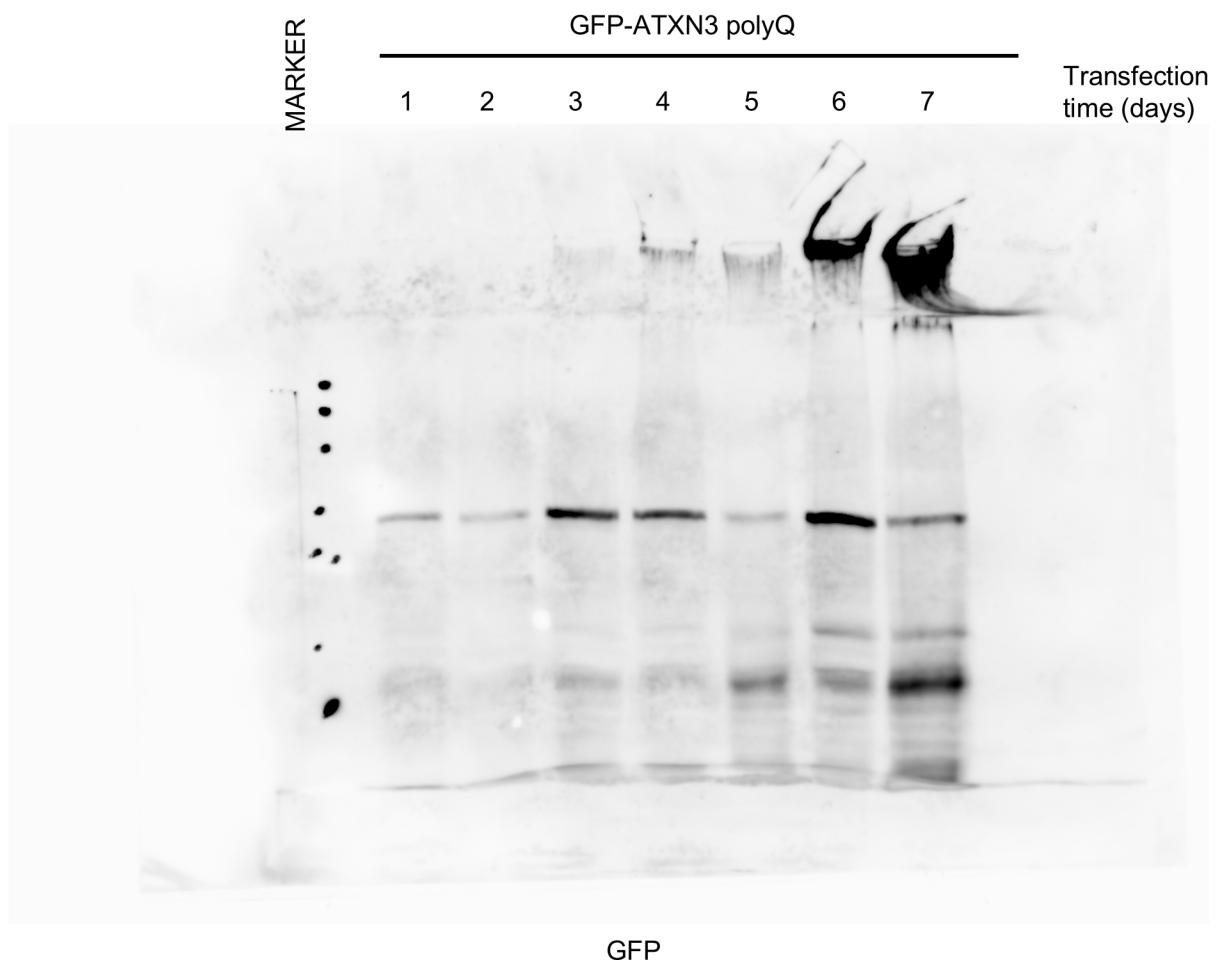
# S1 FigD



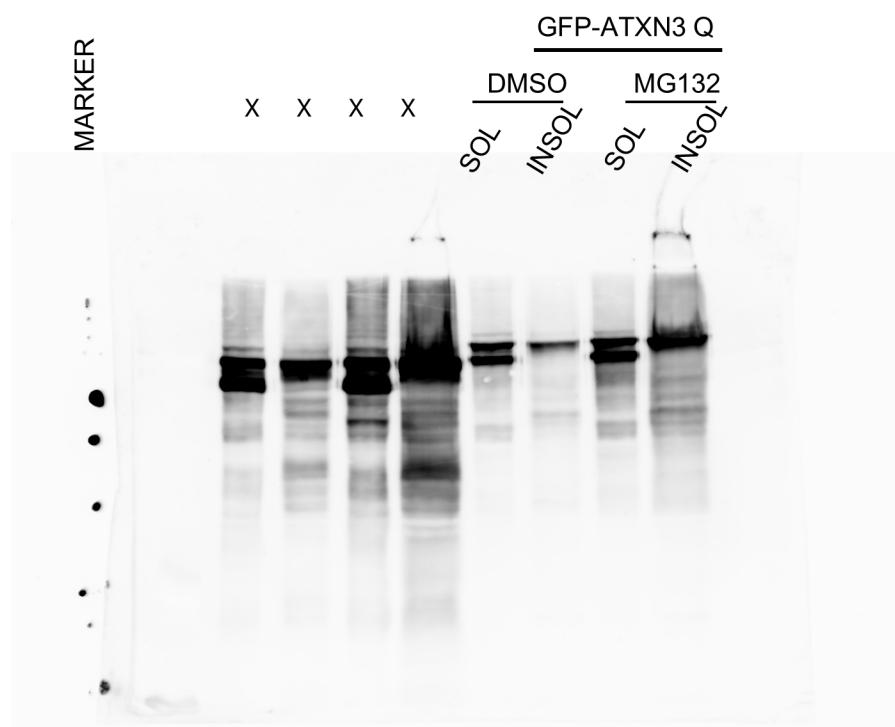
# S1 FigE



## S2 FigA



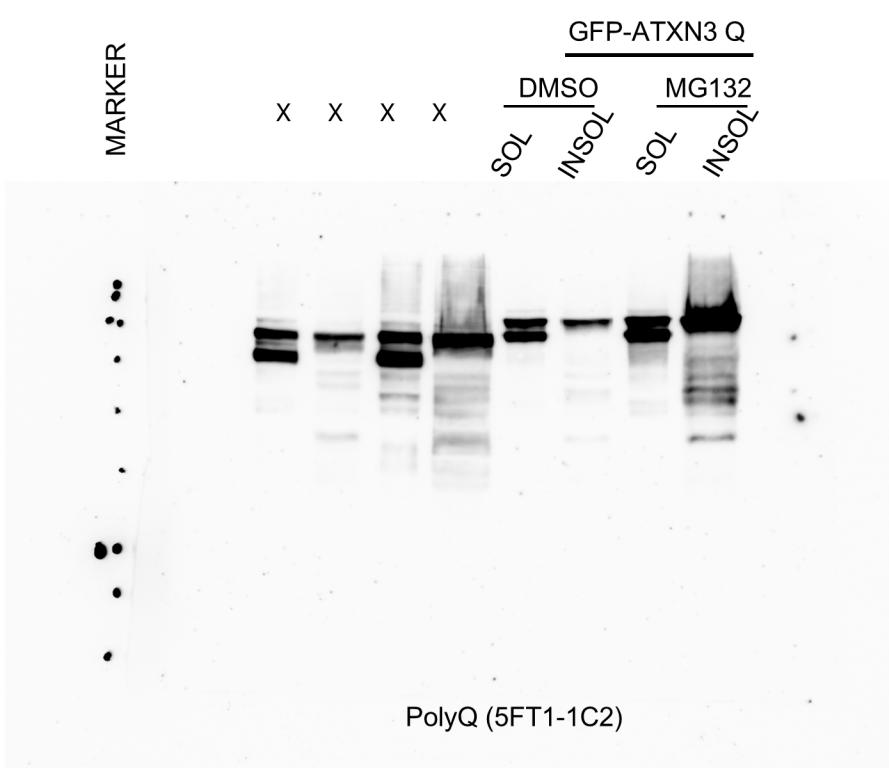
## S2 FigB left panel



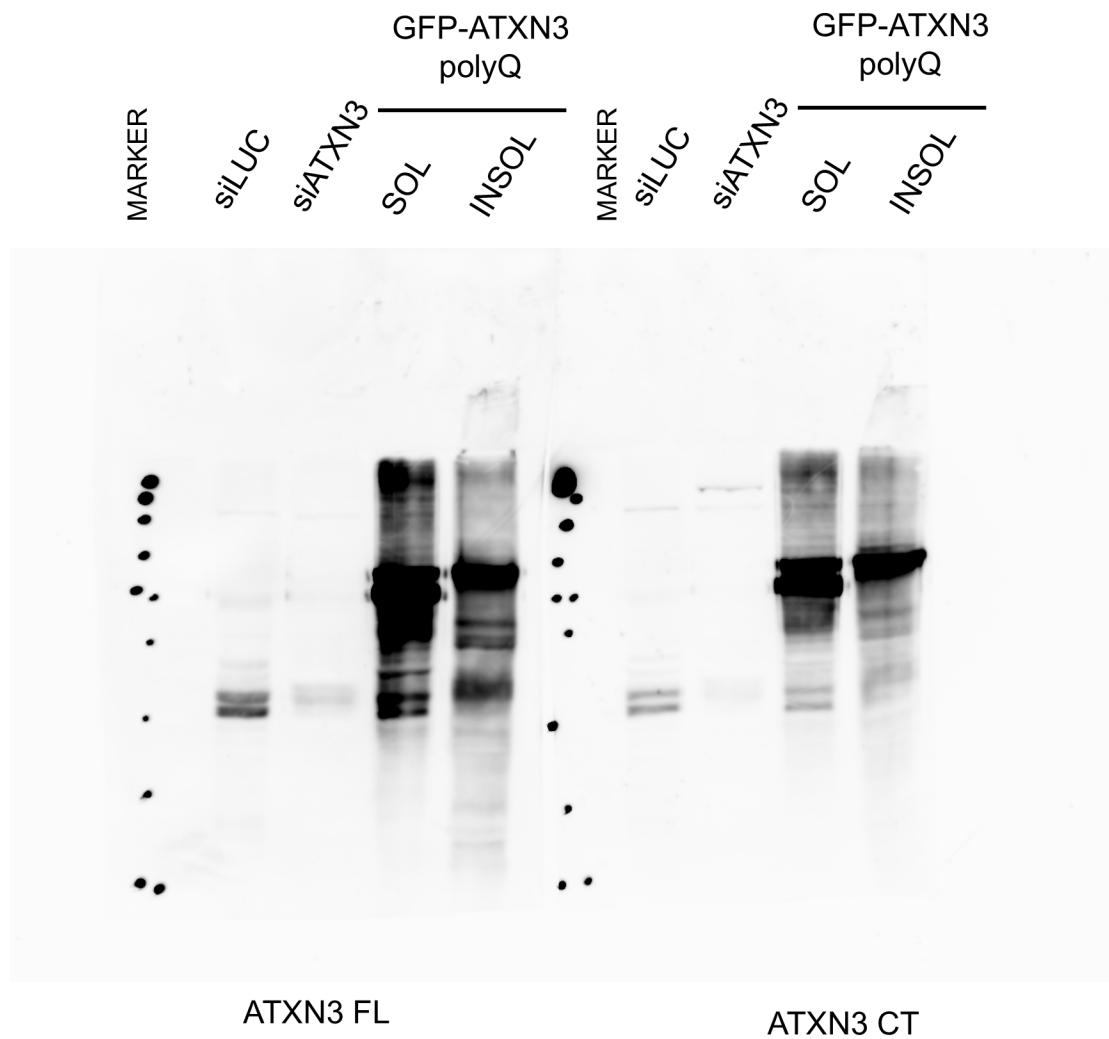
S2 FigB middle panel



S2 FigB right panel

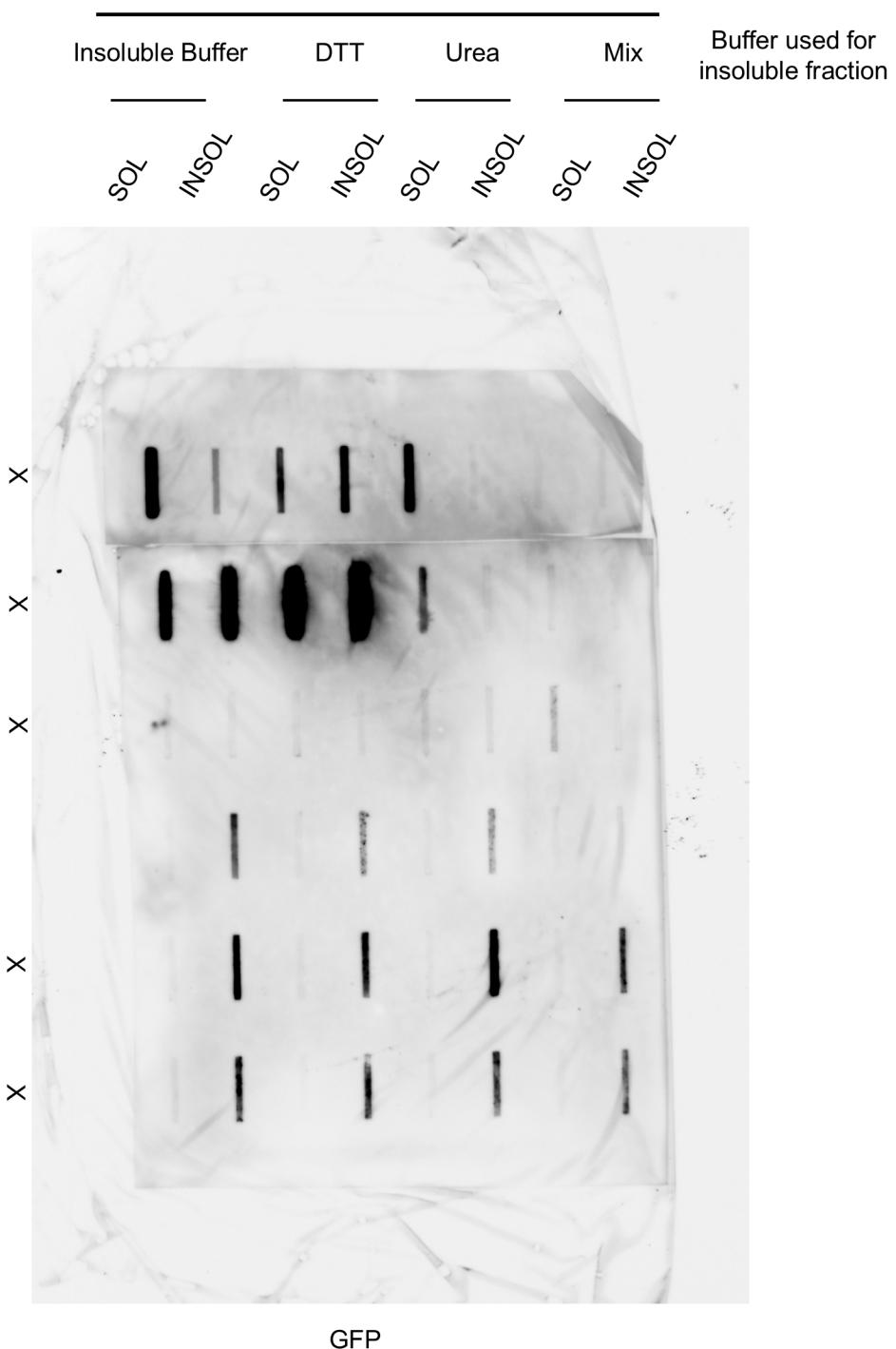


## S2 Fig C left and right panels

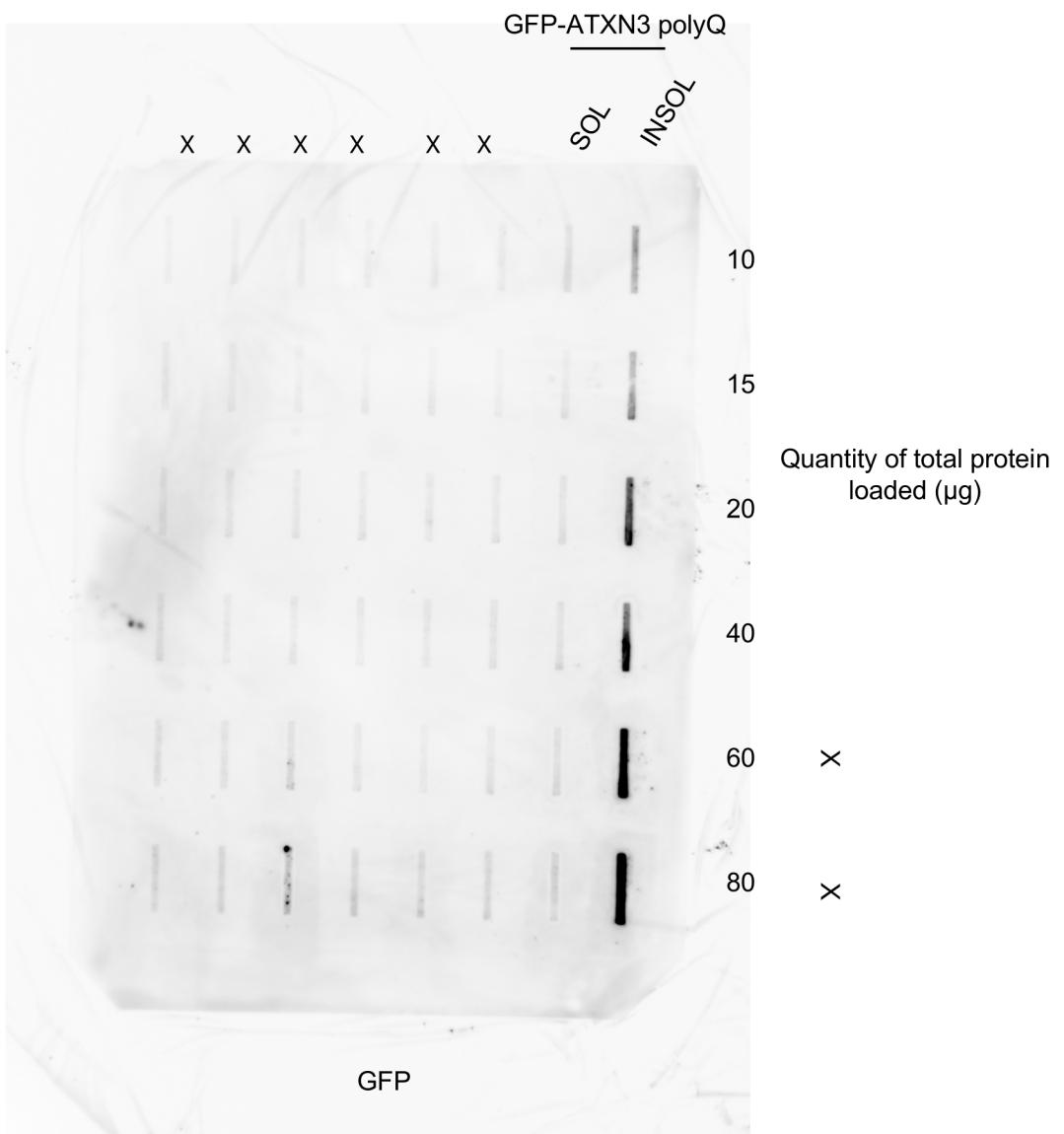


S3 FigA

GFP-ATXN3 polyQ



### S3 FigB



# S4 Fig

