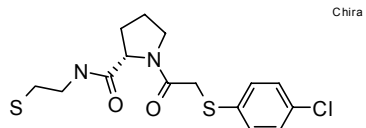
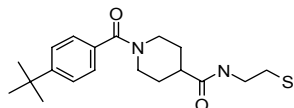
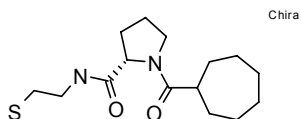
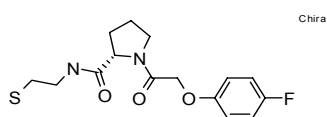
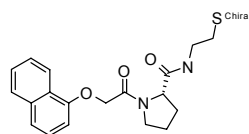
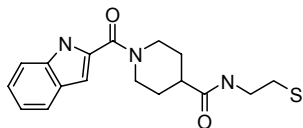
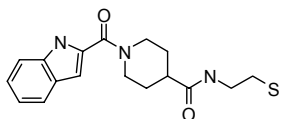
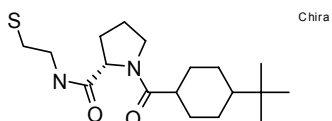
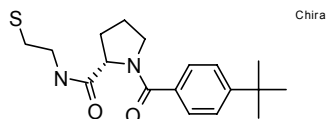
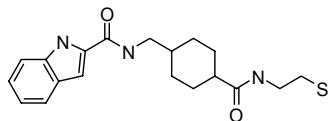
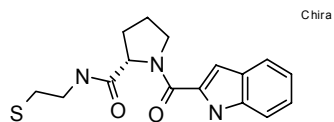
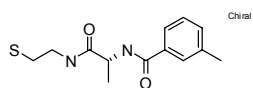
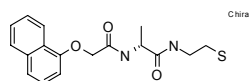
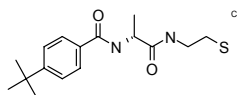
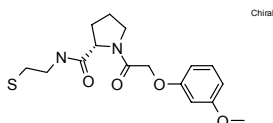
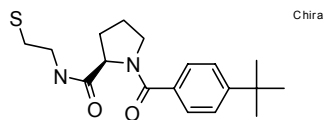
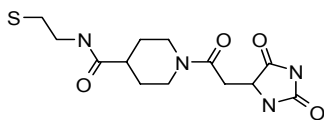
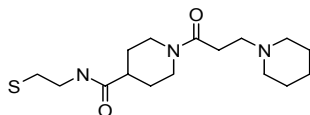
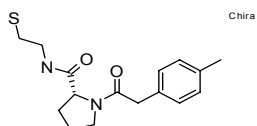
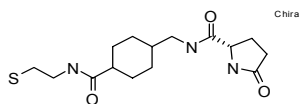
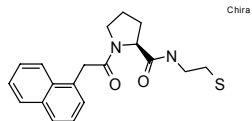
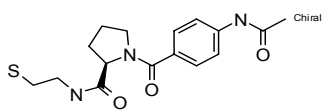


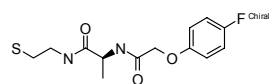
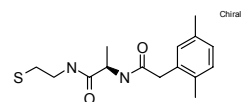
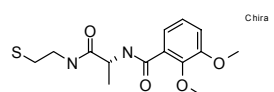
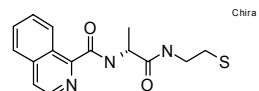
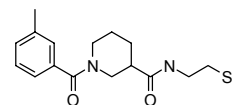
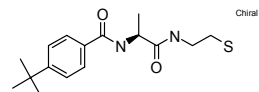
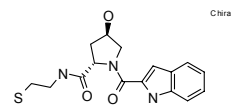
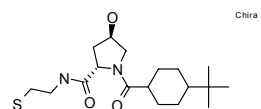
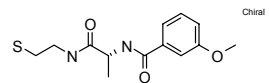
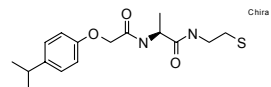
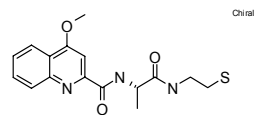
IL2 N30C Tether Hits



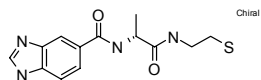
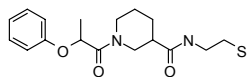
IL2 N30C Tether Hits



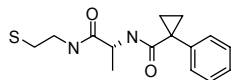
IL2 N30C Tether Hits



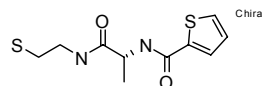
IL2 N30C Tether Hits



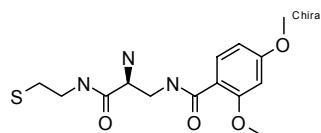
Chiral



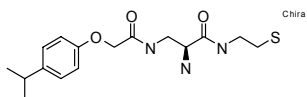
Chiral



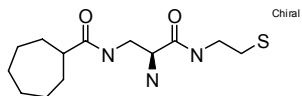
Chiral



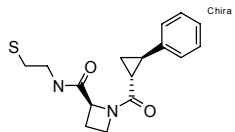
Chiral



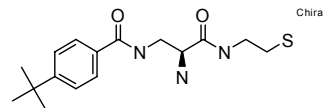
Chiral



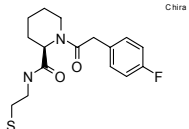
Chiral



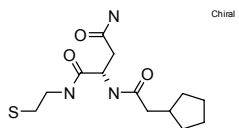
Chiral



Chiral

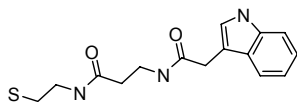


Chiral

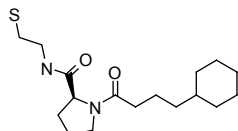


Chiral

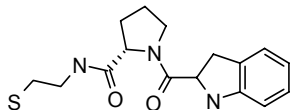
IL2 N30C Tether Hits



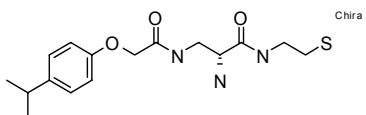
Chiral



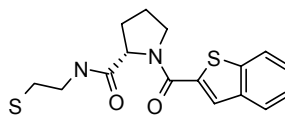
Chiral



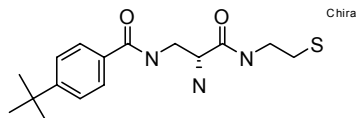
Chiral



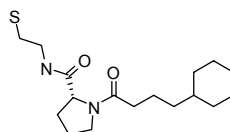
Chiral



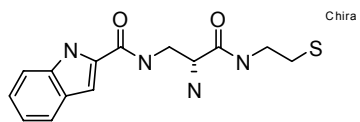
Chiral



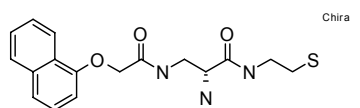
Chiral



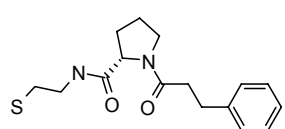
Chiral



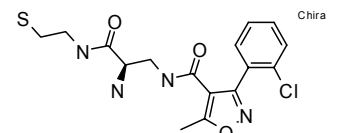
Chiral



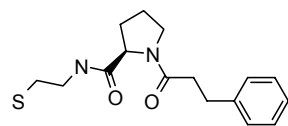
Chiral



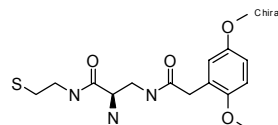
Chiral



IL2 N30C Tether Hits

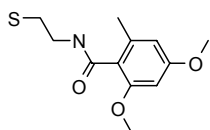
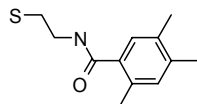
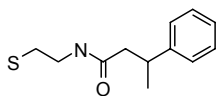
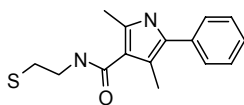
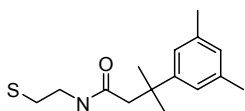
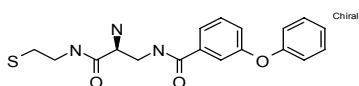
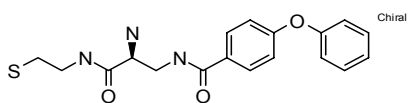
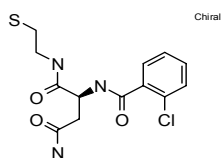
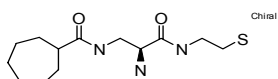
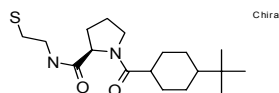
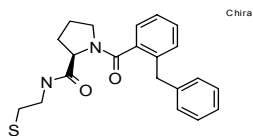


Chiral

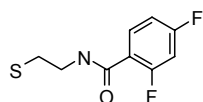
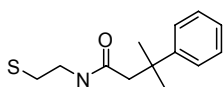
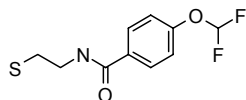
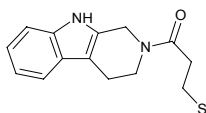
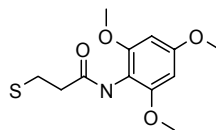
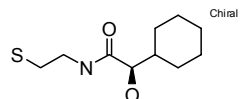
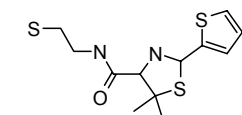
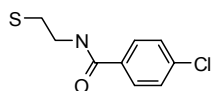
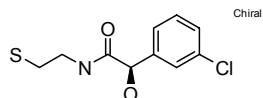
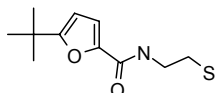
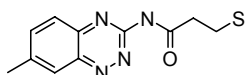


Chiral

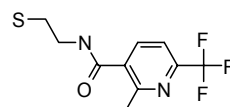
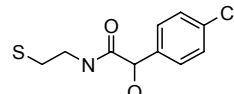
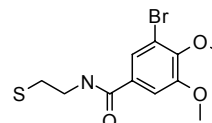
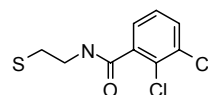
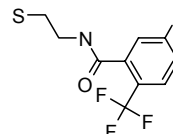
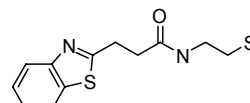
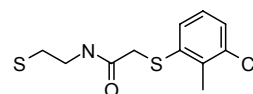
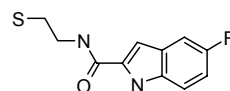
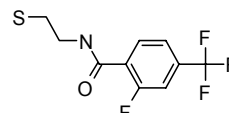
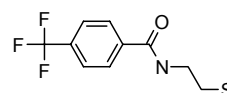
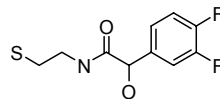
IL2 Y31C Tether Hits



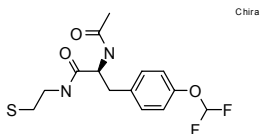
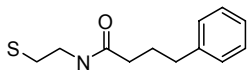
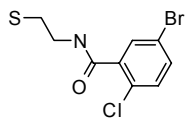
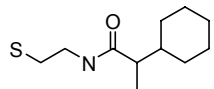
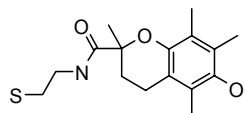
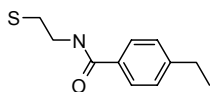
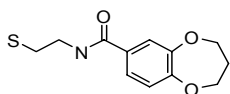
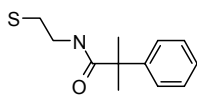
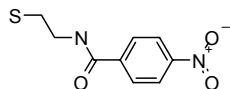
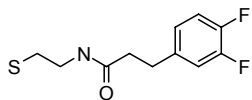
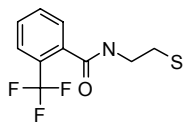
IL2 Y31C Tether Hits



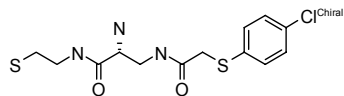
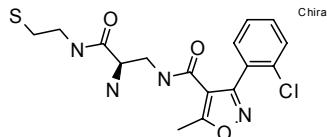
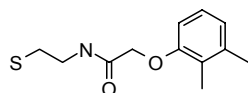
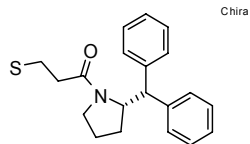
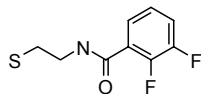
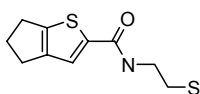
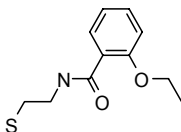
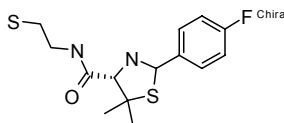
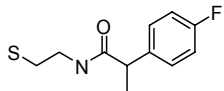
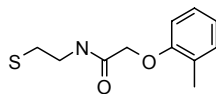
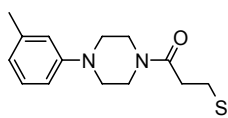
IL2 Y31C Tether Hits



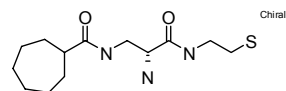
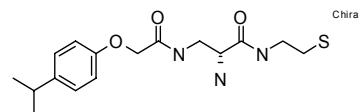
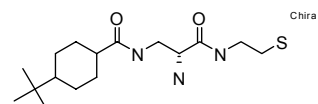
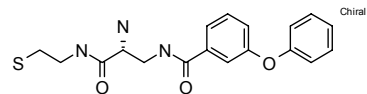
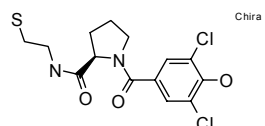
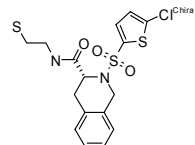
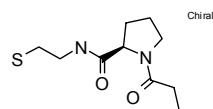
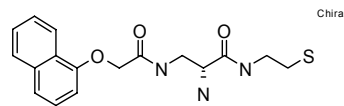
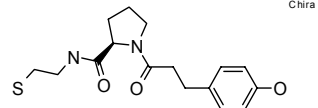
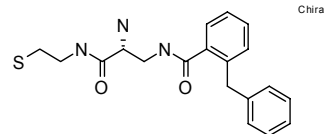
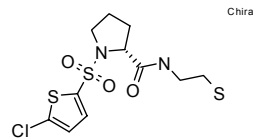
IL2 Y31C Tether Hits



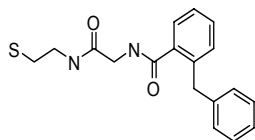
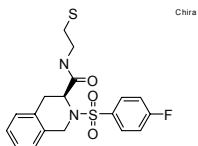
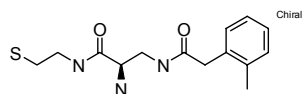
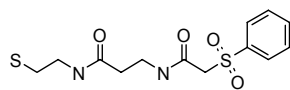
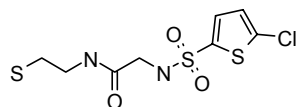
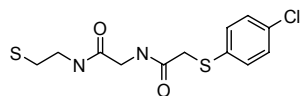
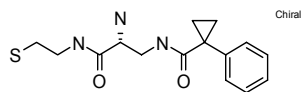
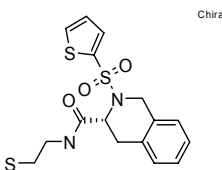
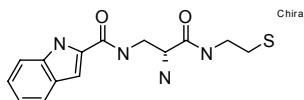
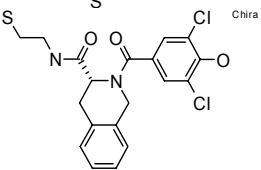
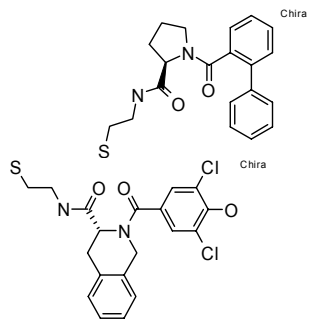
IL2 Y31C Tether Hits



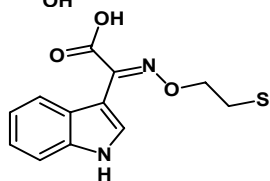
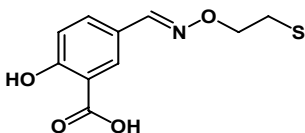
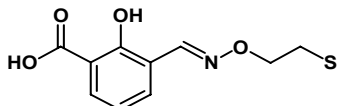
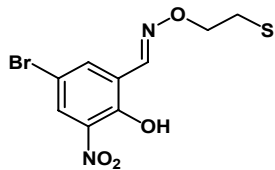
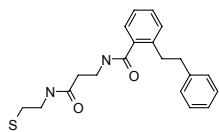
IL2 Y31C Tether Hits



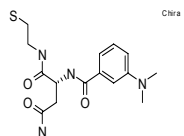
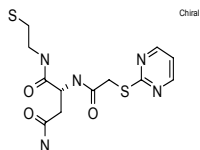
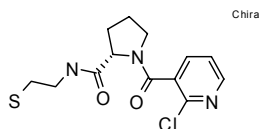
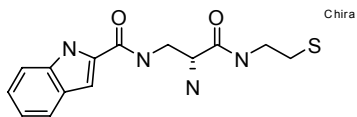
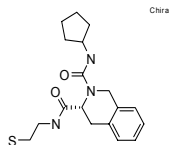
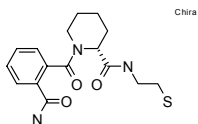
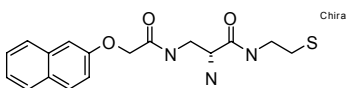
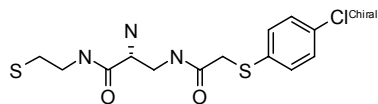
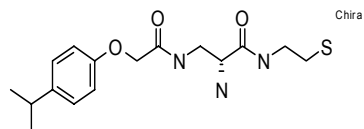
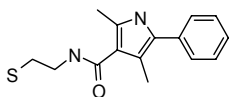
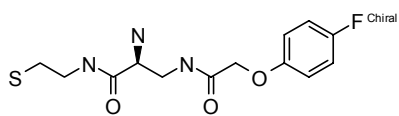
IL2 Y31C Tether Hits



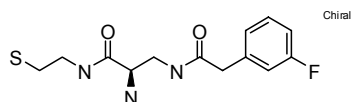
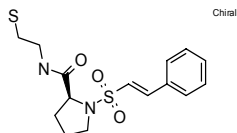
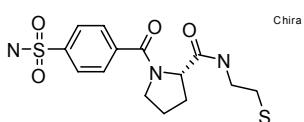
IL2 Y31C Tether Hits



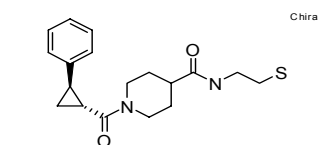
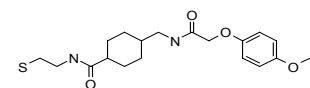
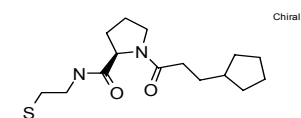
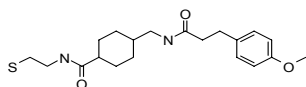
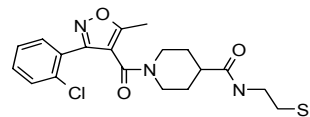
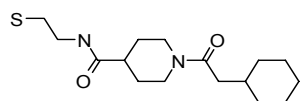
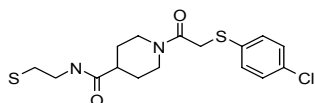
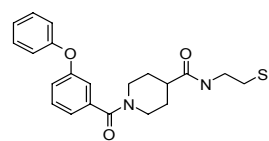
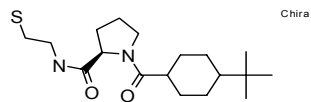
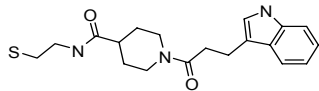
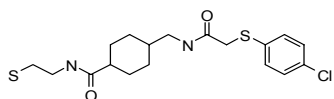
IL2 K32C Tether Hits



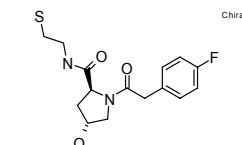
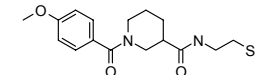
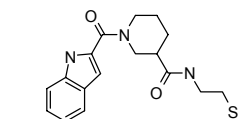
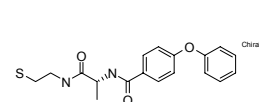
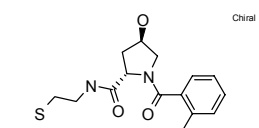
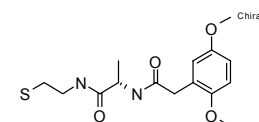
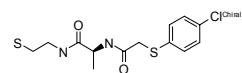
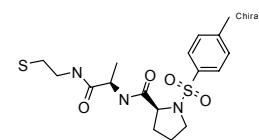
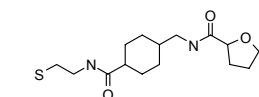
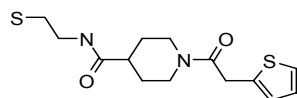
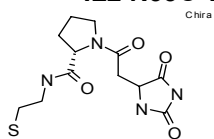
IL2 K32C Tether Hits



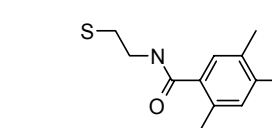
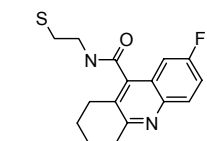
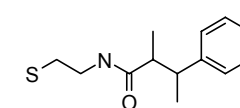
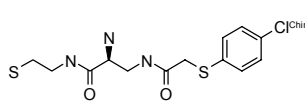
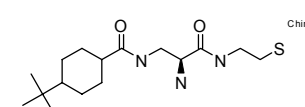
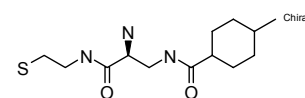
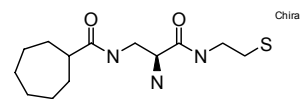
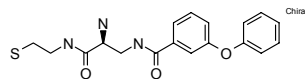
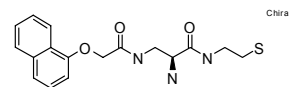
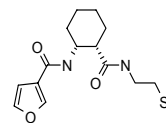
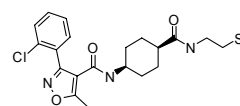
IL2 N33C Tether Hits



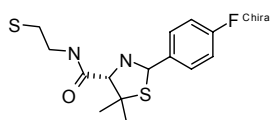
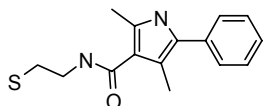
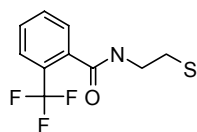
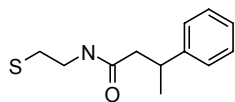
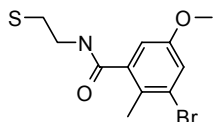
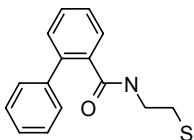
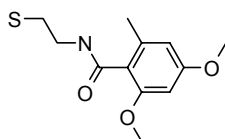
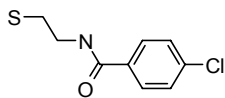
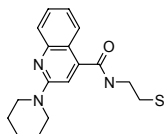
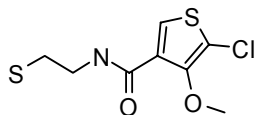
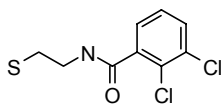
IL2 N33C Tether Hits



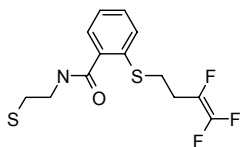
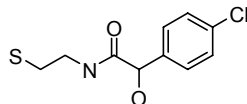
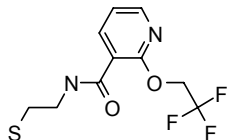
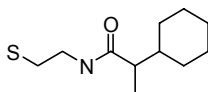
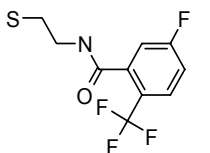
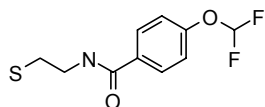
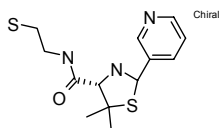
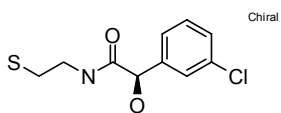
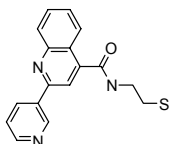
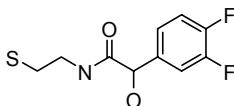
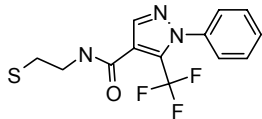
IL2 N33C Tether Hits



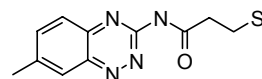
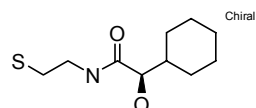
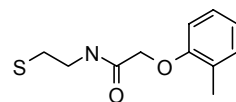
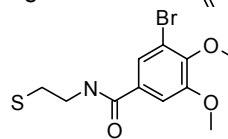
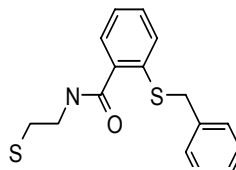
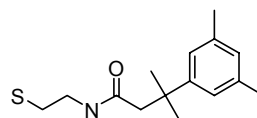
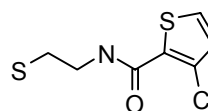
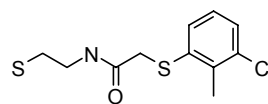
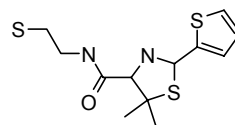
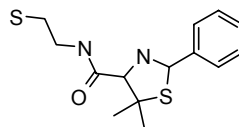
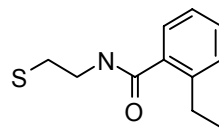
IL2 N33C Tether Hits



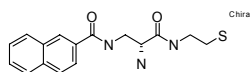
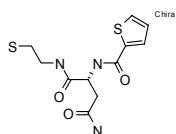
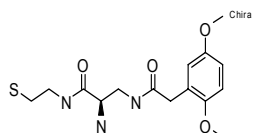
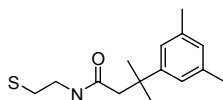
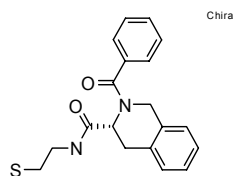
IL2 N33C Tether Hits



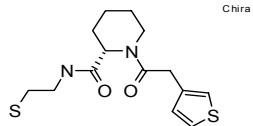
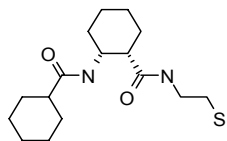
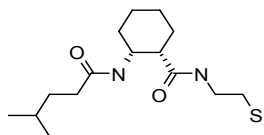
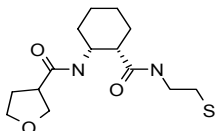
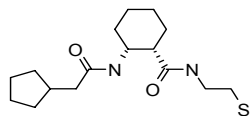
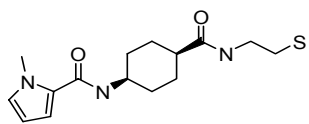
IL2 N33C Tether Hits



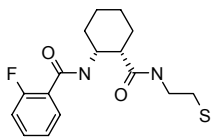
IL2 K35C Tether Hits



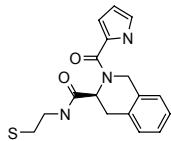
IL2 Y45C Tether Hits



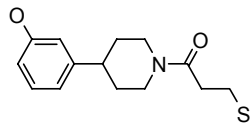
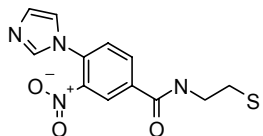
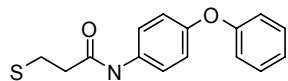
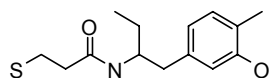
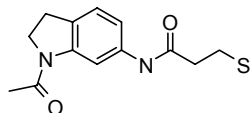
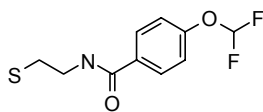
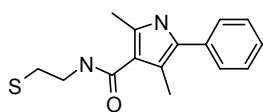
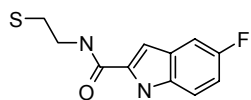
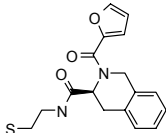
IL2 L72C Tether Hits



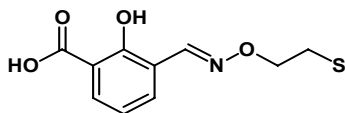
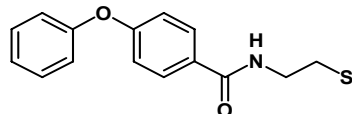
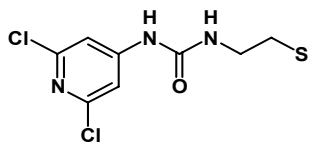
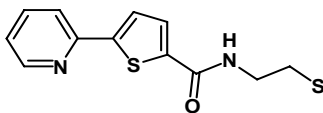
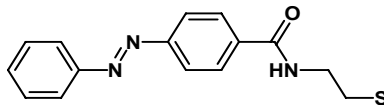
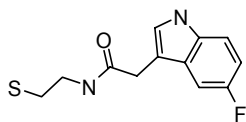
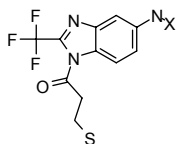
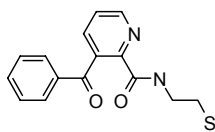
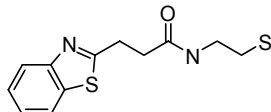
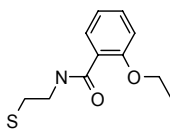
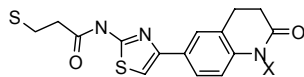
Chira



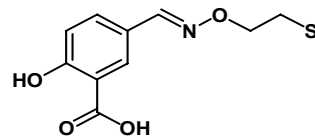
Chira



IL2 L72C Tether Hits

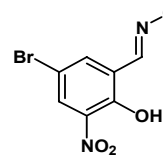


IL2 L72C Tether Hits



HO

COOH

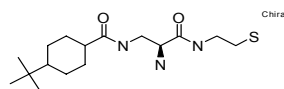
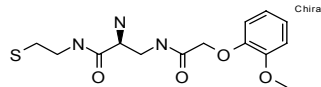
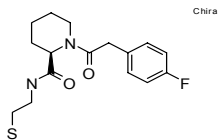
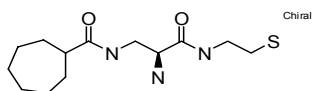
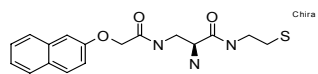
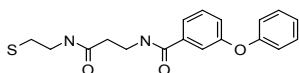
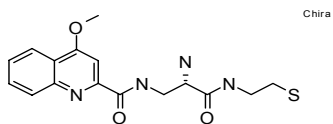
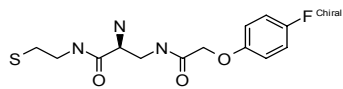
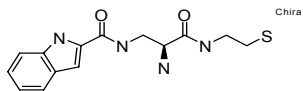
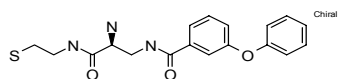
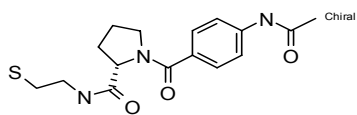


Br

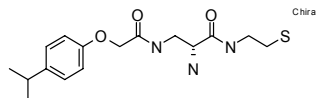
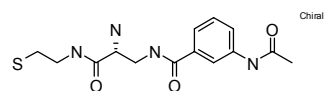
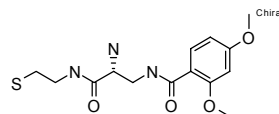
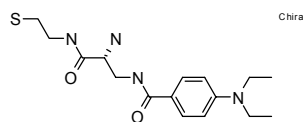
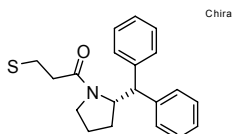
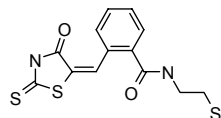
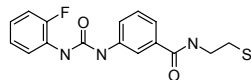
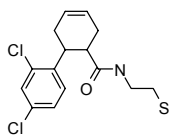
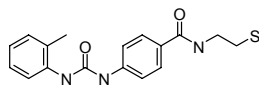
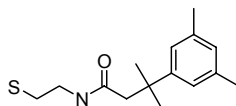
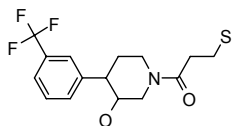
OH

NO₂

IL2 N77C Tether Hits



IL2 N77C Tether Hits



IL2 N77C Tether Hits

