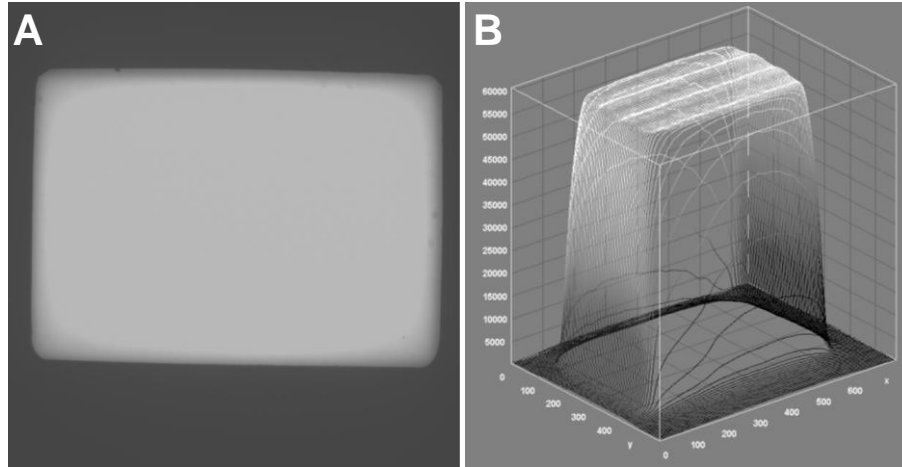
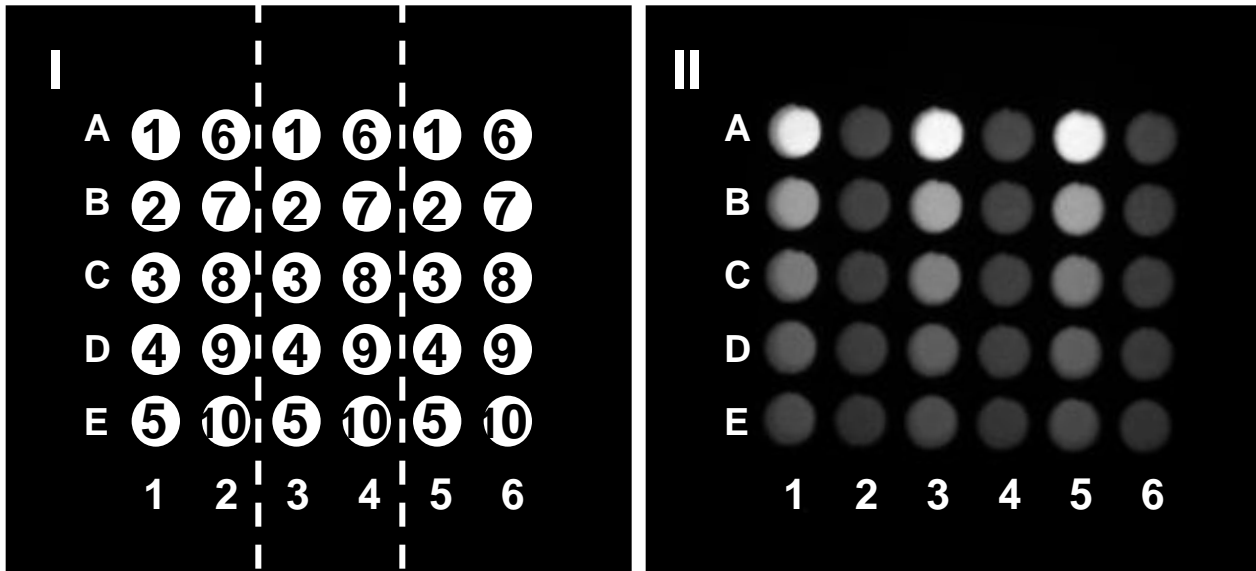


**Supporting information: Figure S1 FRET Scheme and LcA assays**



**Supporting information: Figure S2 uniform illumination analysis**



Supporting information: Figure S3 the layout of the chip