

Figure 1A

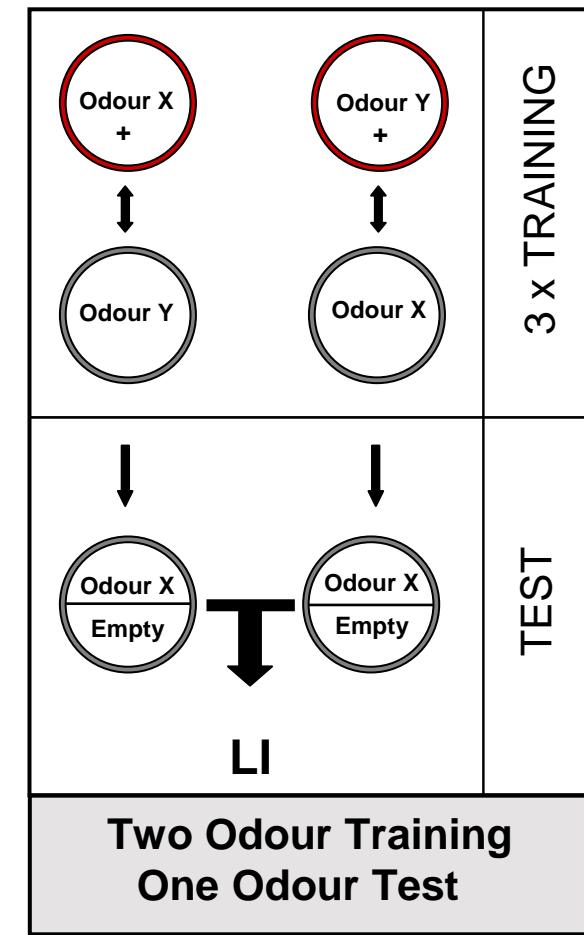
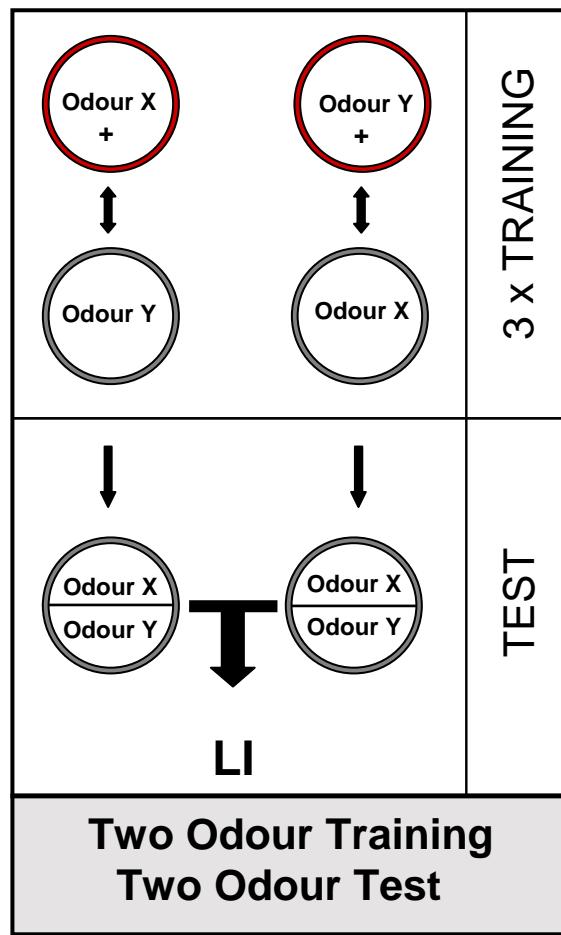
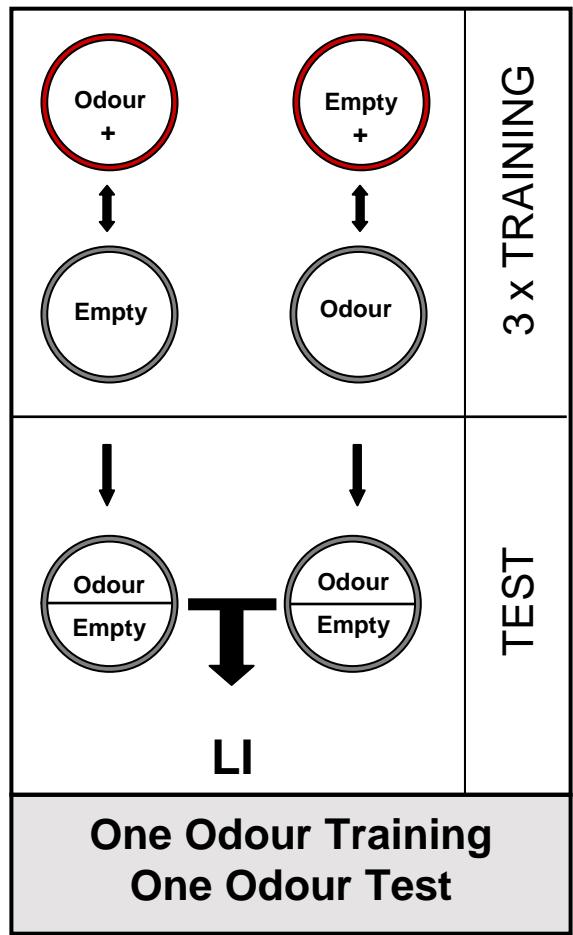


Figure 1B

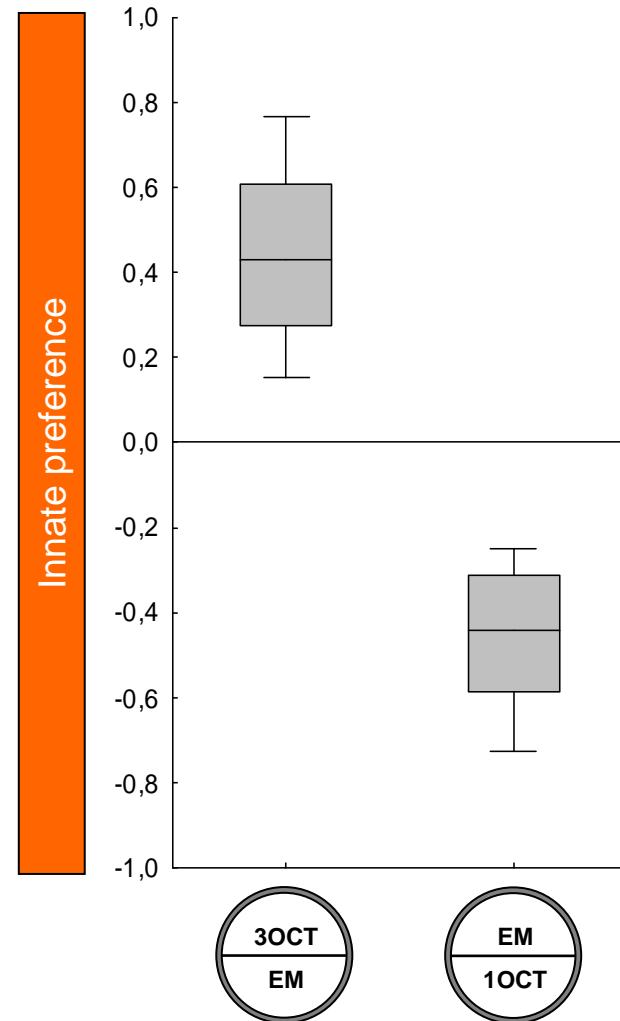


Figure 2A

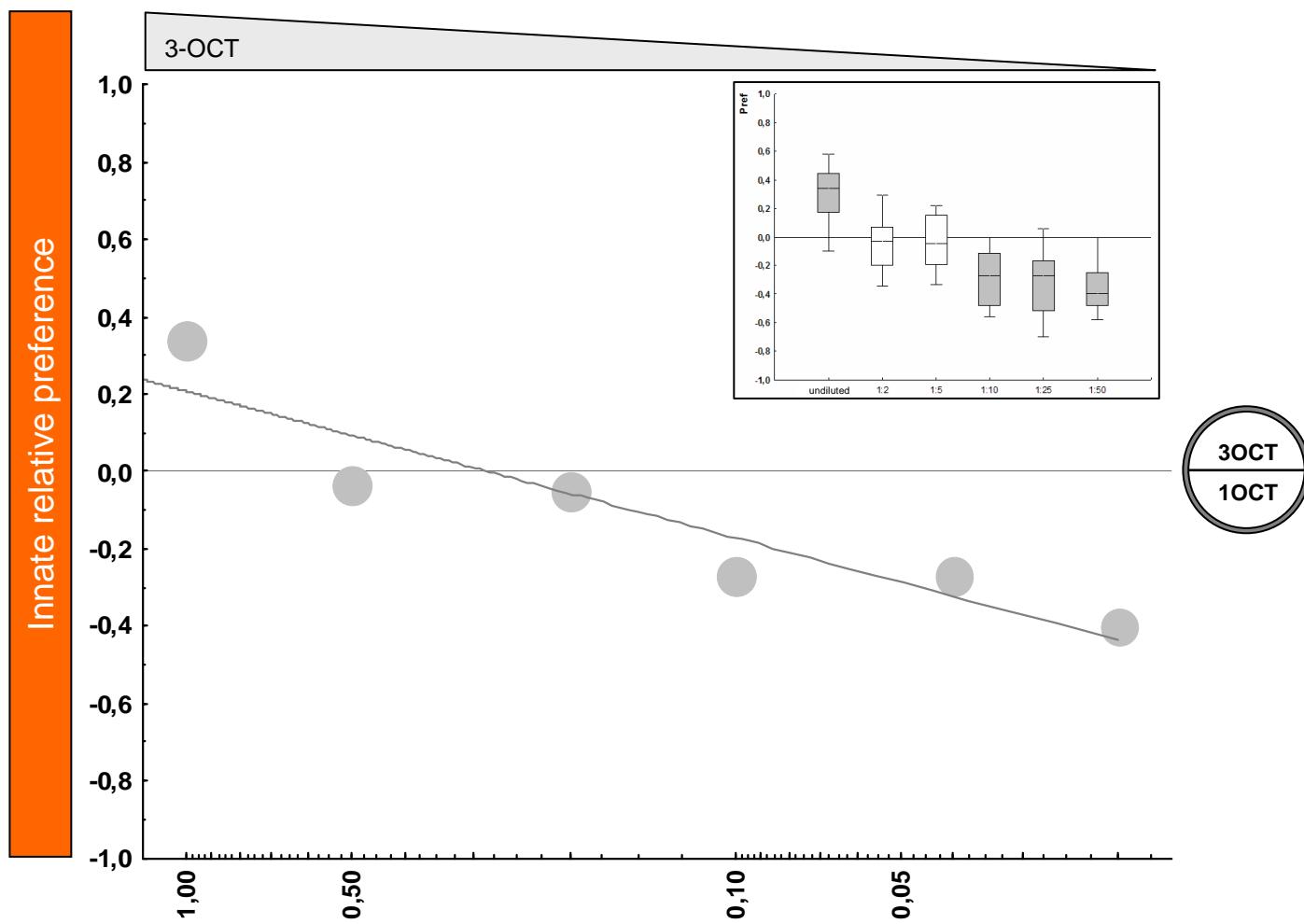


Figure 2B

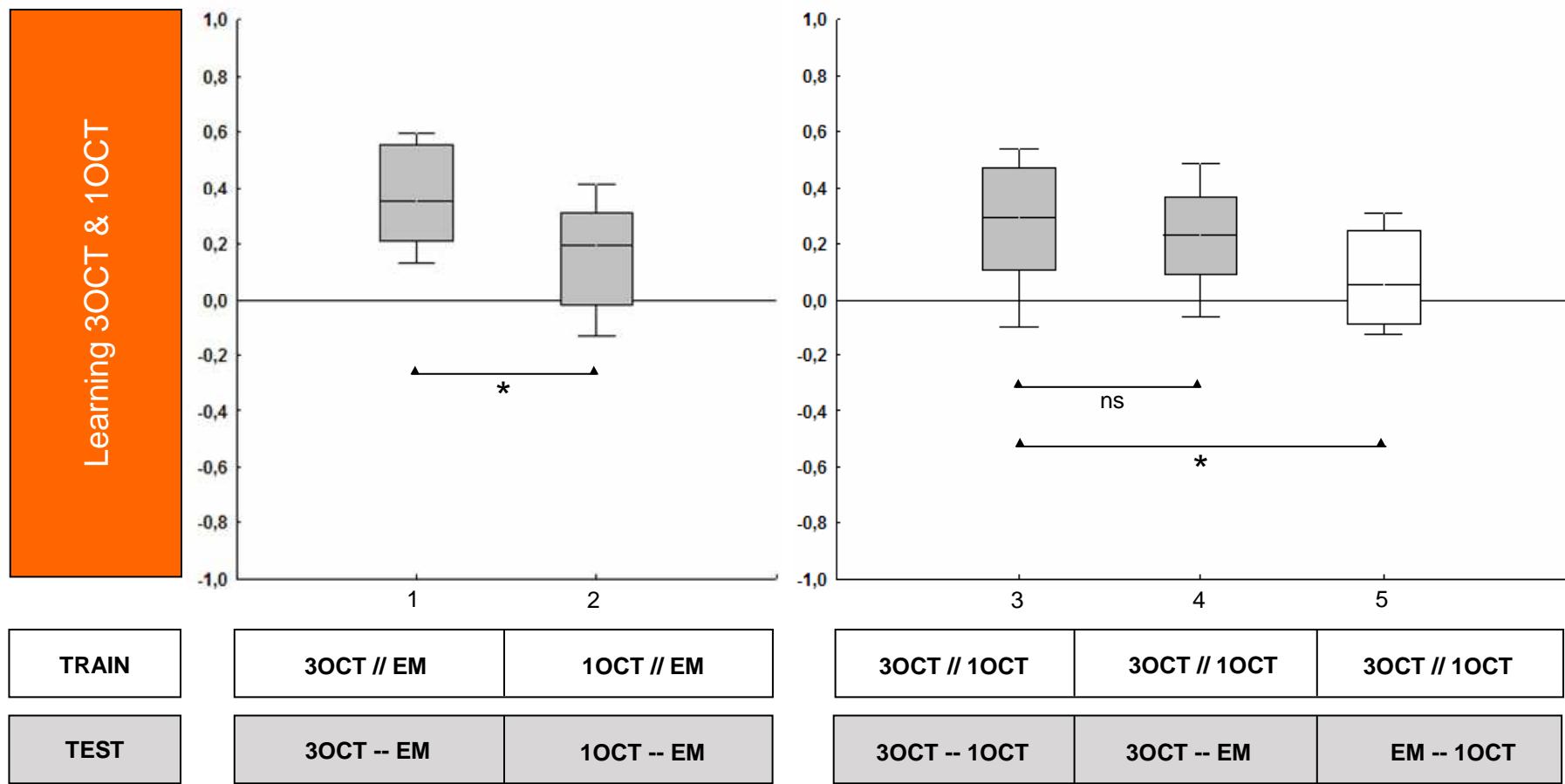


Figure 2C

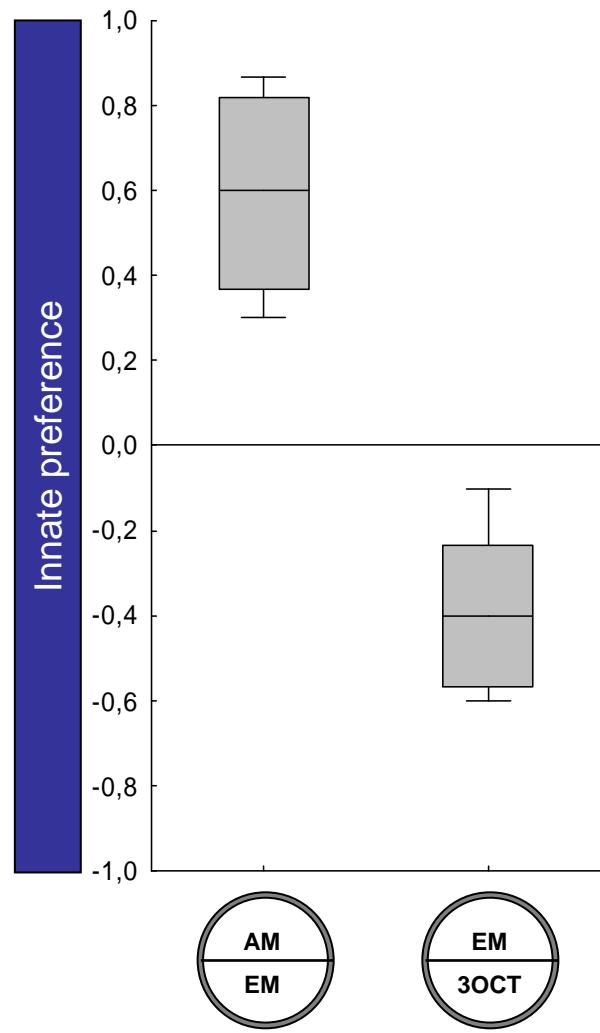


Figure 3A

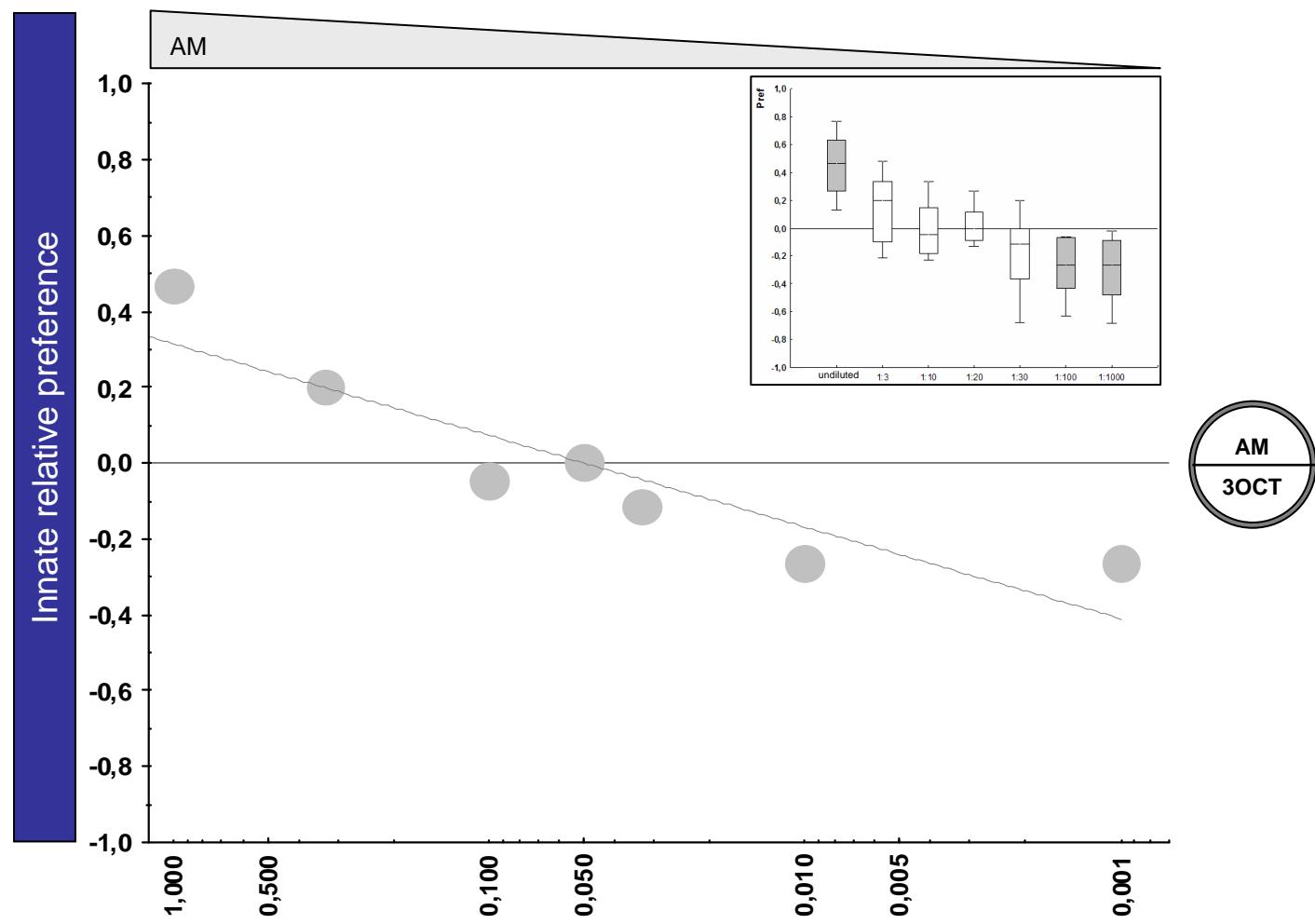


Figure 3B

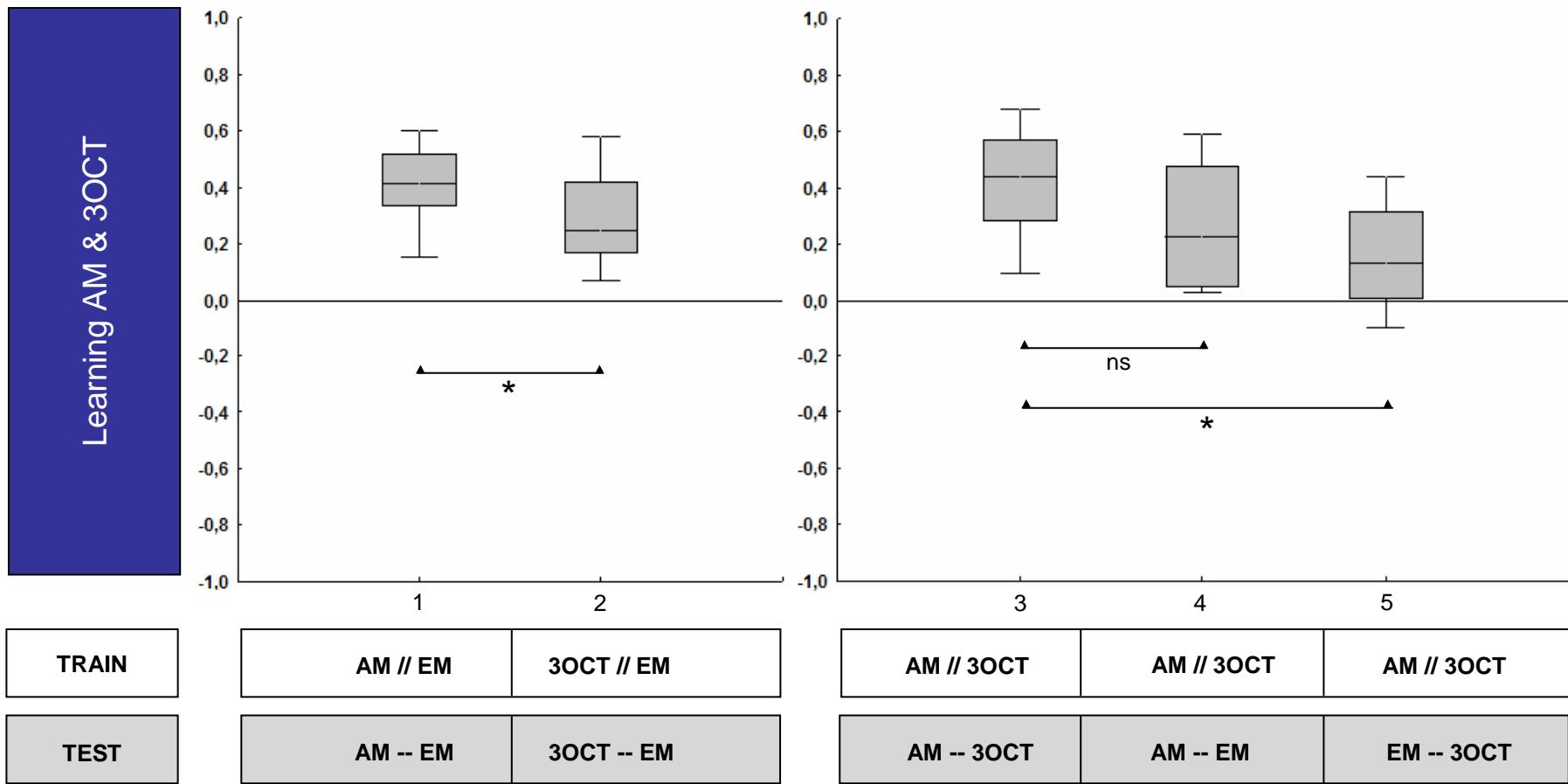


Figure 3C

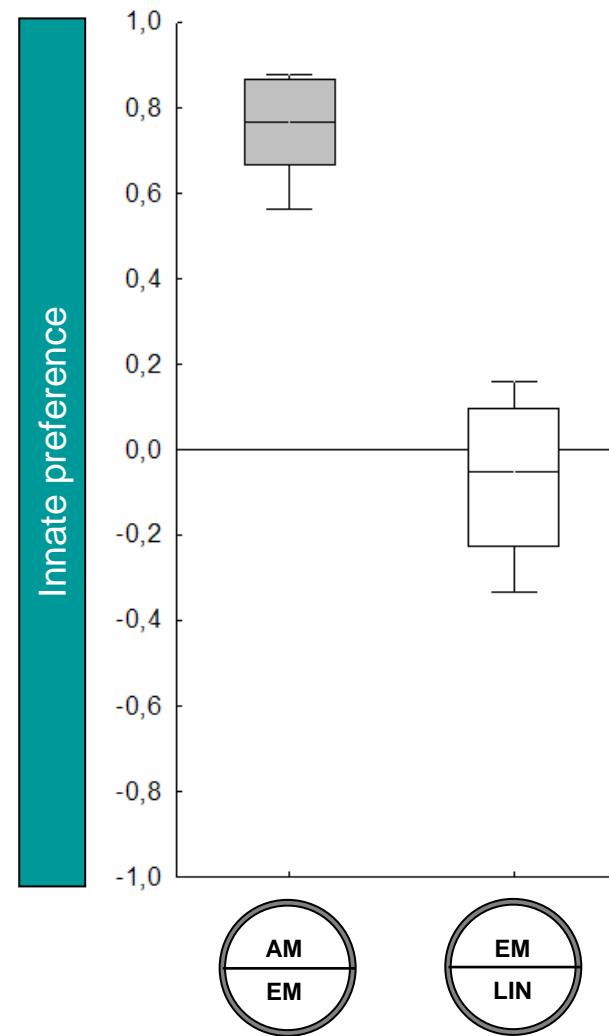


Figure 4A

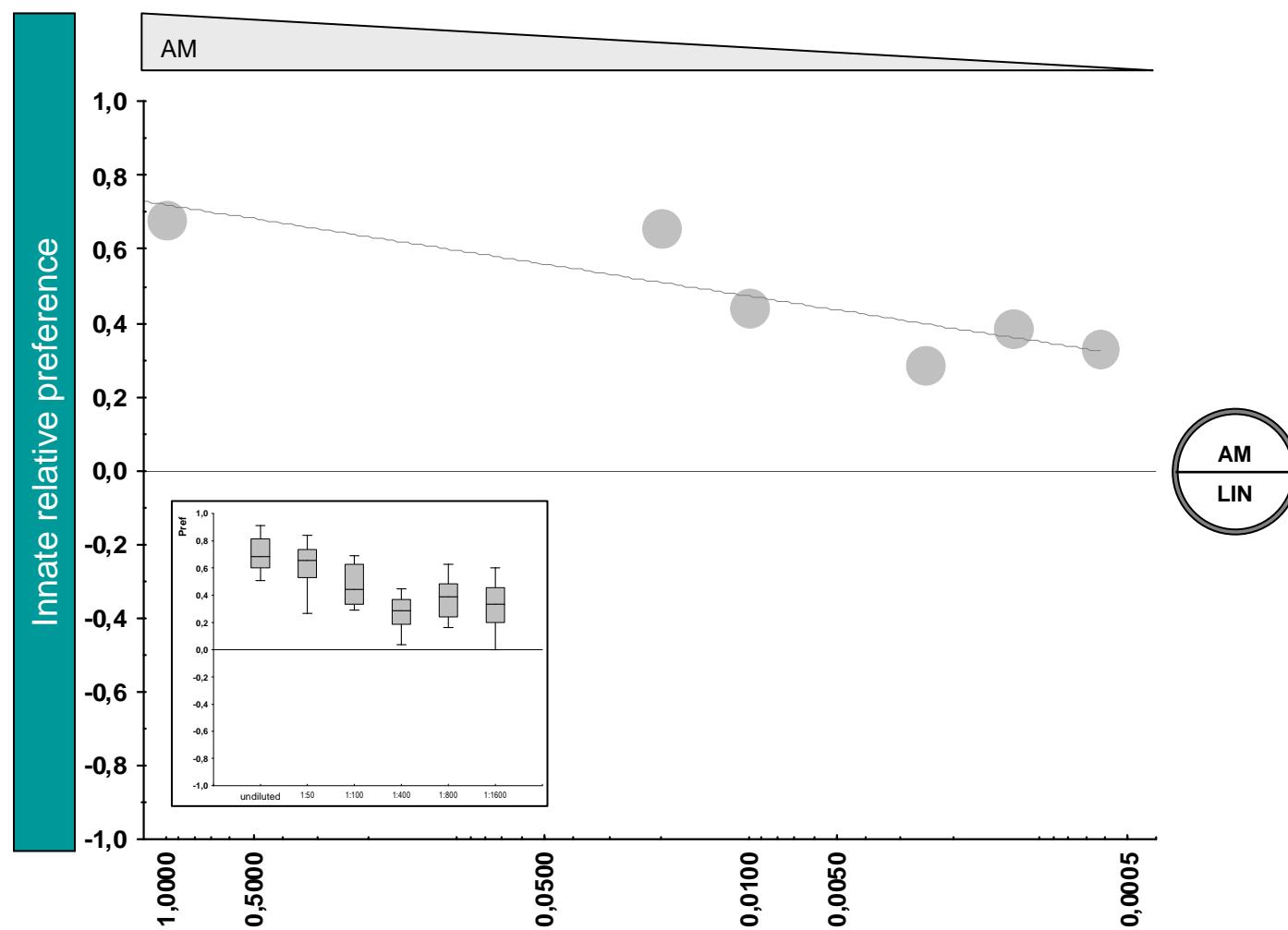


Figure 4B

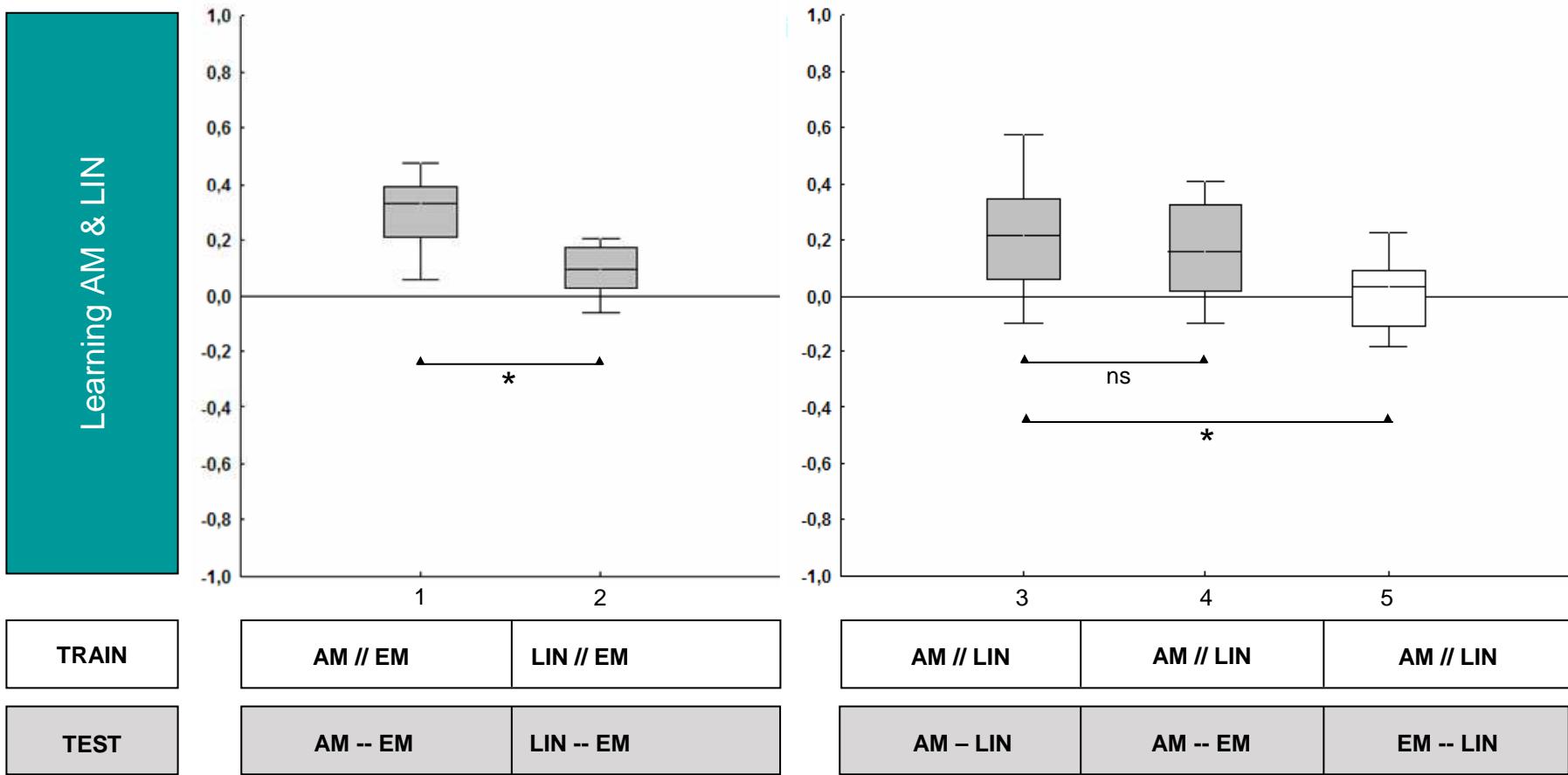


Figure 4C

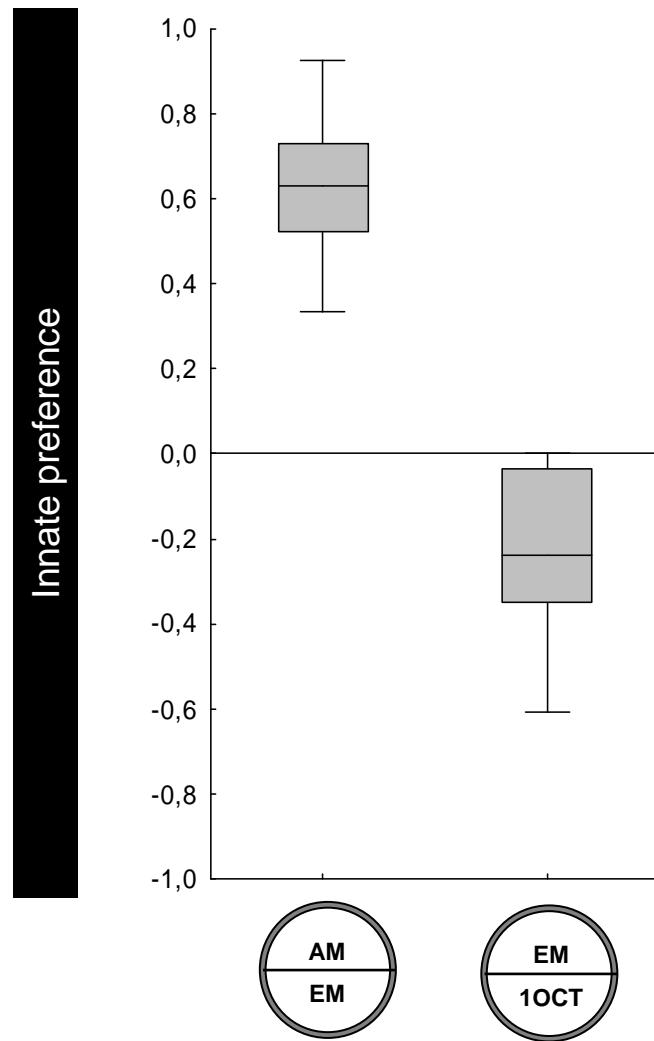


Figure 5A

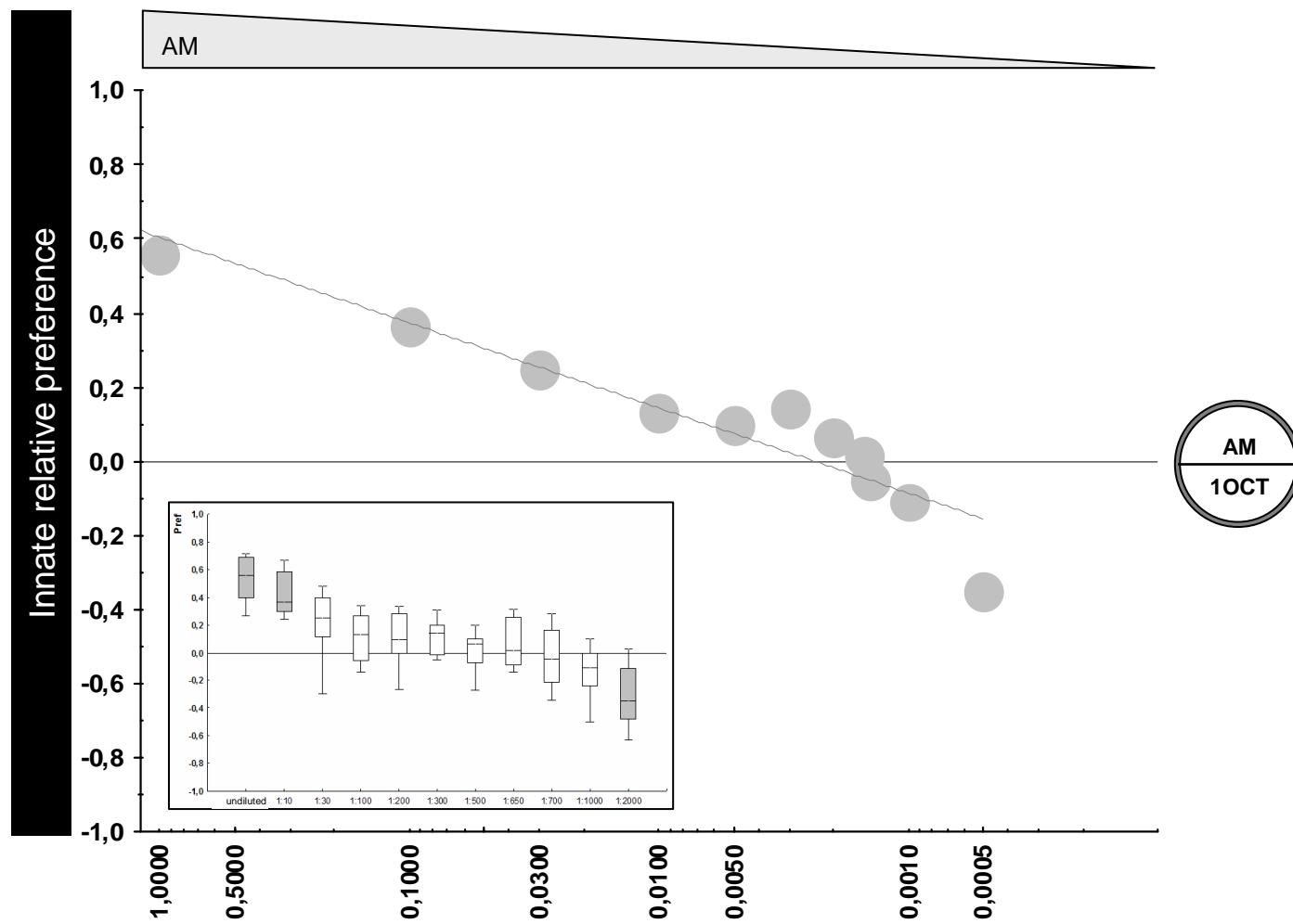


Figure 5B

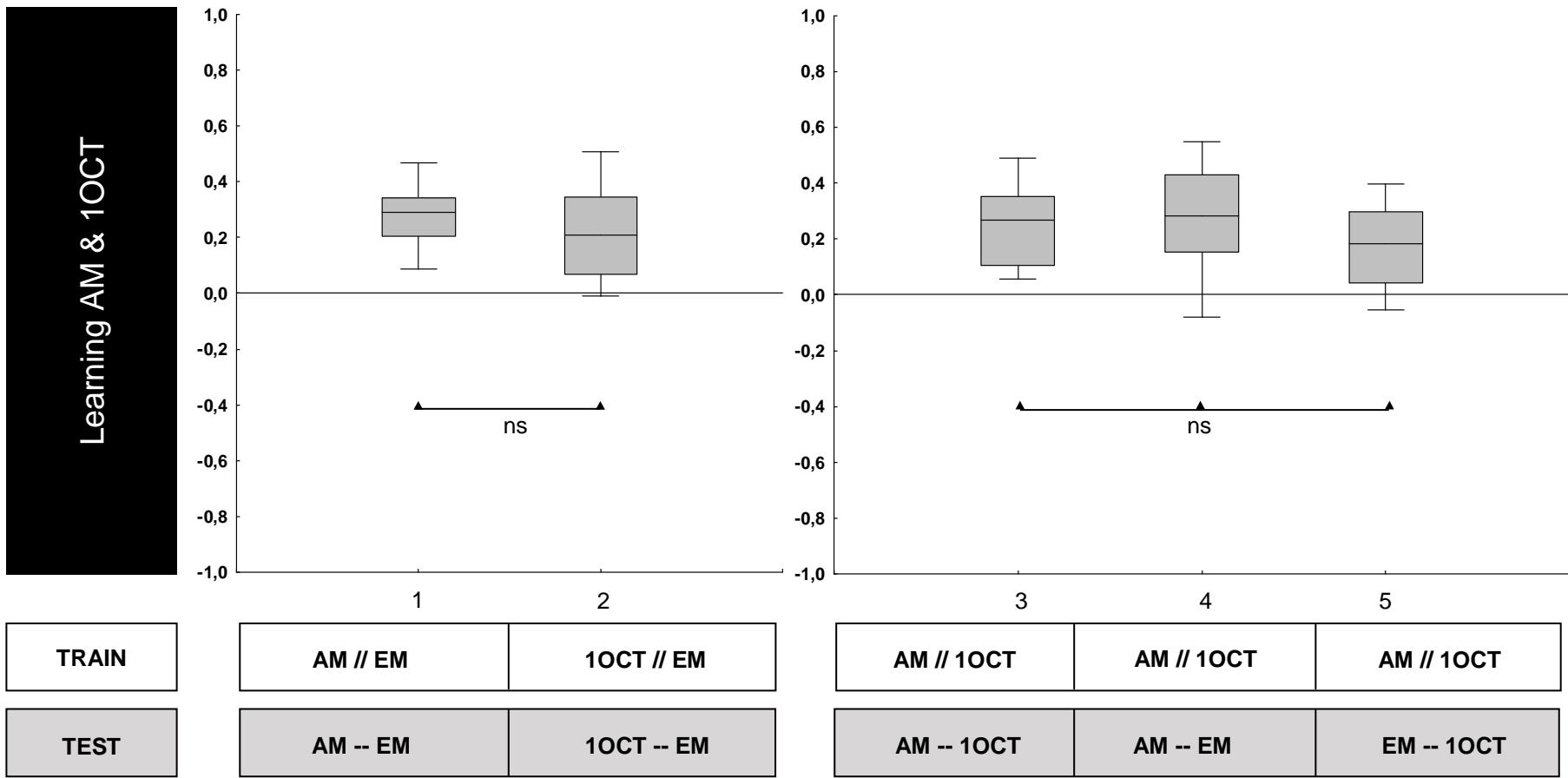


Figure 5C

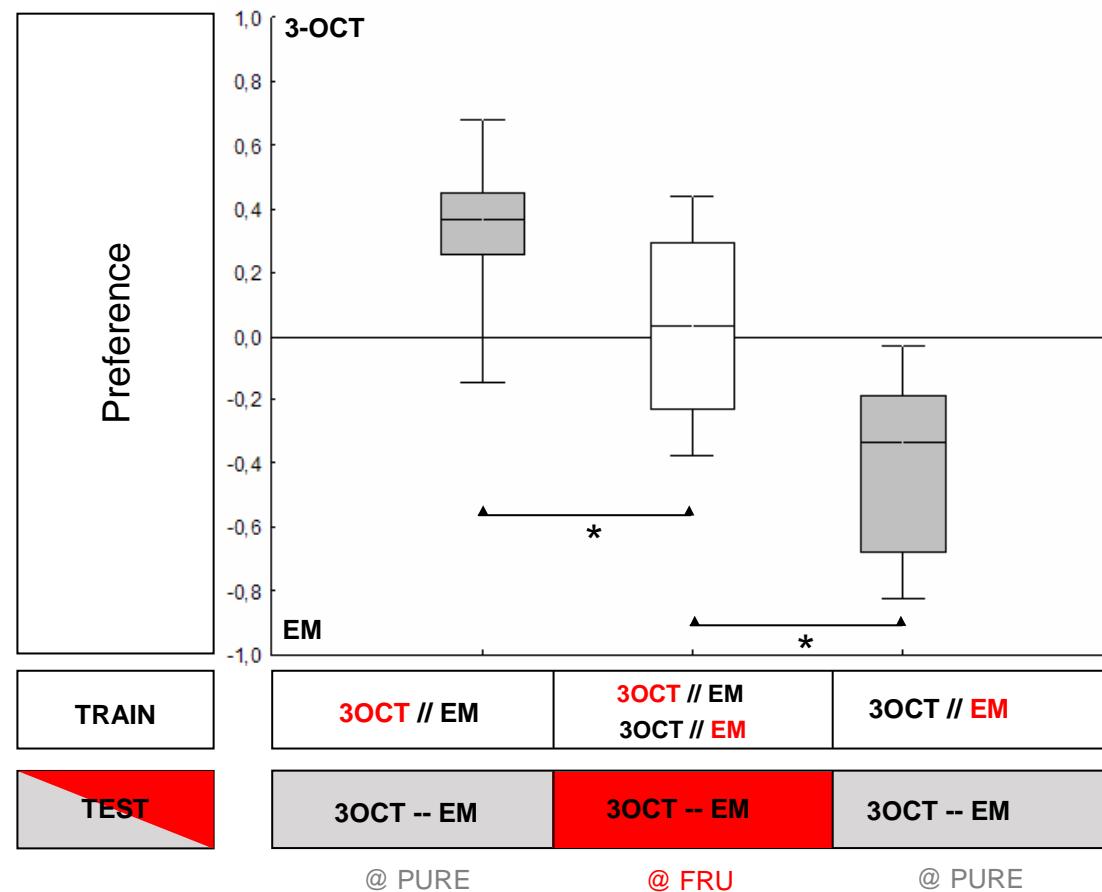


Figure 6A

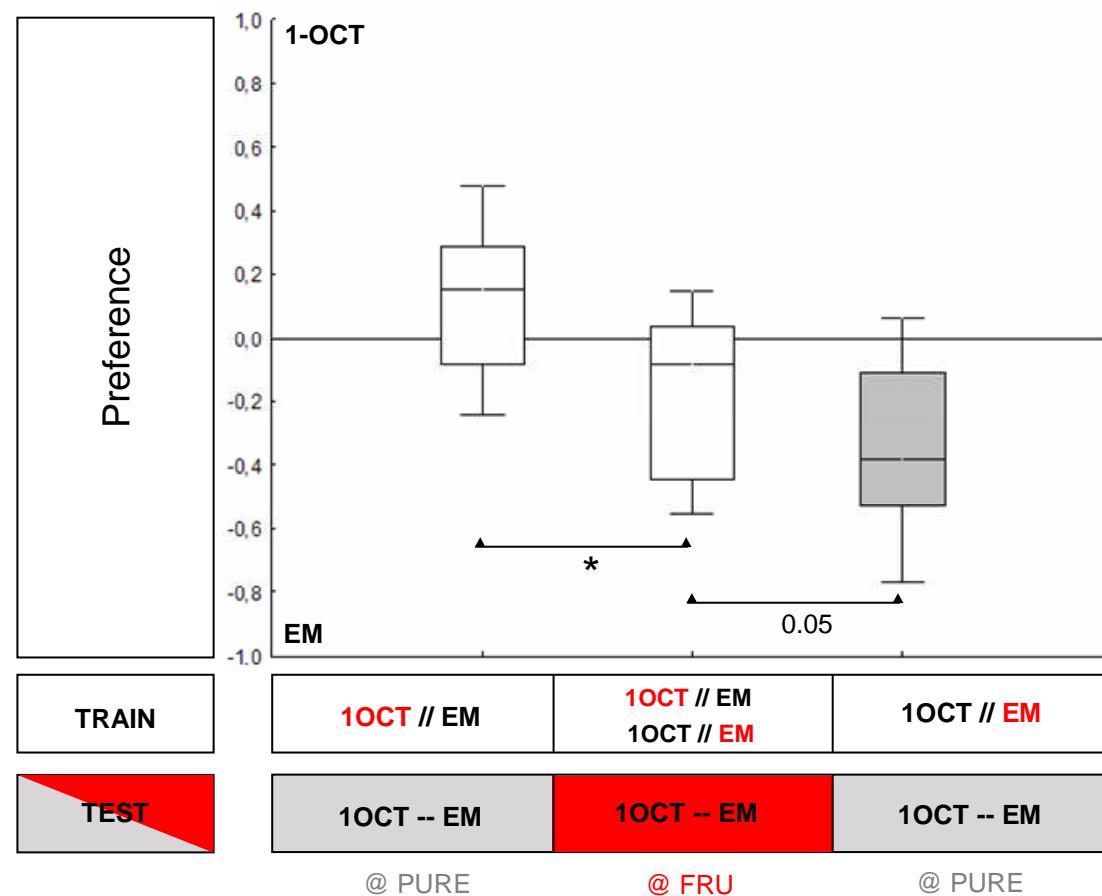


Figure 6B

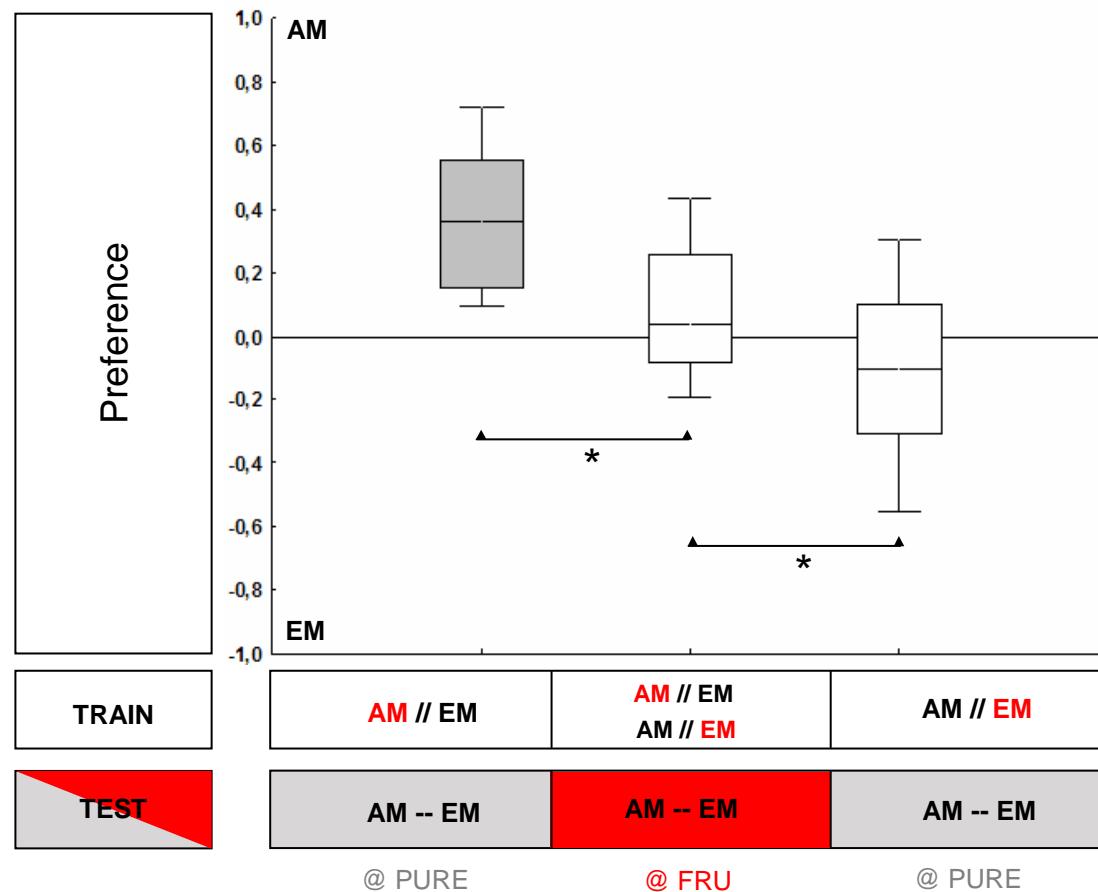
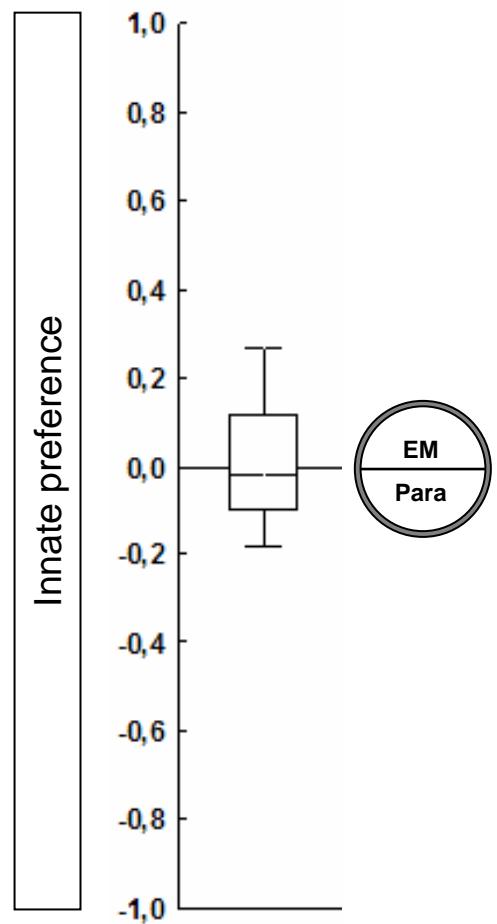
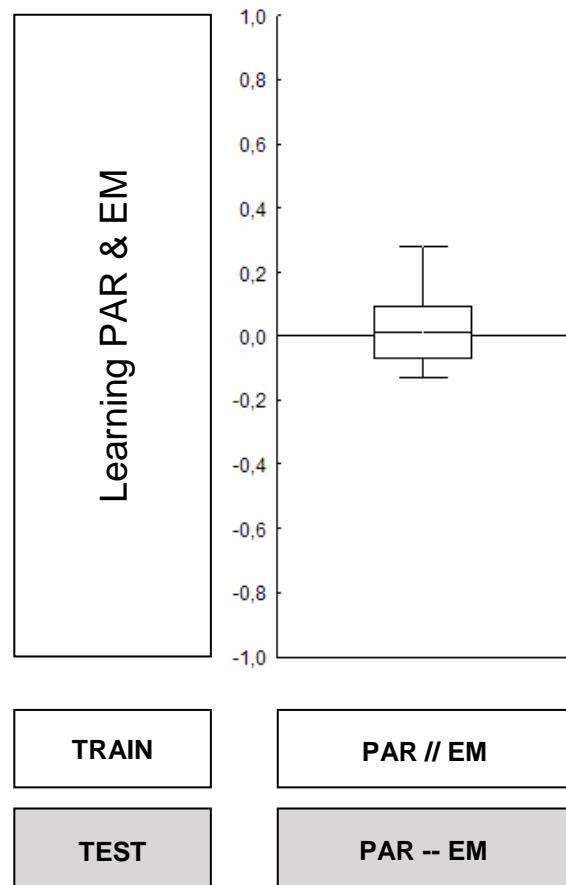


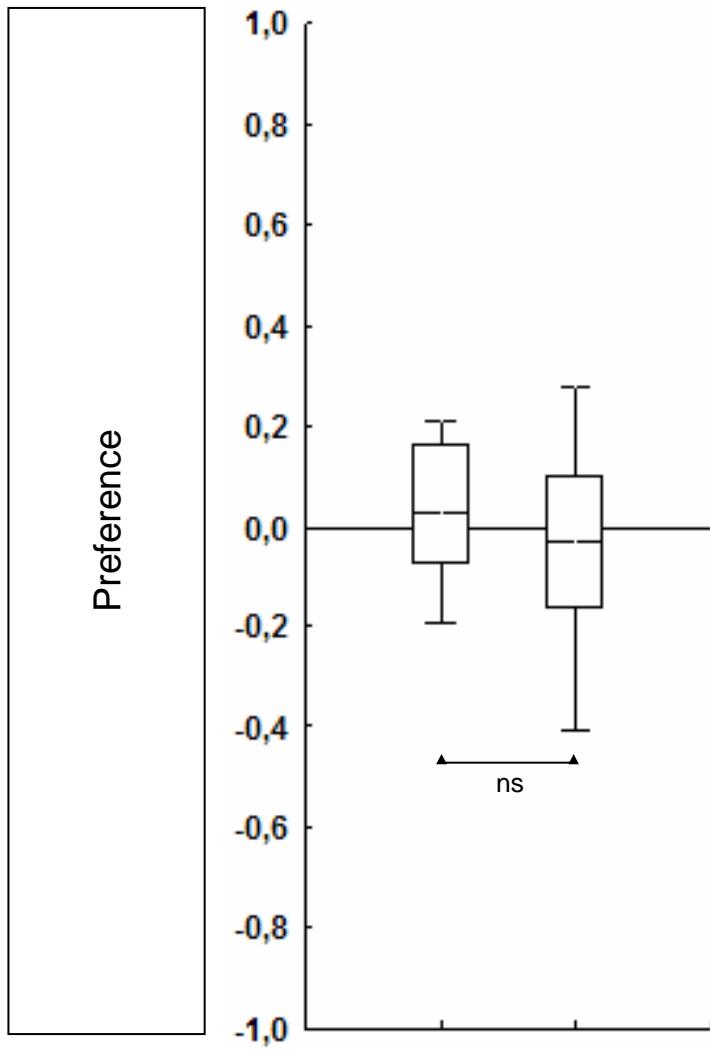
Figure 6C



Suppl Figure 1A



Suppl Figure 1B



TRAIN

TEST

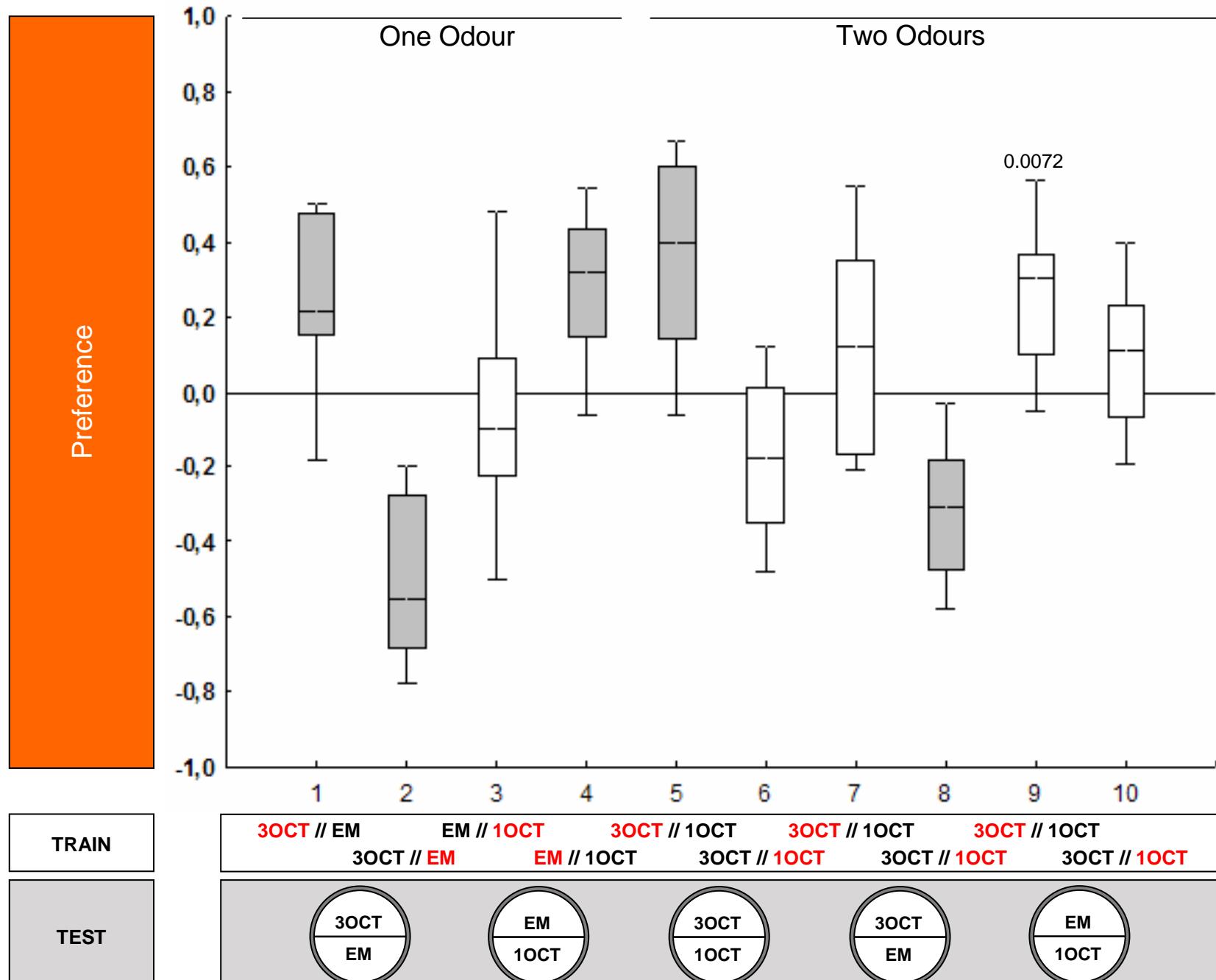
PAR // EM

PAR // EM

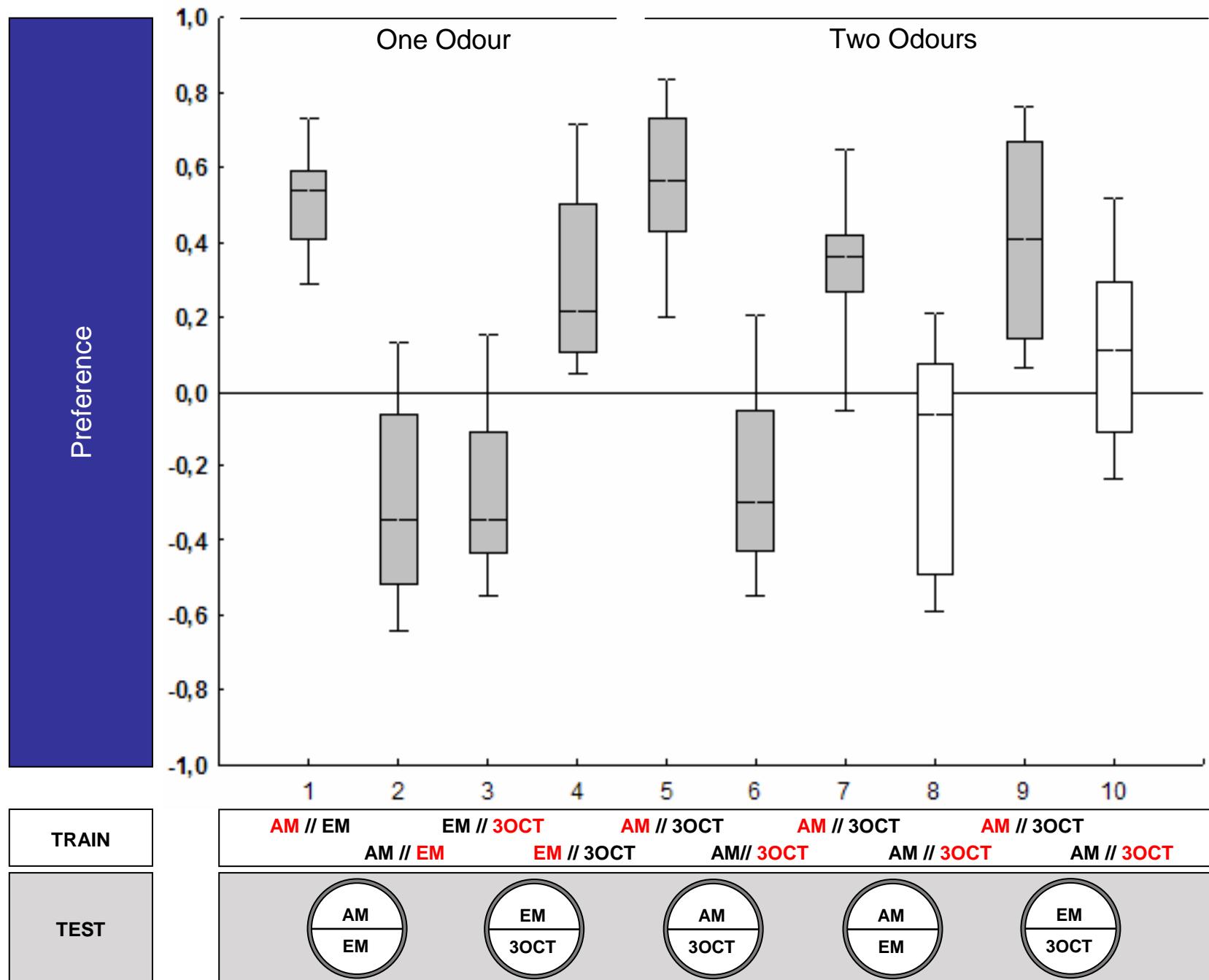
PAR

EM

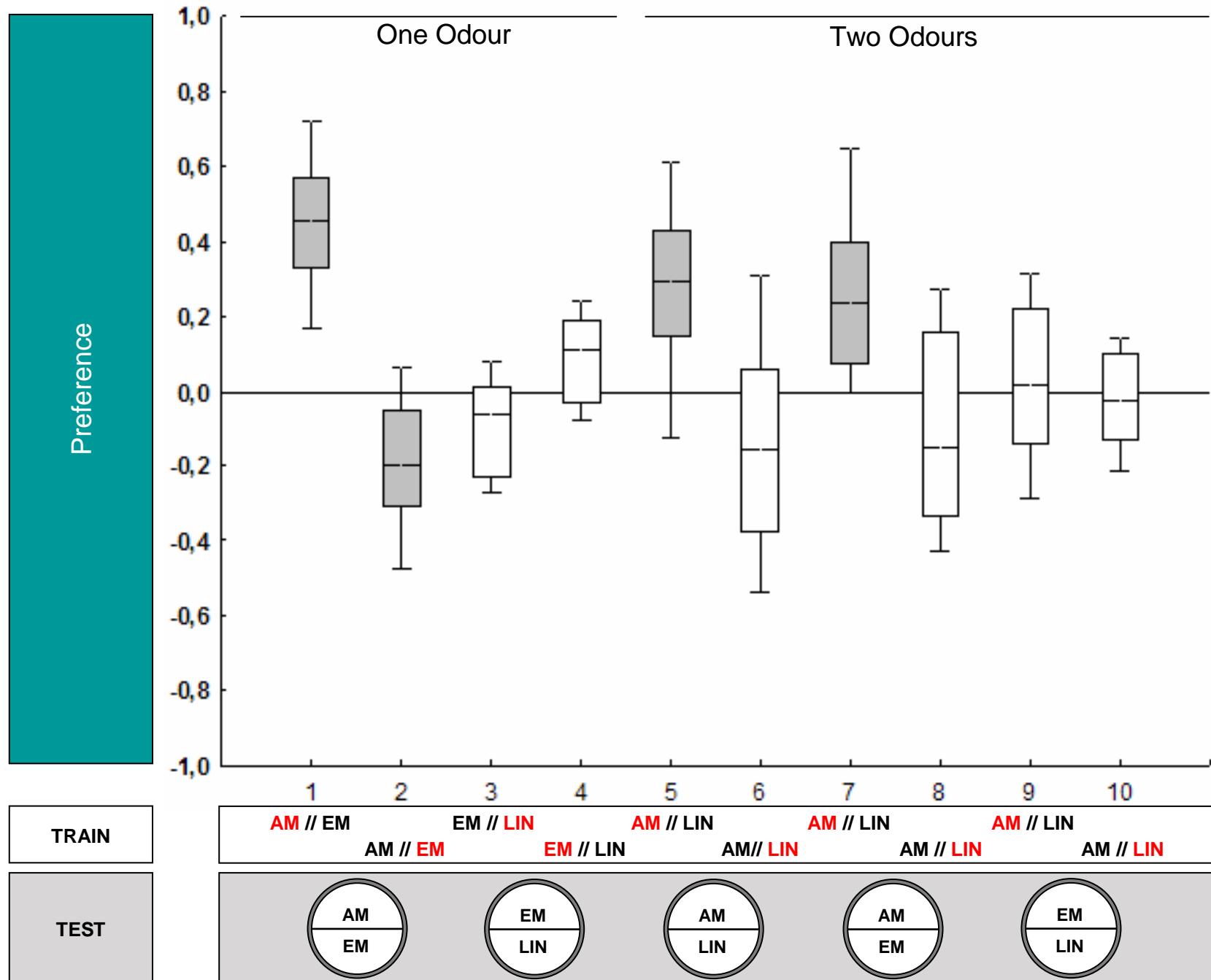
Suppl Figure 1C



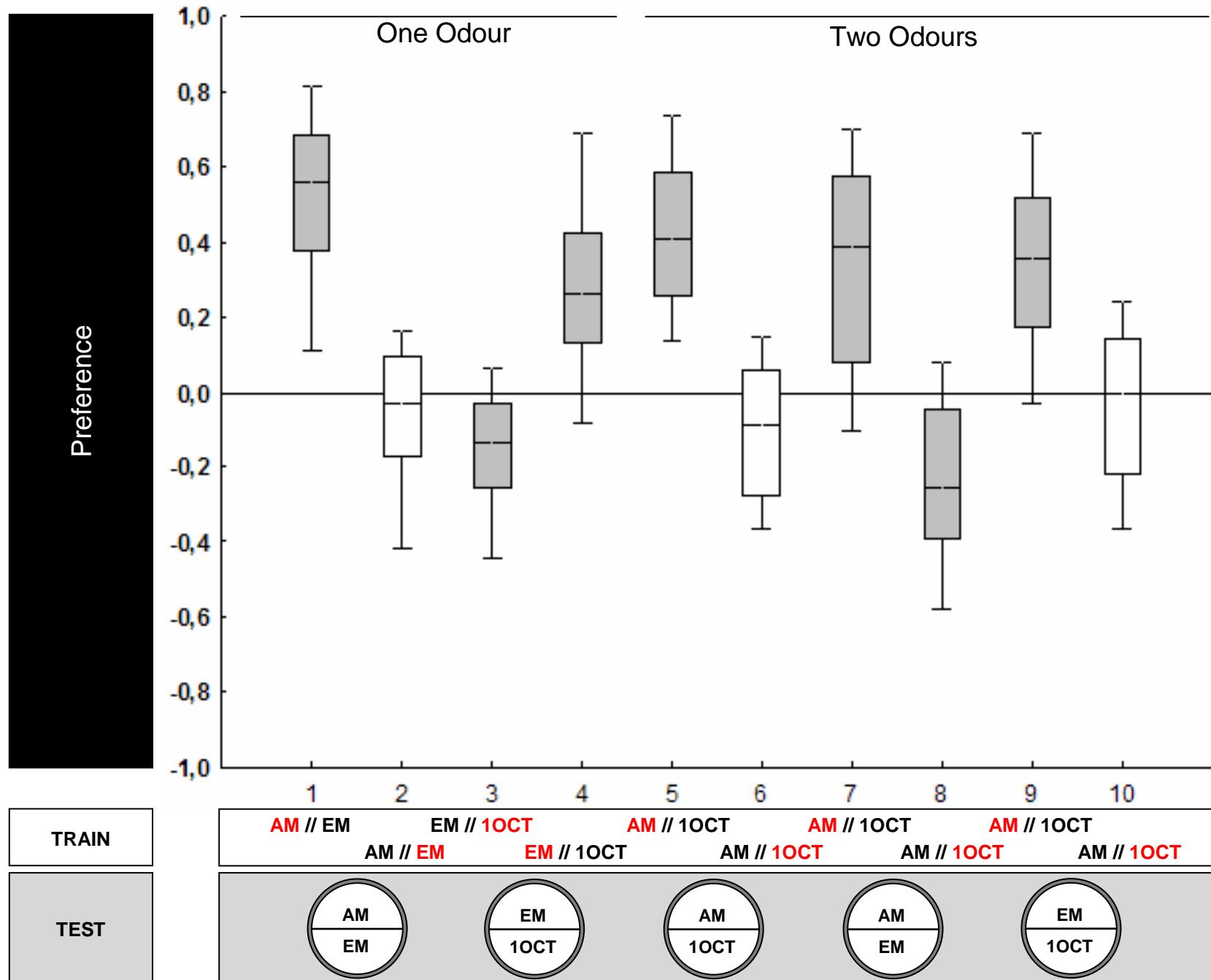
Suppl Figure 2



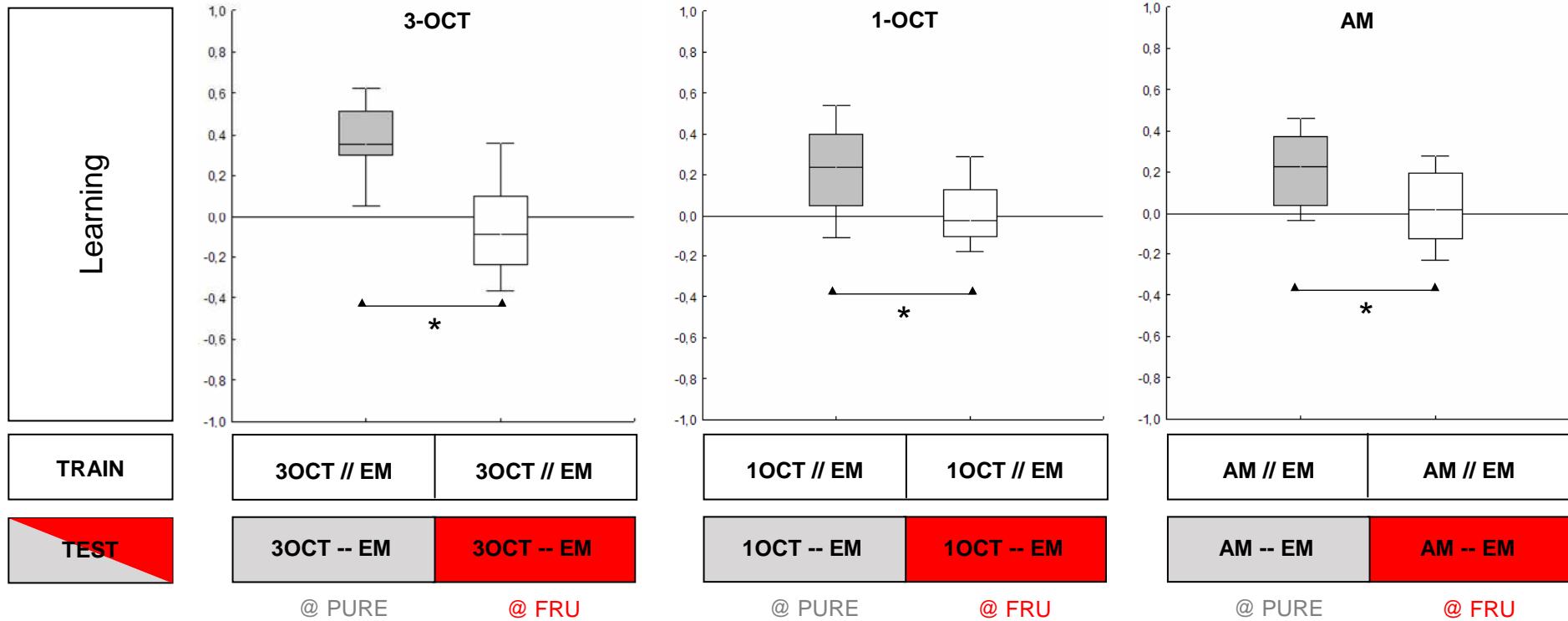
Suppl Figure 3



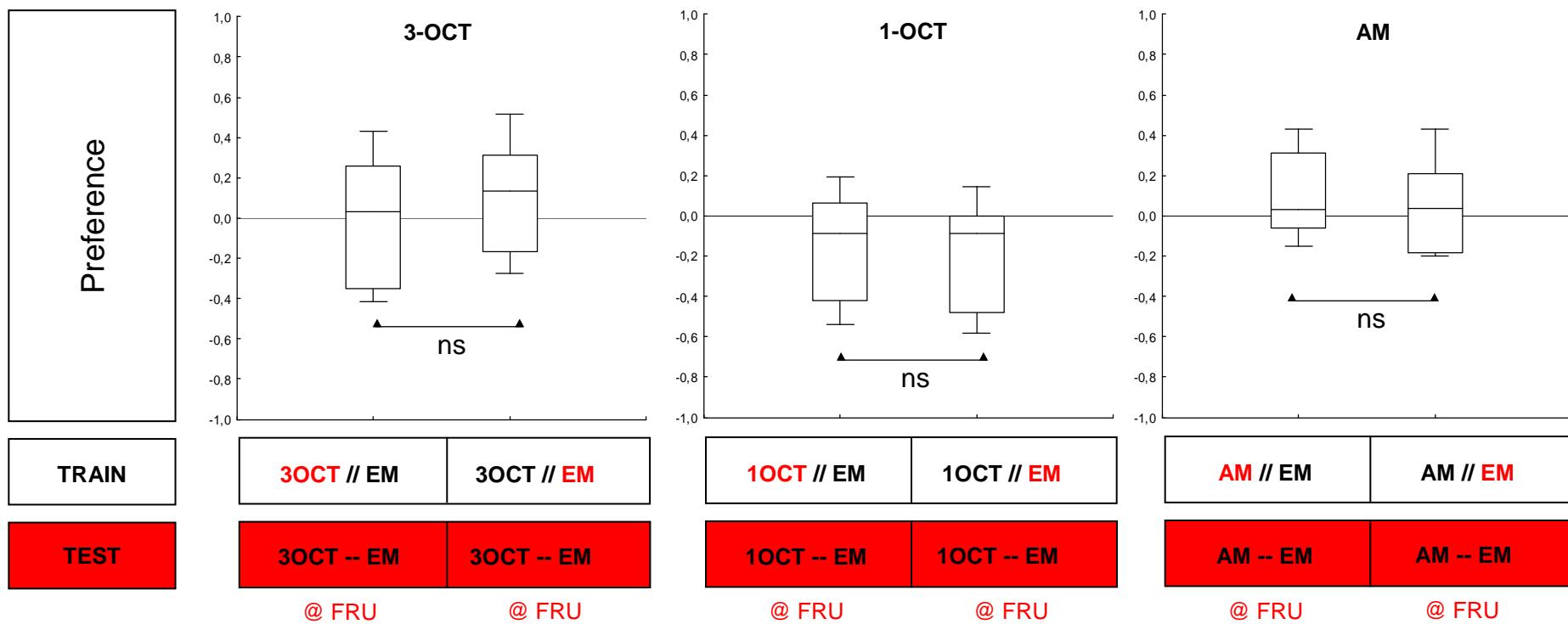
Suppl Figure 4



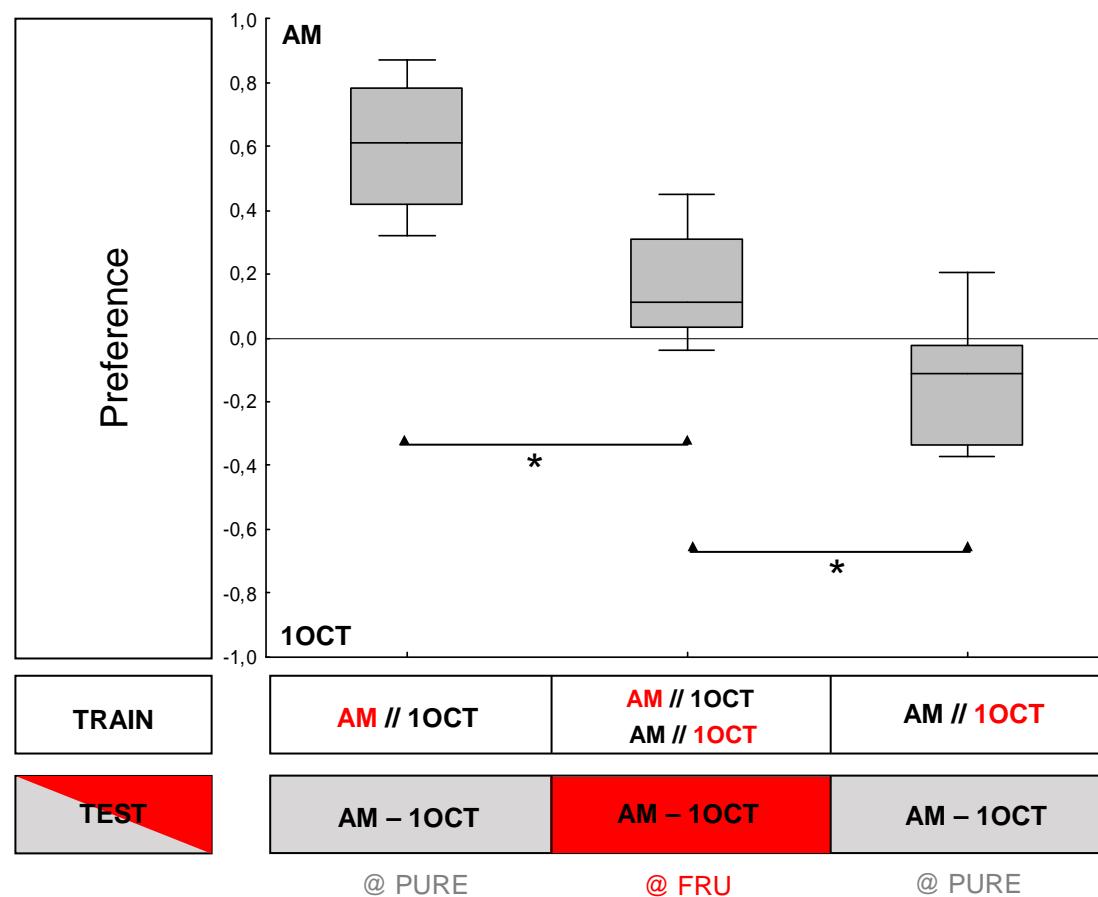
Suppl Figure 5



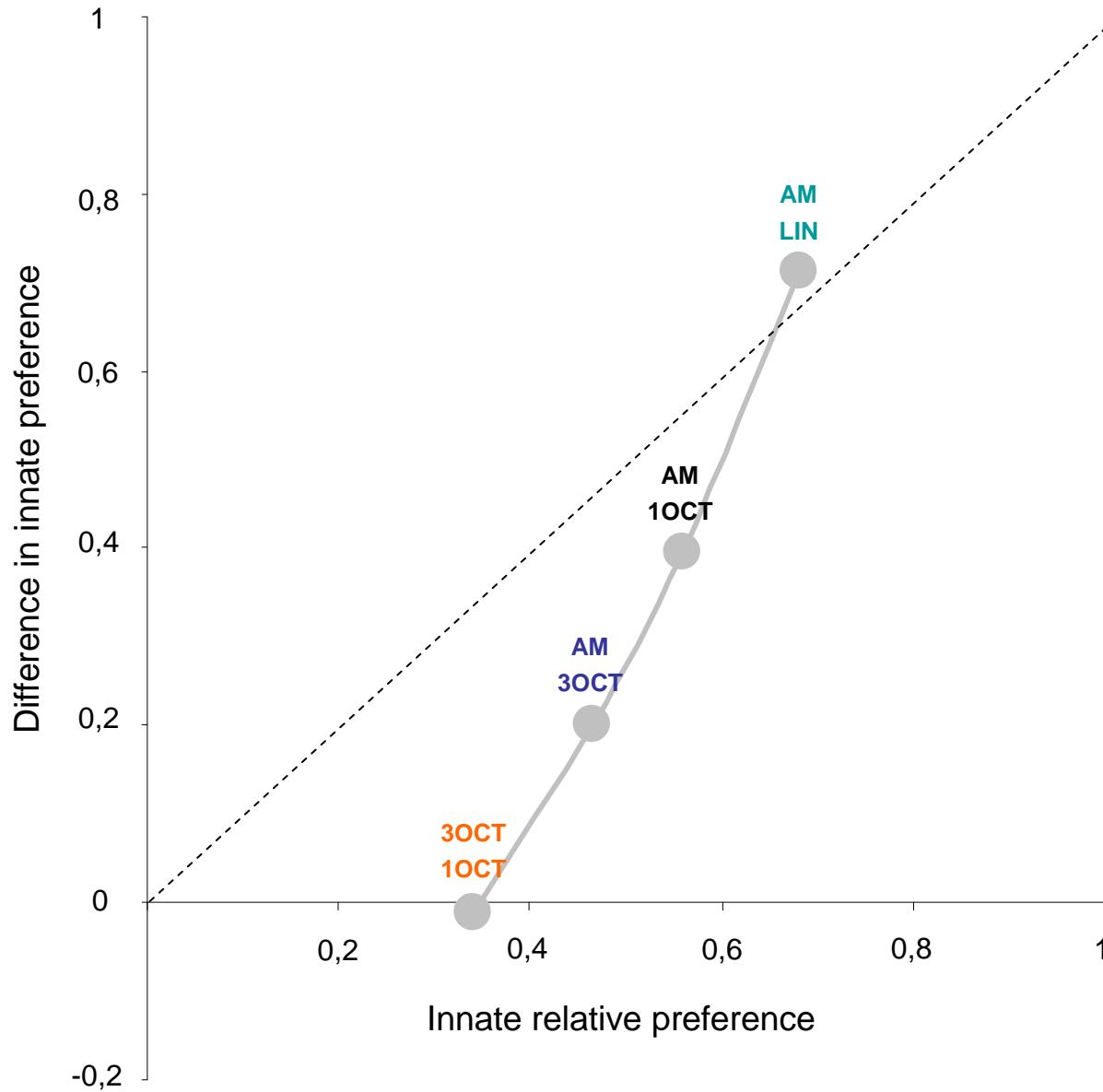
Suppl Figure 6A



Suppl Figure 6B



Suppl Figure 6C



Suppl Figure 7