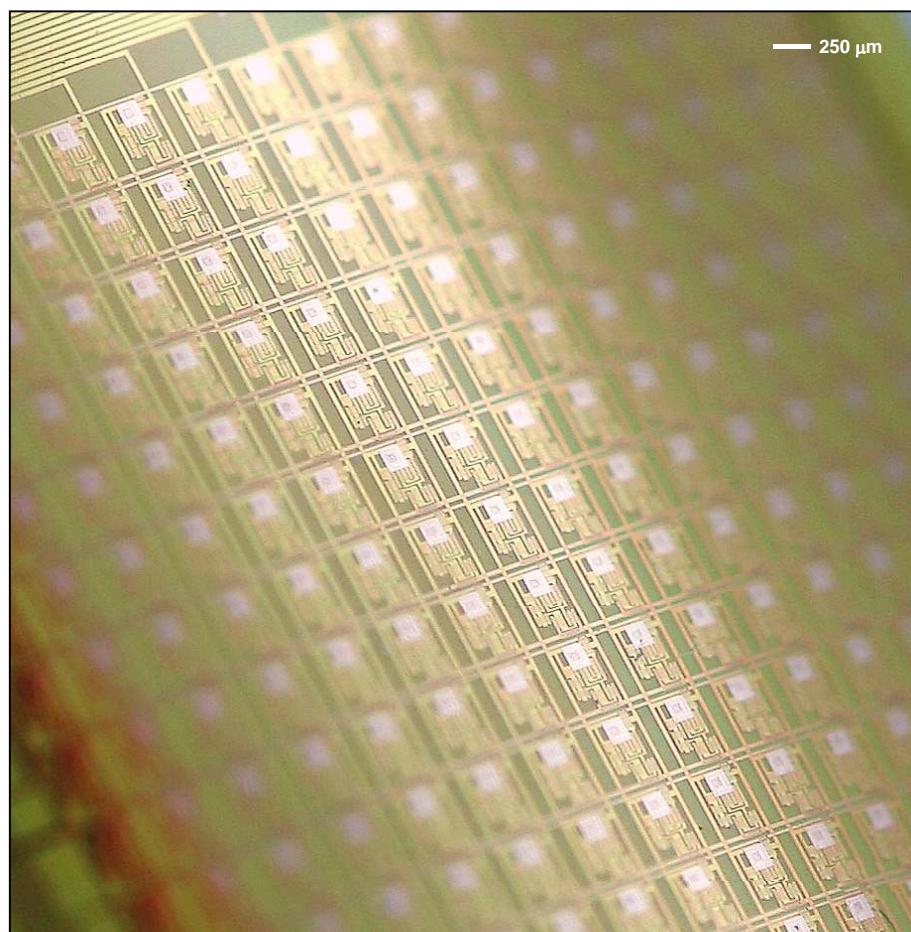
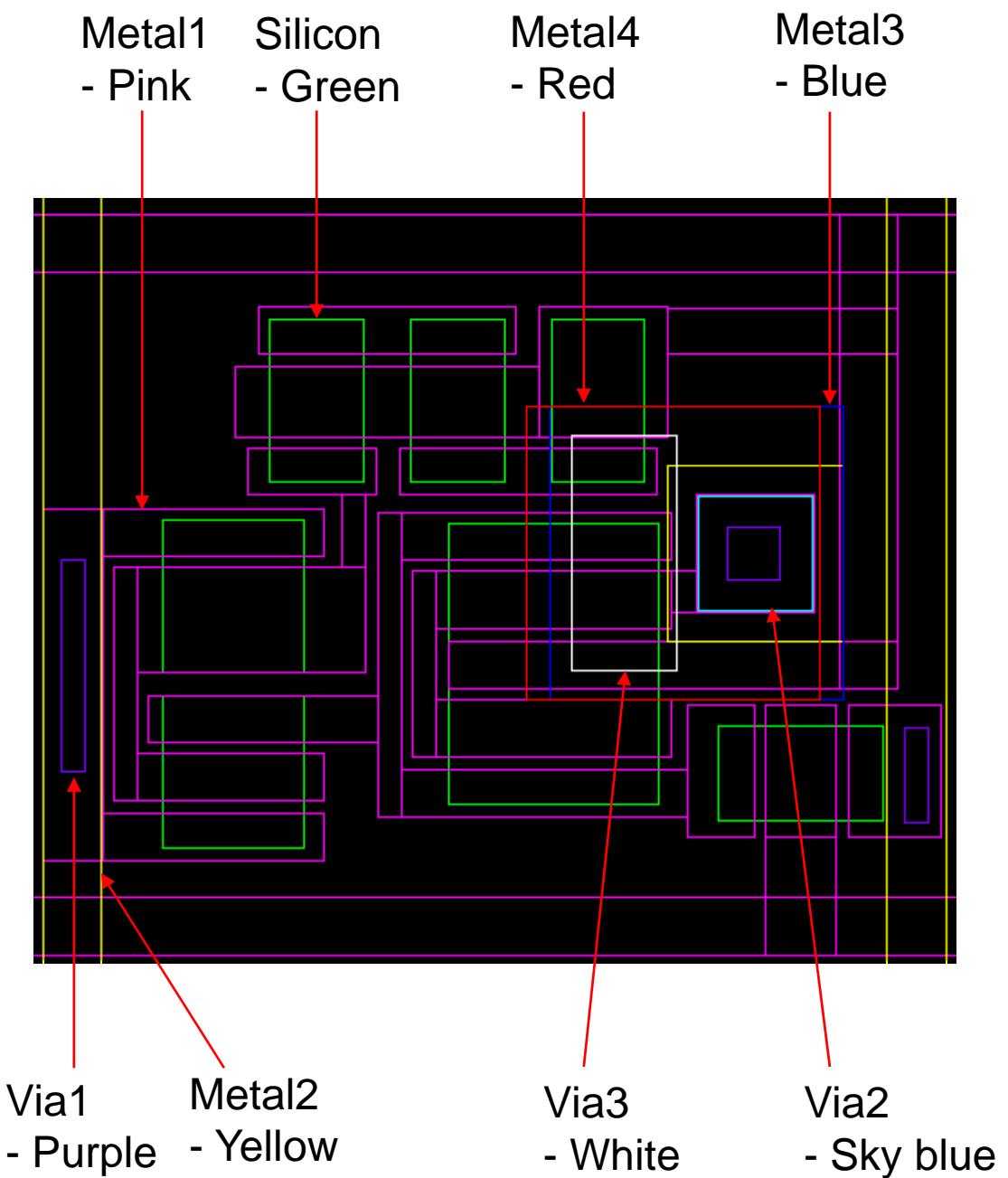


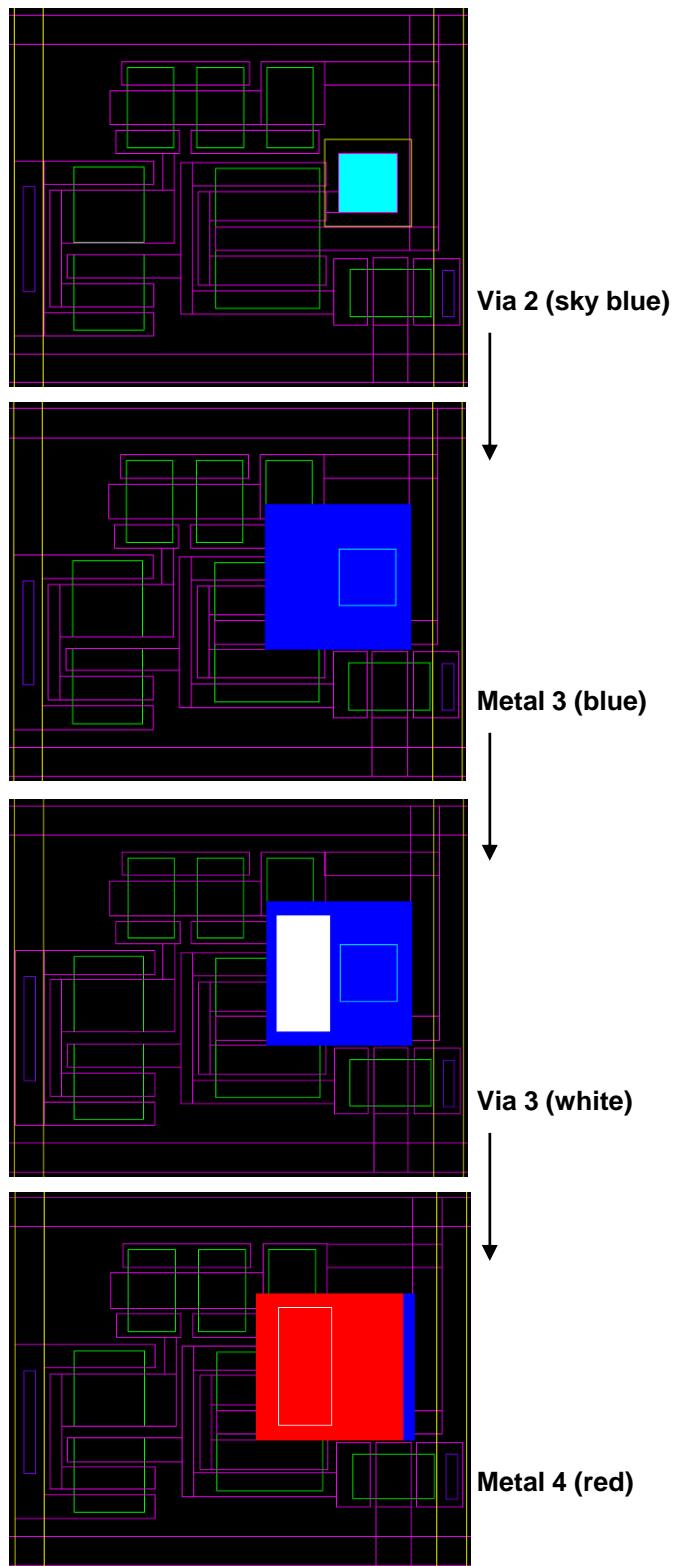
**Fig. S1.** Schematic illustration corresponding to steps for fabricating active, conformal electronics for cardiac electrophysiology mapping. Nine unit cells are shown to illustrate their interconnection at each metal level.



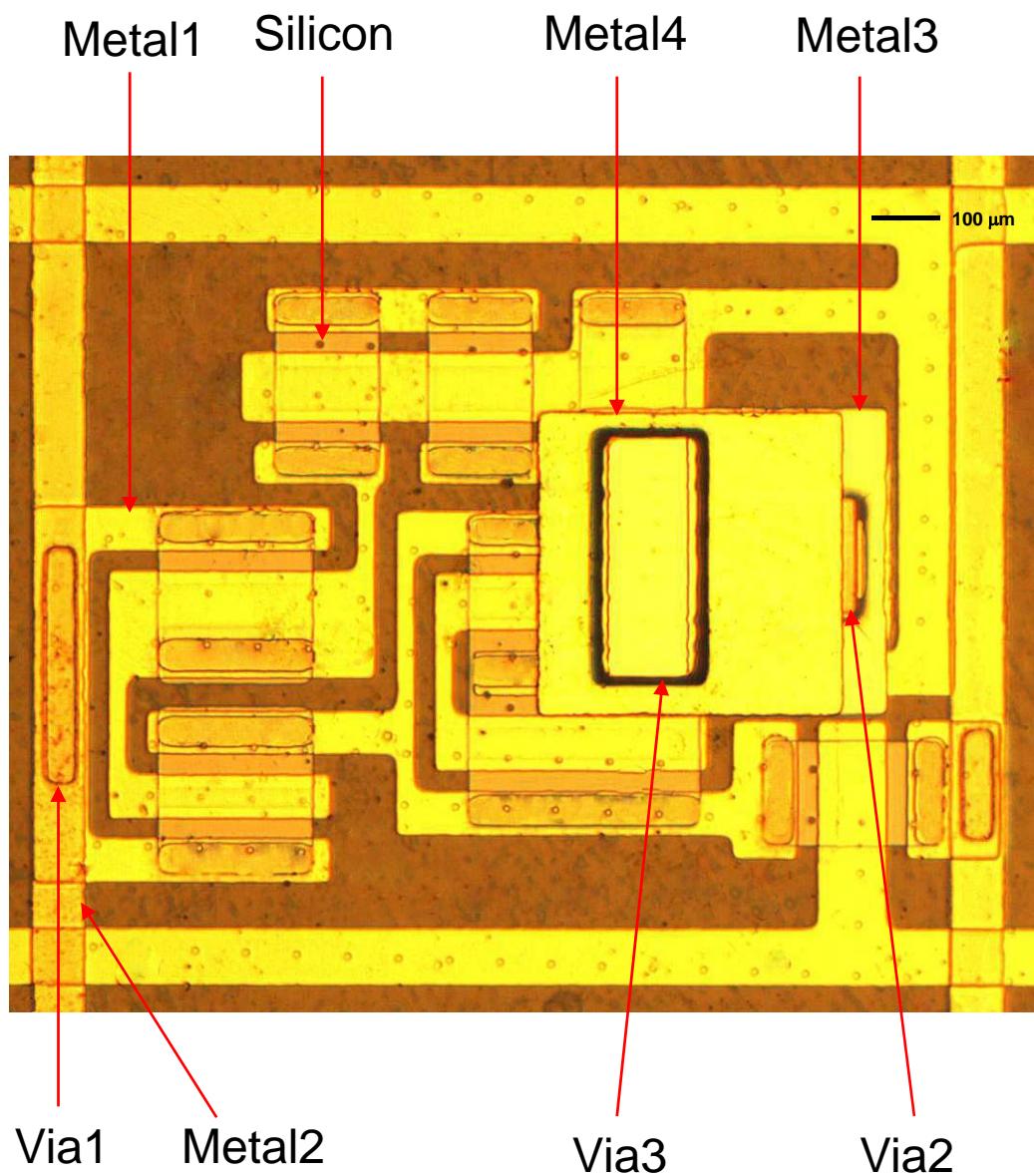
**Fig. S2.** Magnified view of a completed device, in a slightly bent state to illustrate detail.



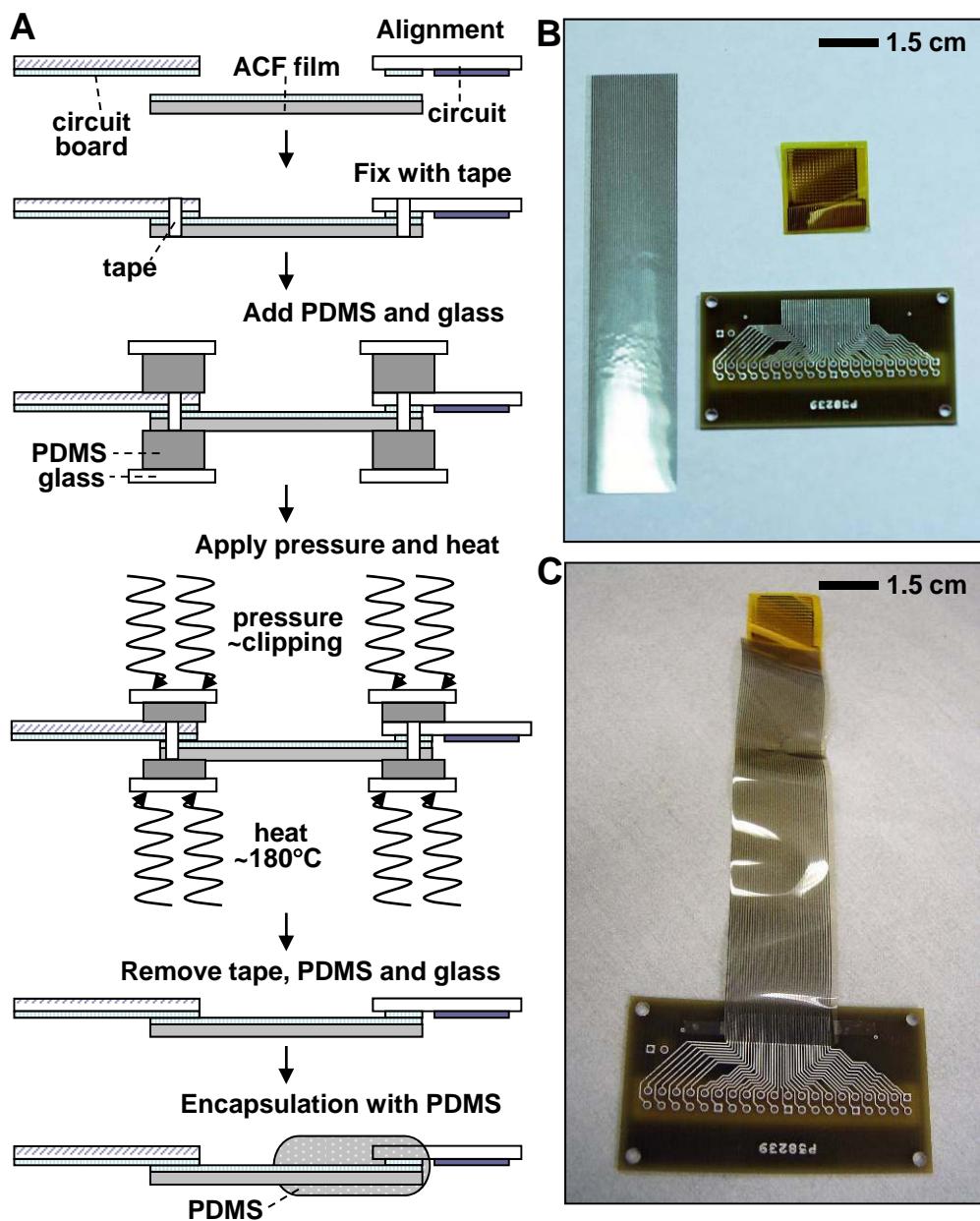
**Fig. S3.** Physical layout of a single unit cell. Additional insulation layers are added to prevent leakage current in saline solution. The green boxes correspond to the isolated silicon active regions that are connected by a first metal layer, shown in pink.



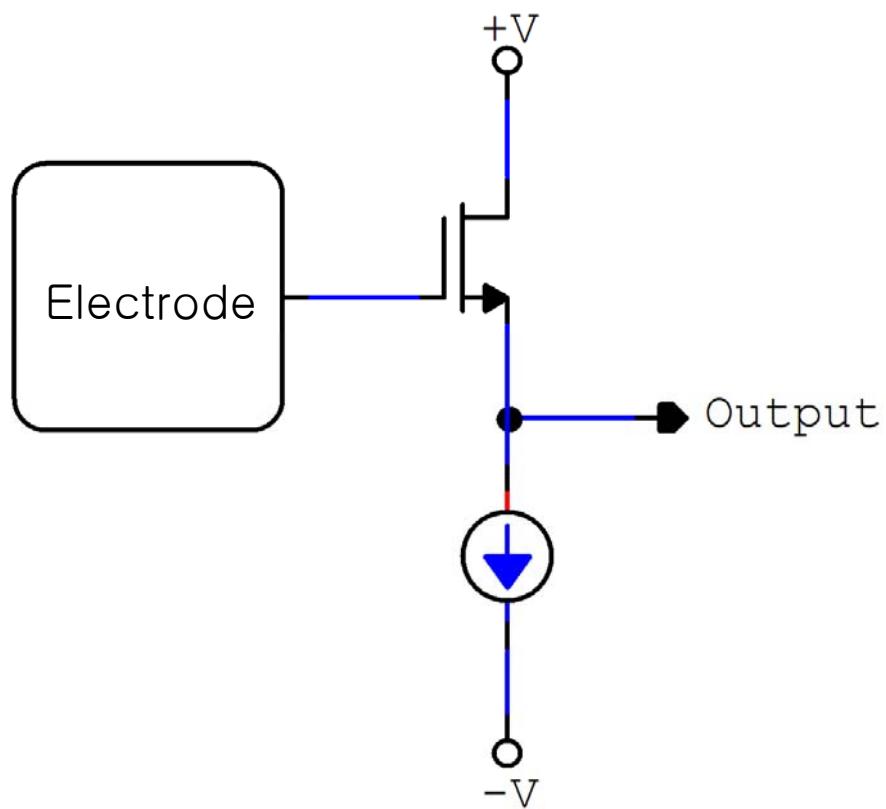
**Fig. S4.** Sequential process of trilayer organic/inorganic stack fabrication.



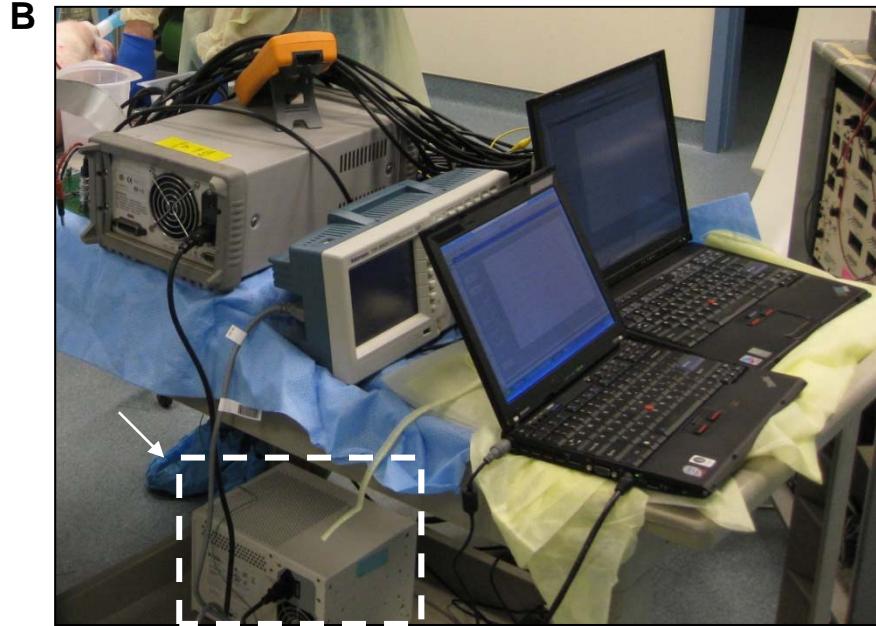
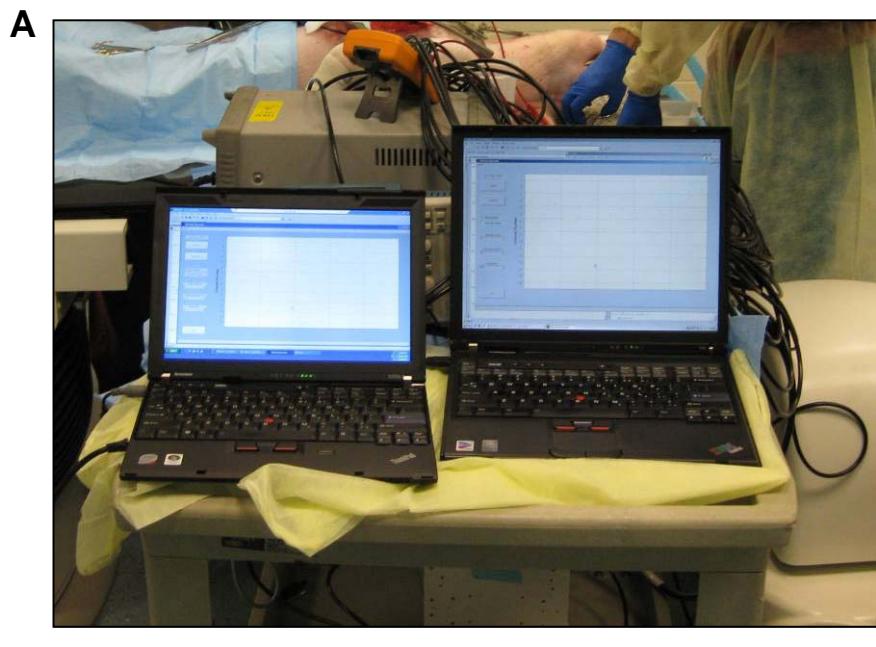
**Fig. S5.** Optical microscope image of a single unit cell with completed insulation layers.



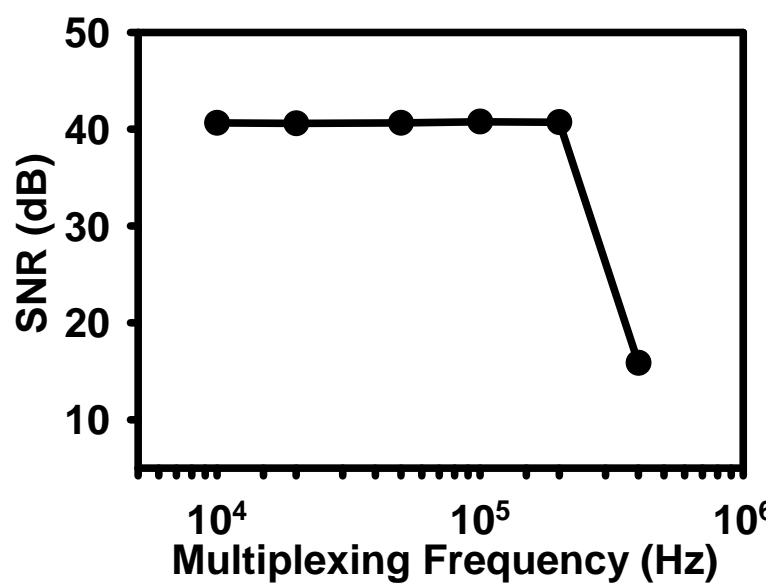
**Fig. S6.** ACF connection process. (A) Schematic diagram. Image of flexible electrode array, ACF film and the circuit board before (B) and after (C) heat seal connection.



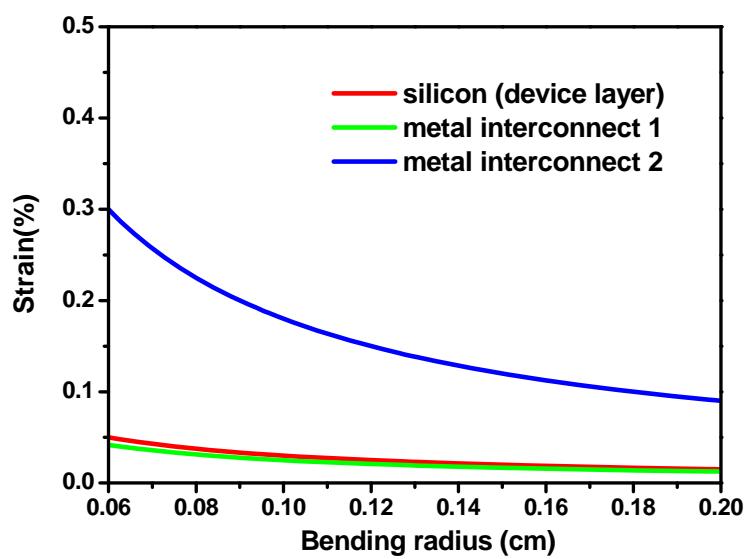
**Fig. S7.** Simplified schematic diagram of a source-follower buffer amplifier, as utilized in the basic unit cell design.



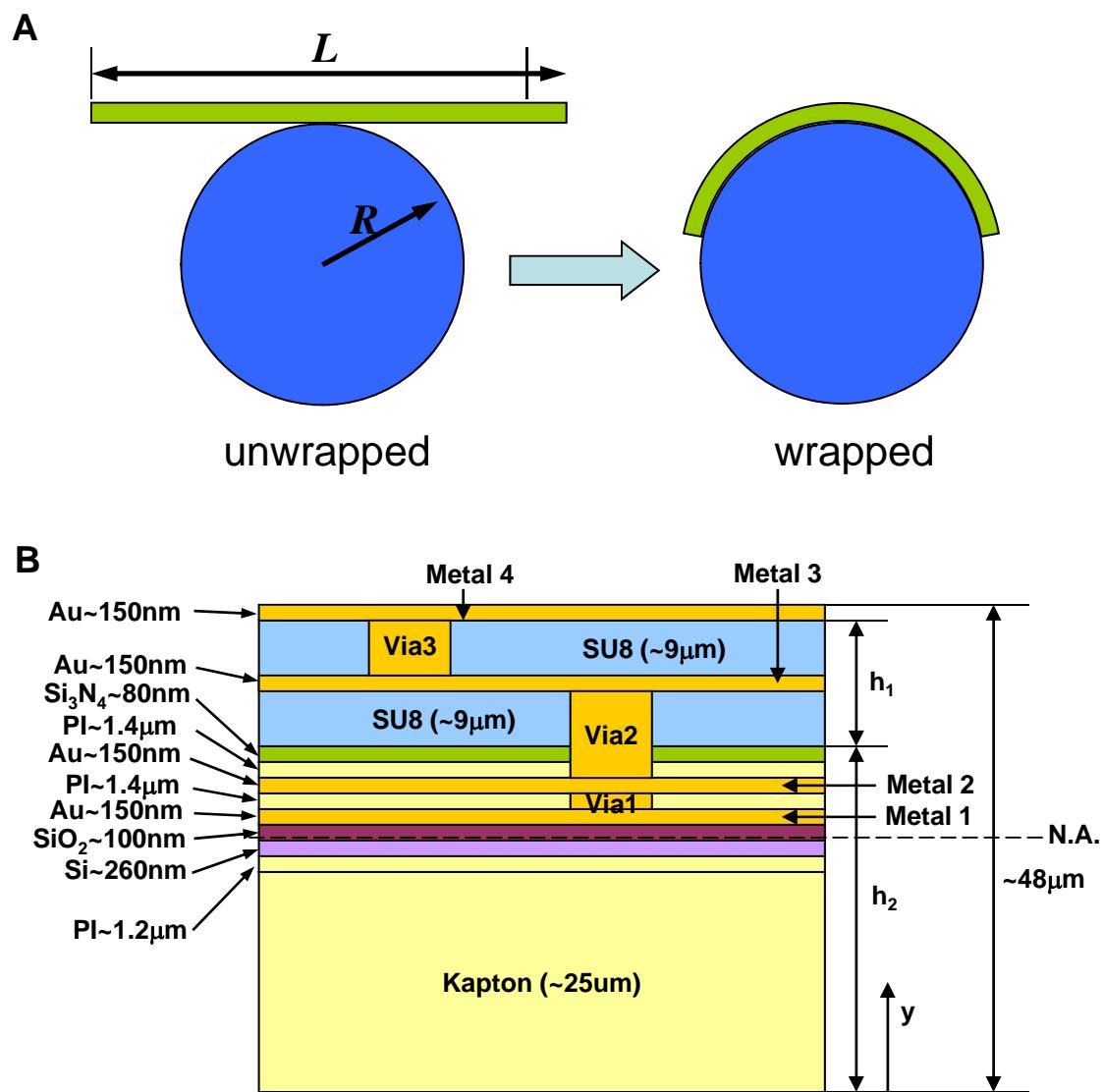
**Fig. S8.** Image of acquisition system during the animal experiment (National Instruments, USA). **(A)** Front view. **(B)** Side view.



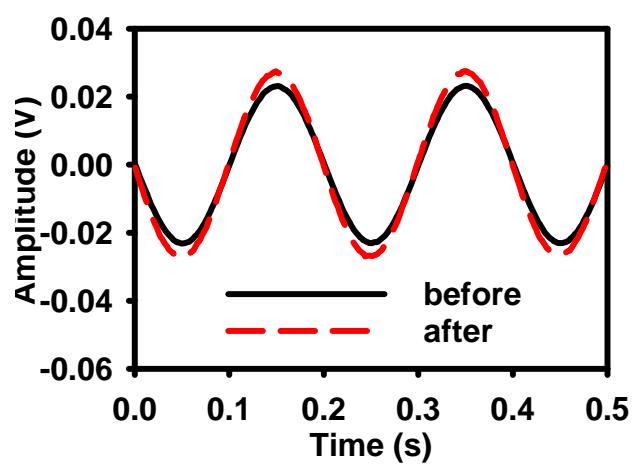
**Fig. S9.** SNR dependence on multiplexing frequency for a 20 Hz test signal.



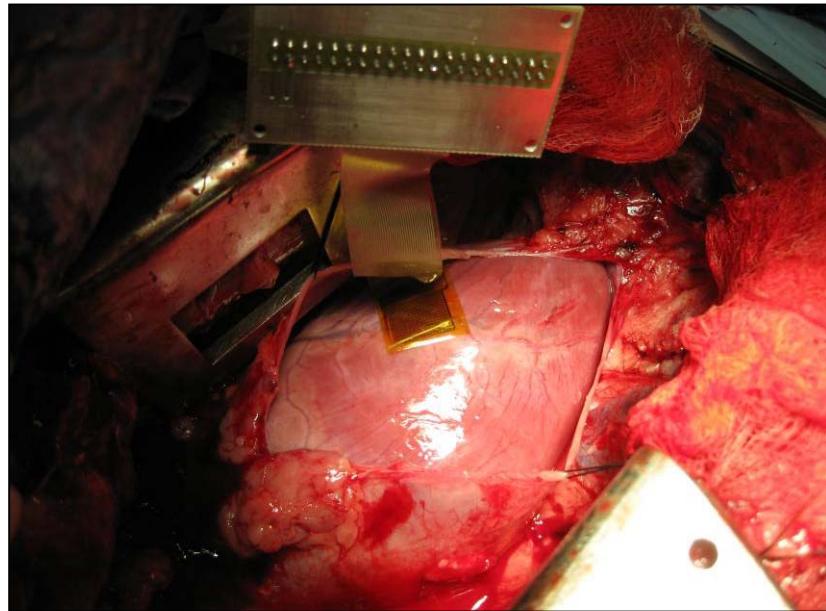
**Fig. S10.** Calculated induced strain in each layer of the device during tight folding.



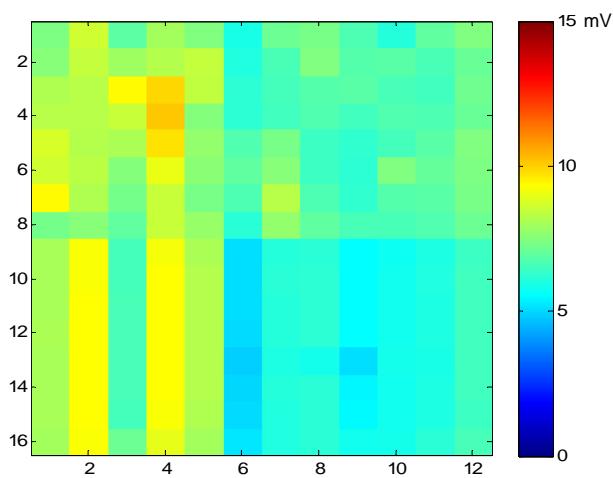
**Fig. S11.** Schematic diagram of wrapping model (**A**) and cross-sectional view of sensor (**B**).



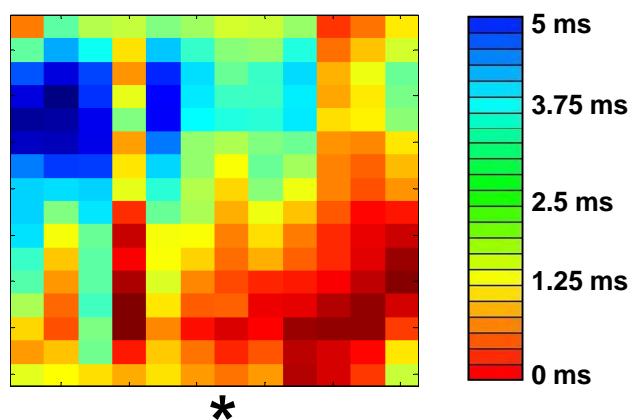
**Fig. S12.** Sine wave response (at 5 Hz) before and after saline immersion for 3 hours.

**A****B**

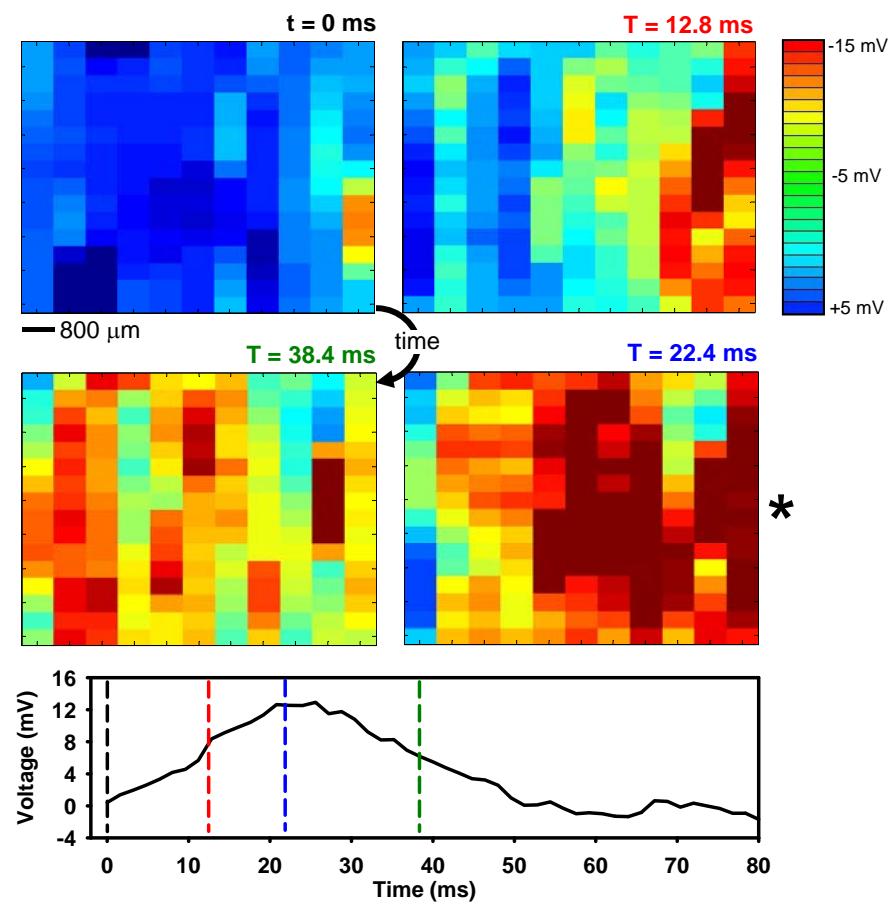
**Fig. S13.** Images of experiment with porcine animal model. (A) Surgical setup and photograph of flexible device conforming to the cardiac tissue via surface tension (B).



**Fig. S14.** Color map illustrating the amplitude uniformity of all of the channels. The average peak amplitude of the cardiac activation cycle was plotted.



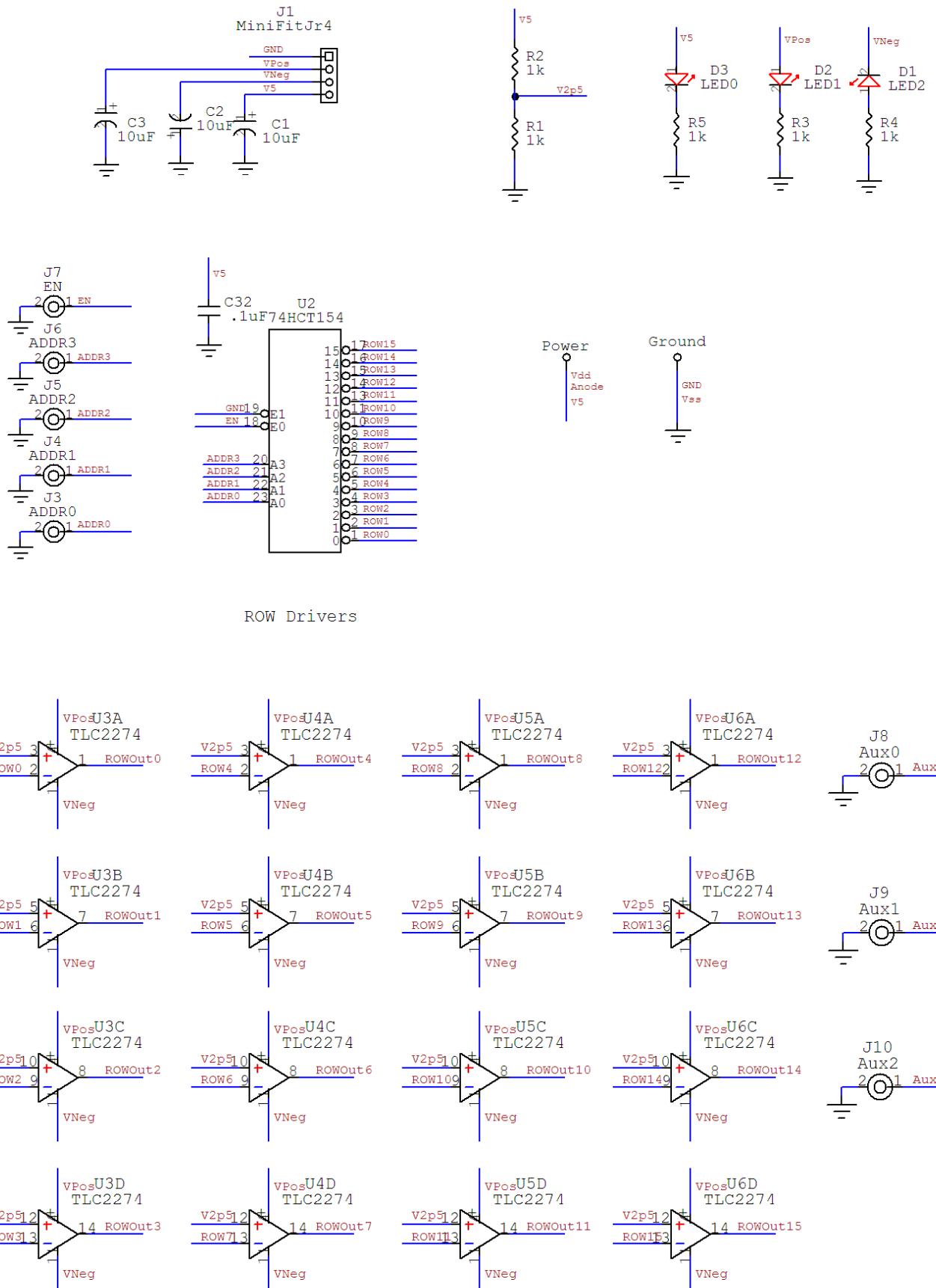
**Fig. S15.** Isochronal activation map with pacing. The relative pacing electrode location is indicated by an asterisk (\*). Data from 3 columns in A and 6 columns in B have been removed due to failures in the metal interconnections.



**Fig. S16.** Representative voltage data for all electrodes at four points in time showing paced cardiac wave front propagation. The relative pacing electrode location is indicated by an asterisk (\*). Voltage is plotted using the color scale in the right corner. The bottom frame shows the average voltage from all electrodes. The dashed color lines illustrate the points in time at which each frame was taken. Note that negative is plotted up by convention.

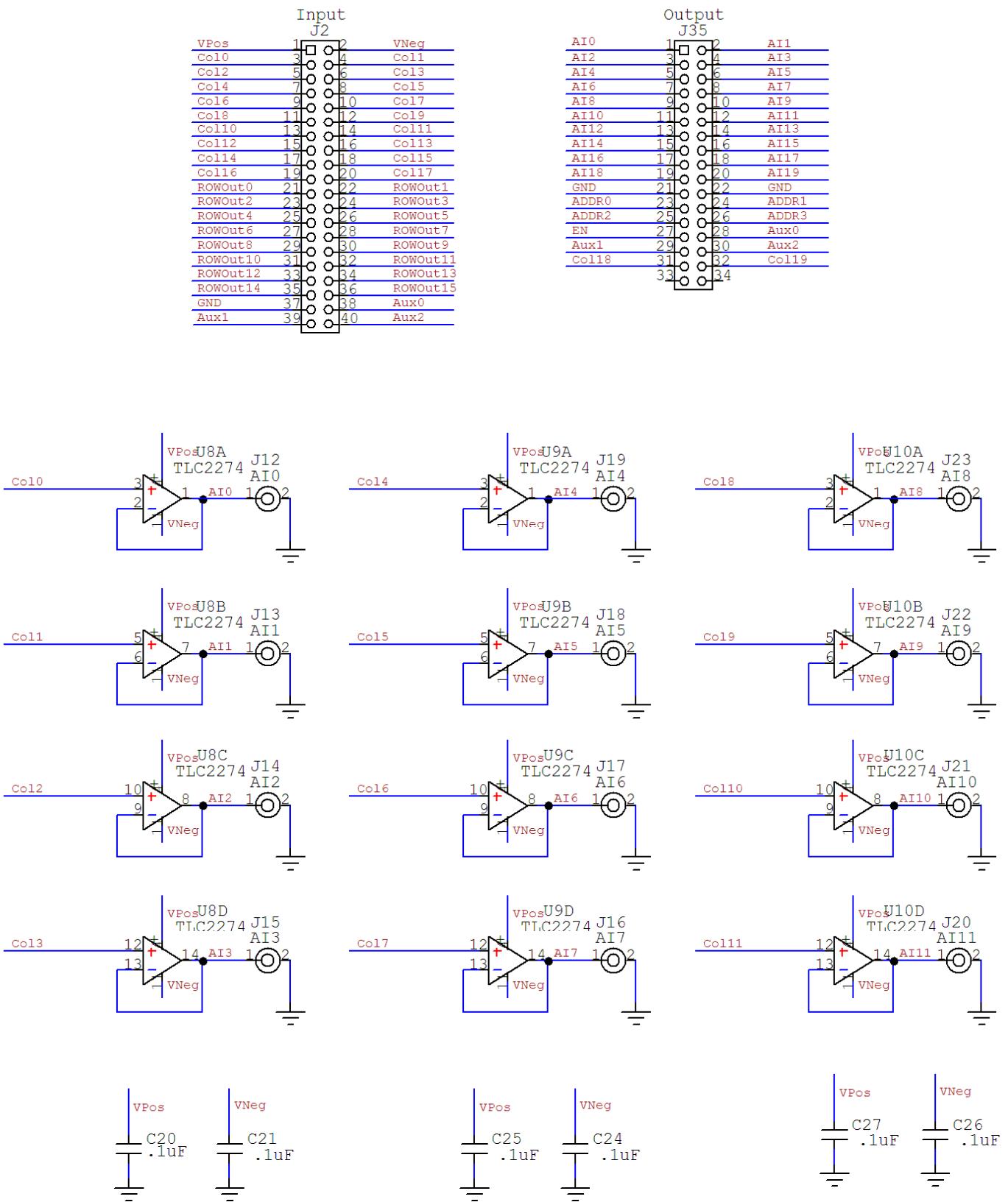


**Fig. S17.** Design of the adapter circuit board, which adapts the ACF ribbon to a 40 pin connector.

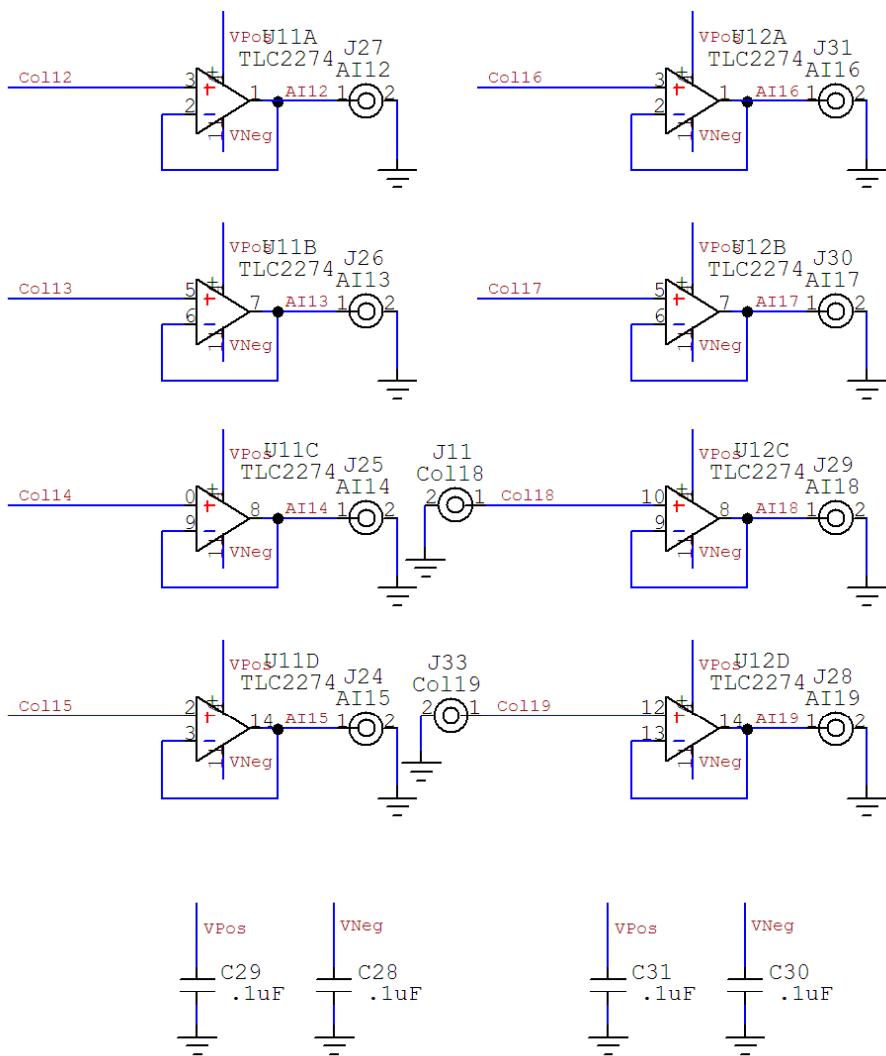


Alt: TLV2374

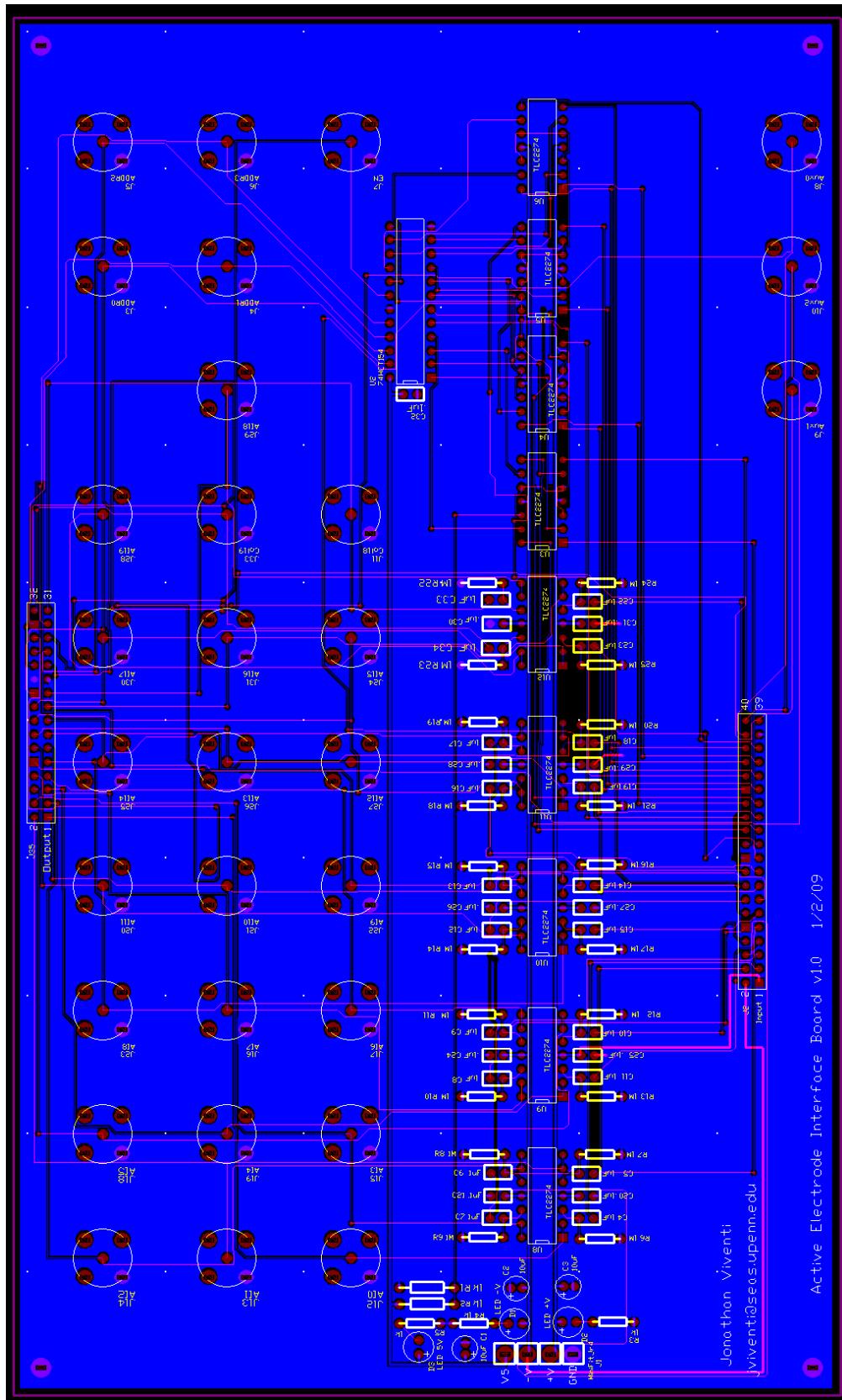
**Fig S18.** Schematic design and layout of the main interface circuit board, which connects the 40 pin ribbon cable to the acquisition system.



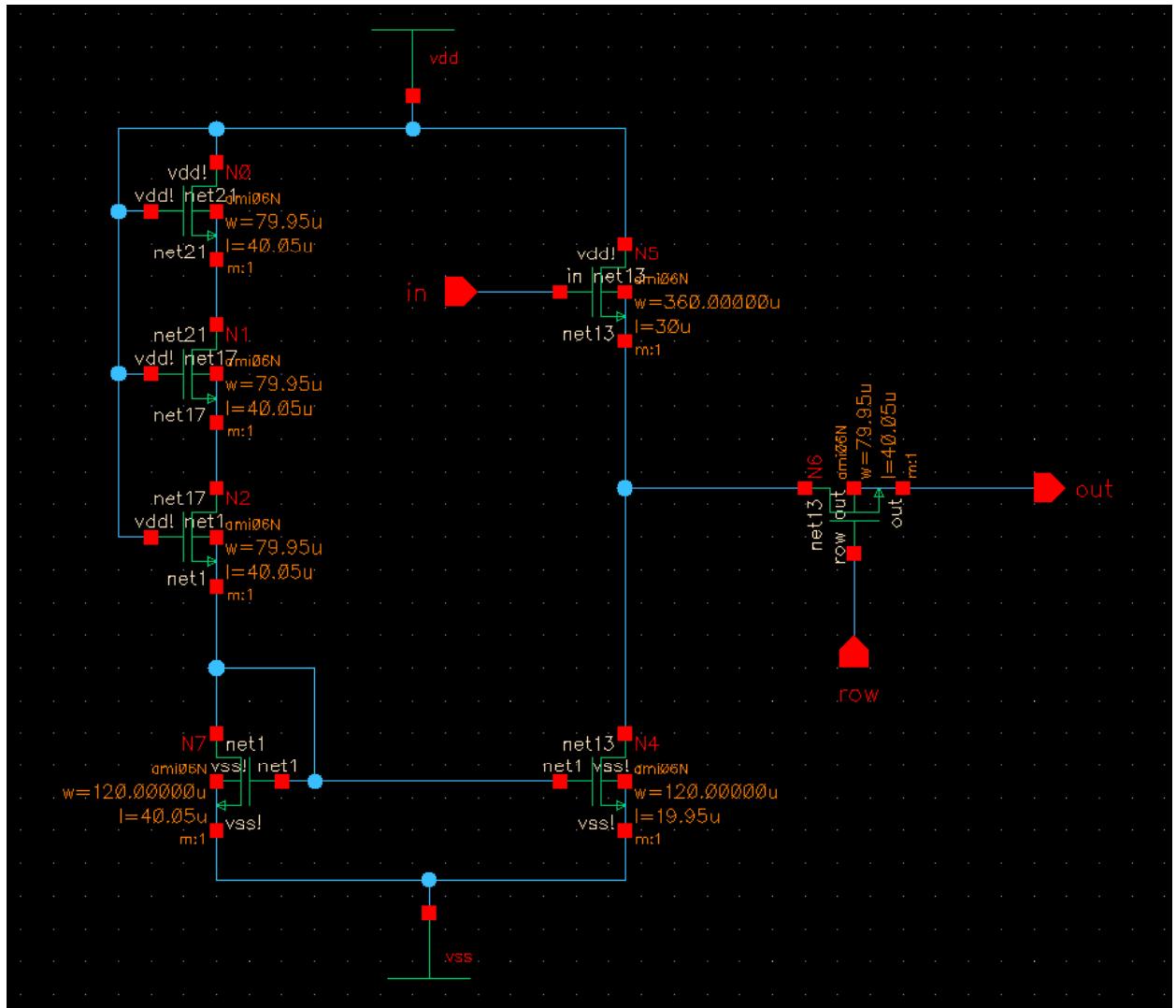
**Fig S19.** Schematic design and layout of the main interface circuit board, which connects the 40 pin ribbon cable to the acquisition system.



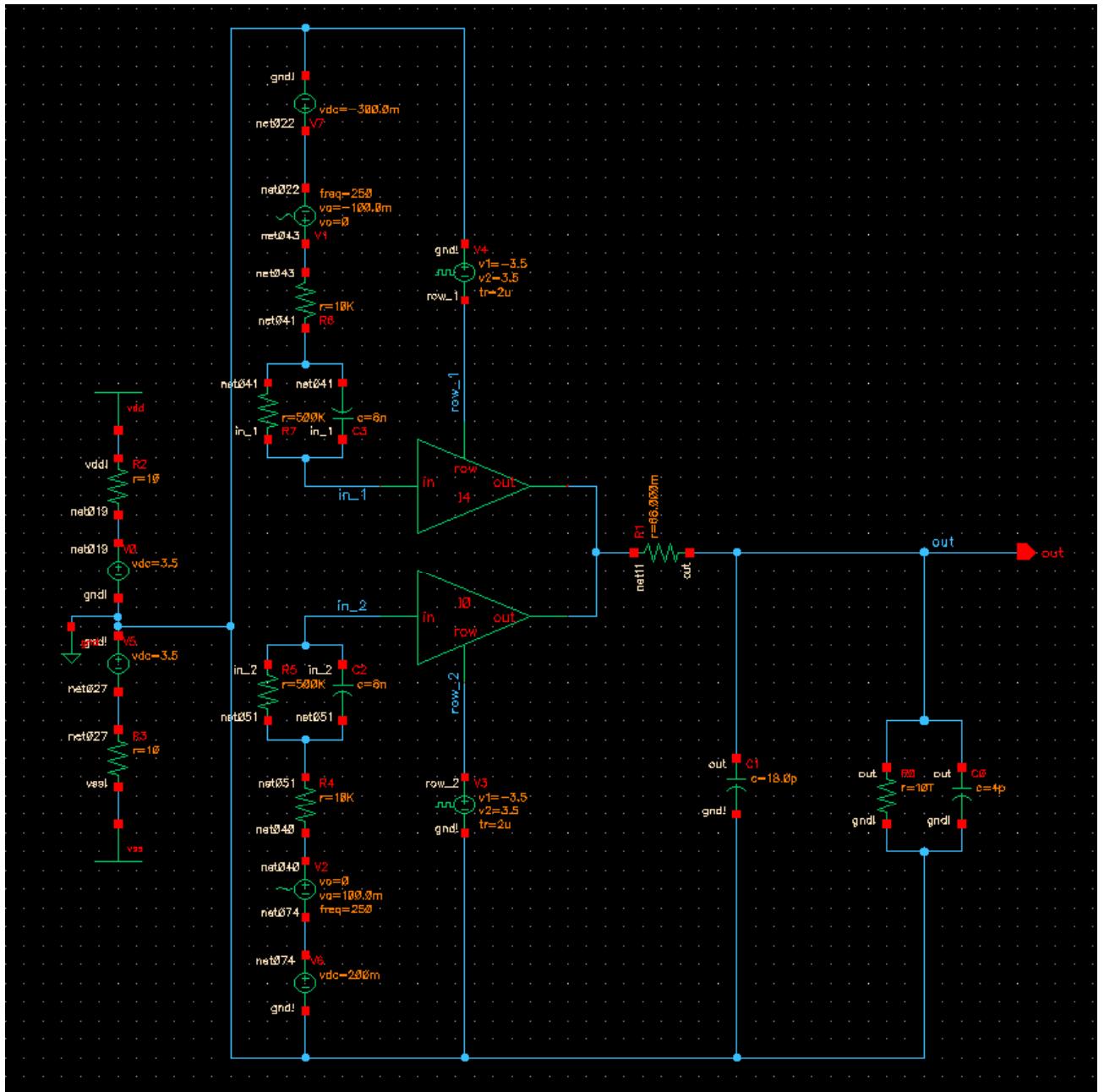
**Fig S20.** Schematic design and layout of the main interface circuit board, which connects the 40 pin ribbon cable to the acquisition system.



**Fig S21.** Schematic design and layout of the main interface circuit board, which connects the 40 pin ribbon cable to the acquisition system.

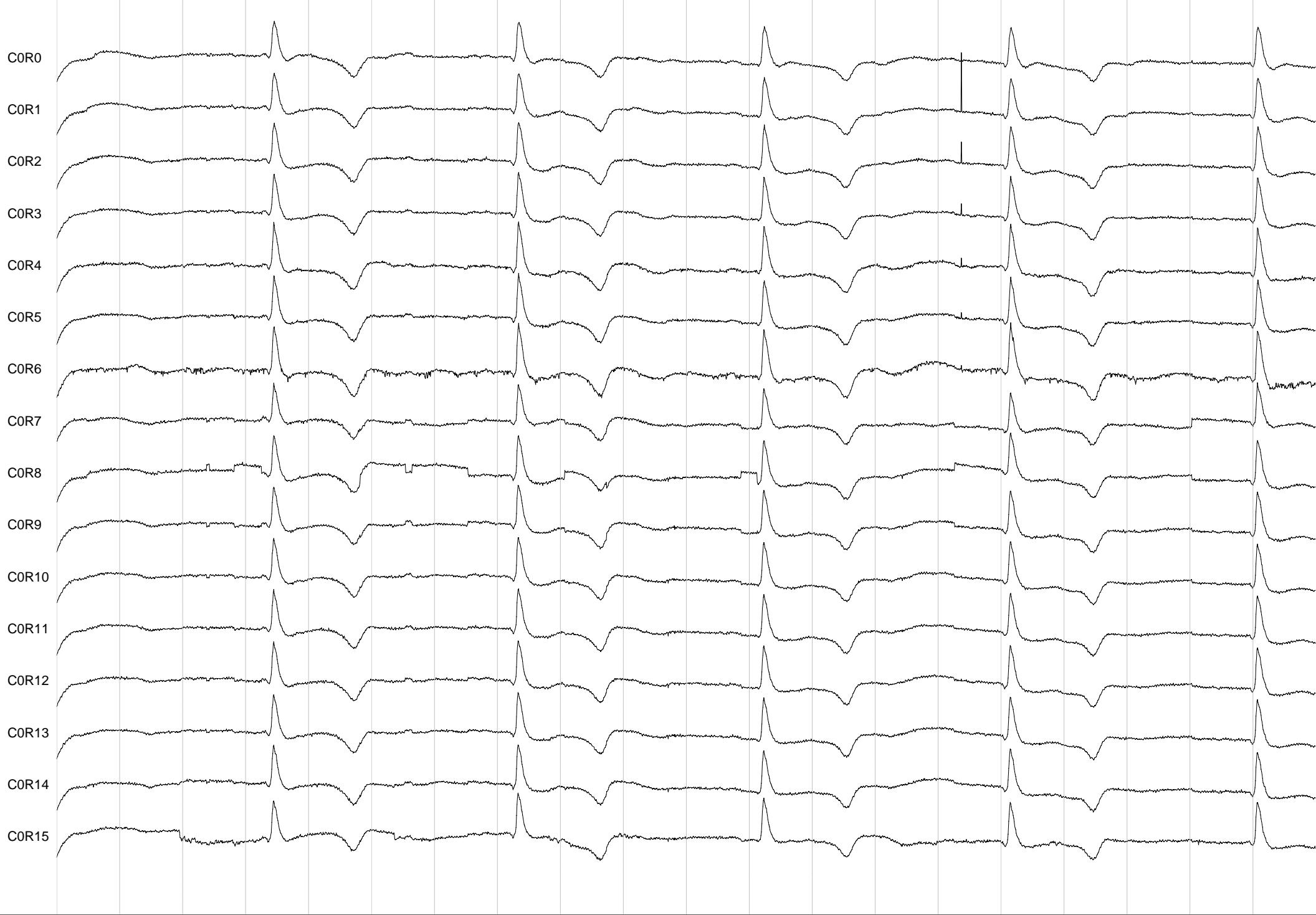


**Fig. S22.** Screenshot from Cadence simulation environment showing the schematic of one unit cell of the array. The dimensions are labeled for each transistor and match the dimensions of the fabricated array.



**Fig. S23.** Screenshot from Cadence simulation environment showing the test setup for two unit cells connected to a common output. The inputs are 200mV peak to peak, 250 Hz sine waves with different offset voltages to measure switching time. The output includes a model of the ribbon cable and the buffer op-amp. The values for the output resistors and capacitors shown in the schematic are 68m ohms, 18 pF, 10T ohms, and 4pF. These values came from specification sheets for the ribbon cable and the TLC2274 op-amp (3, 4). The schematic also includes a model of the electrodes (5). The row select signals alternate with a period of 100us. The rise and fall time for the row select signals are 2  $\mu$ s.

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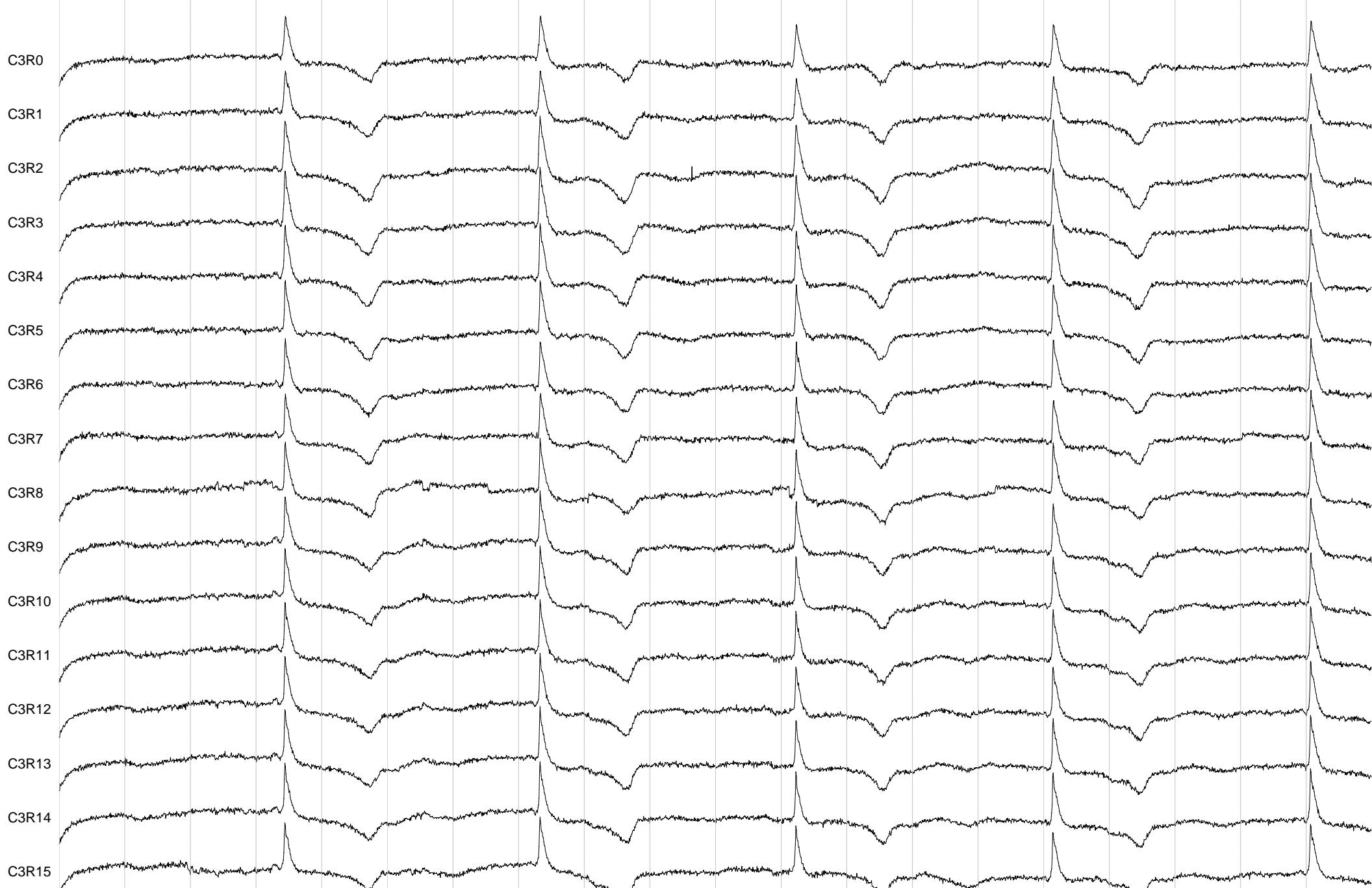
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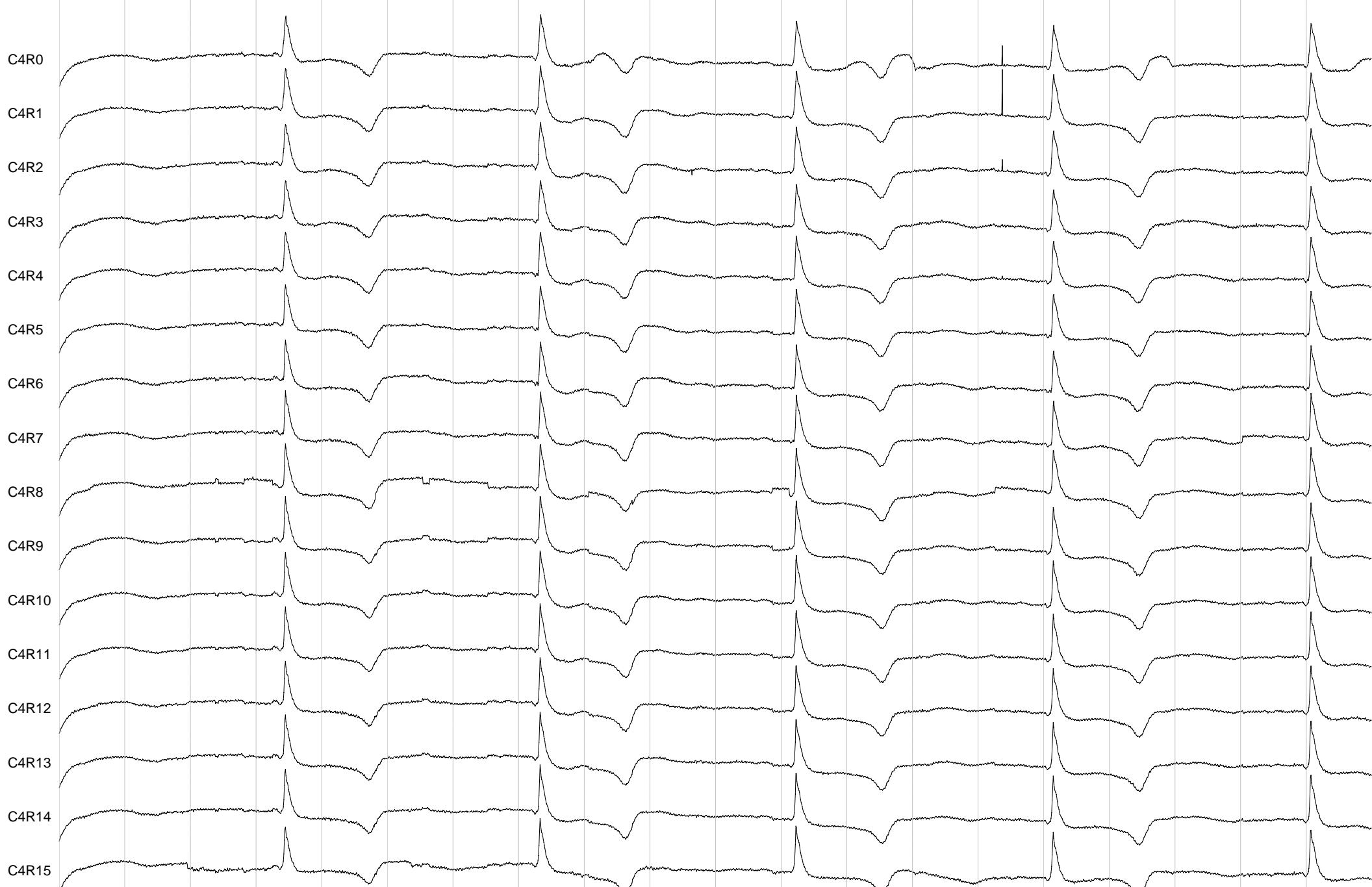
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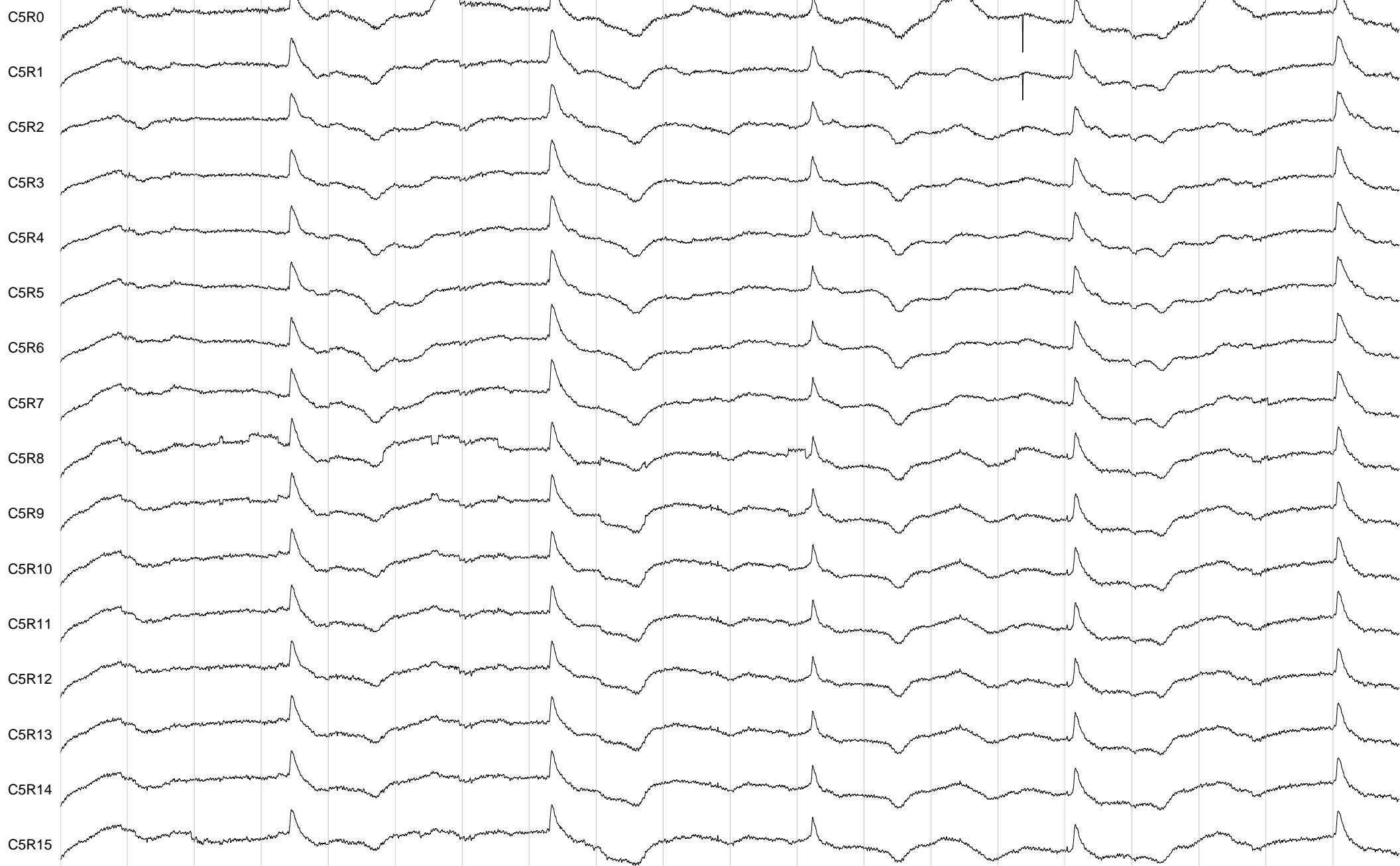
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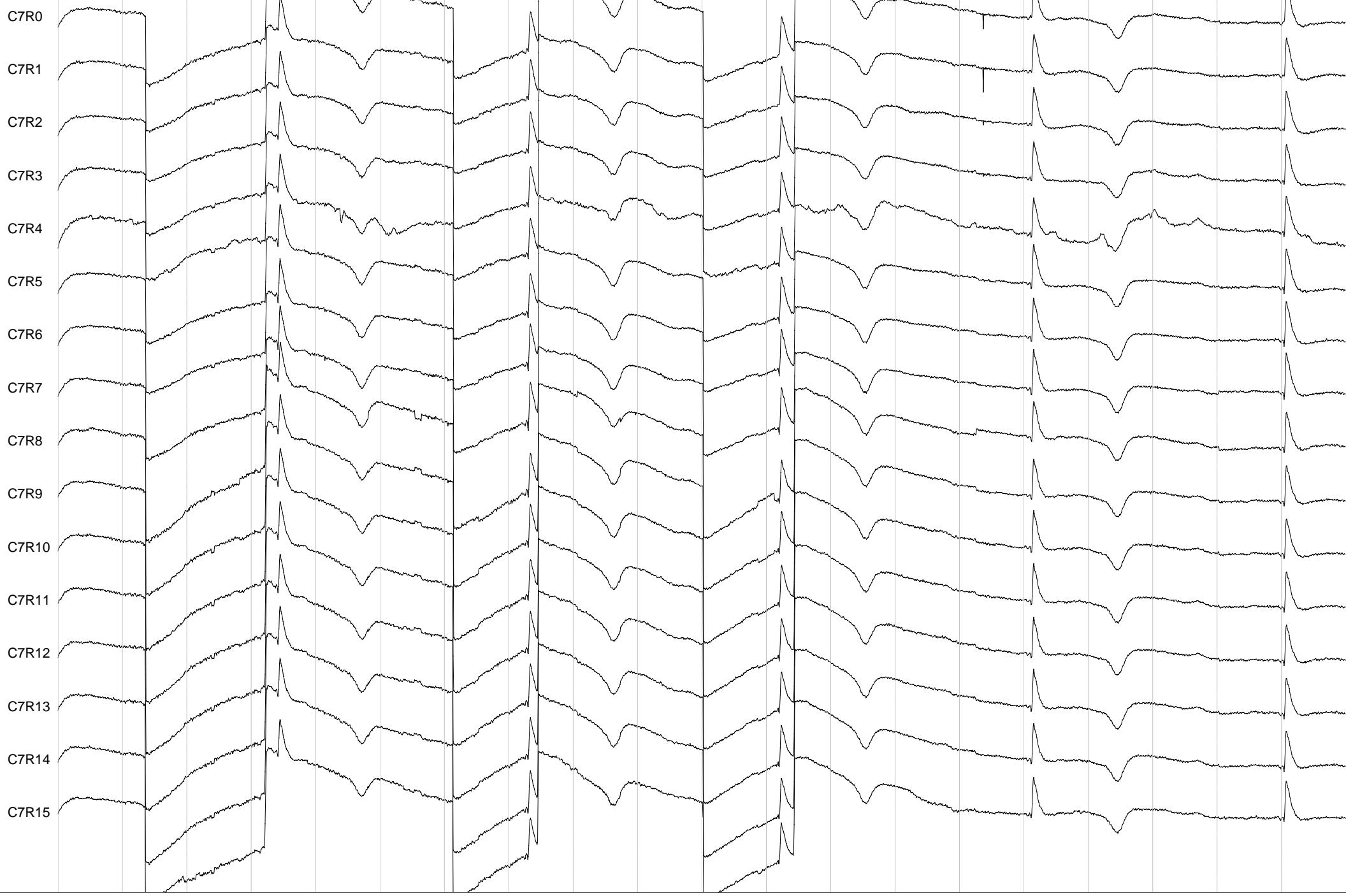
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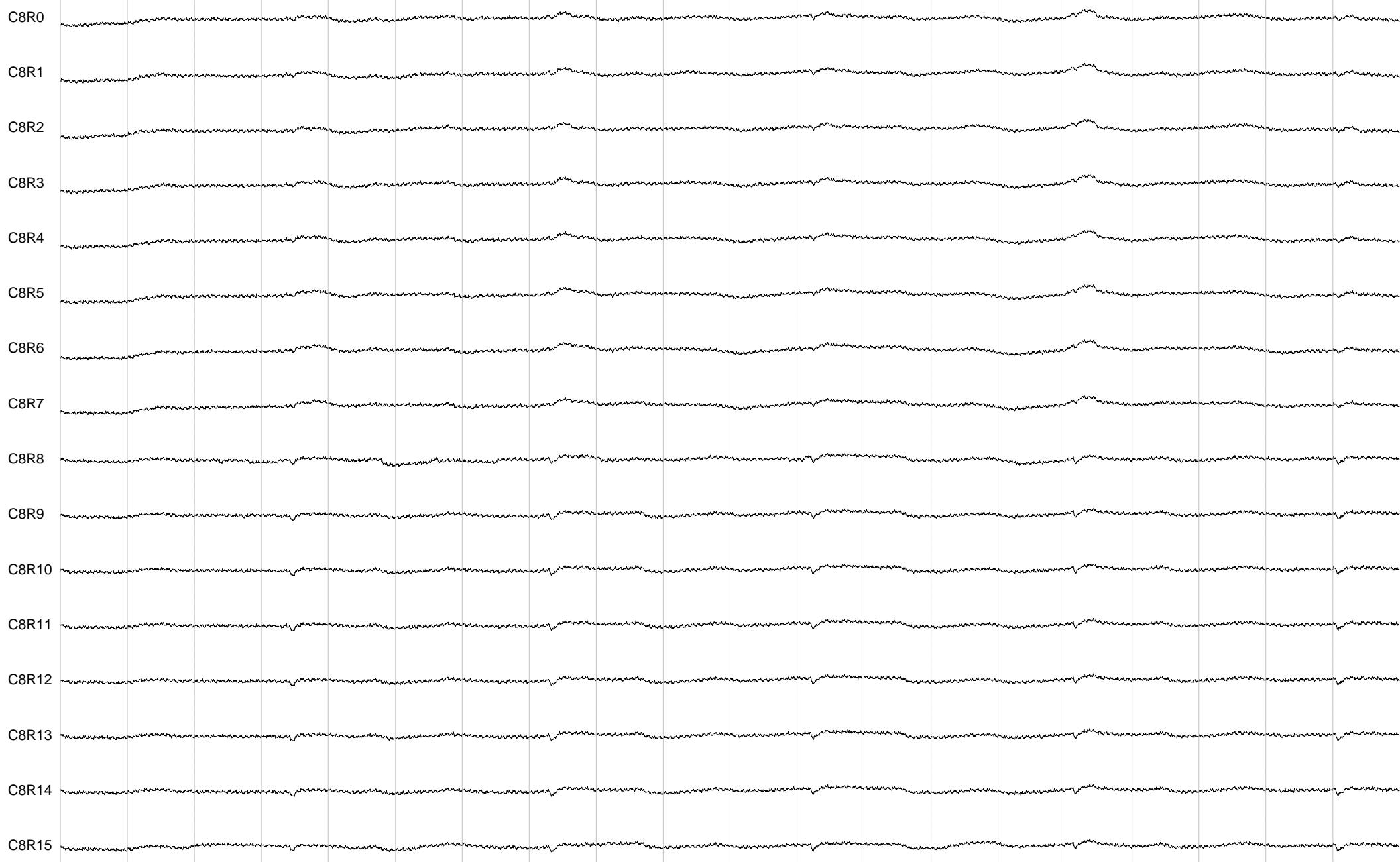
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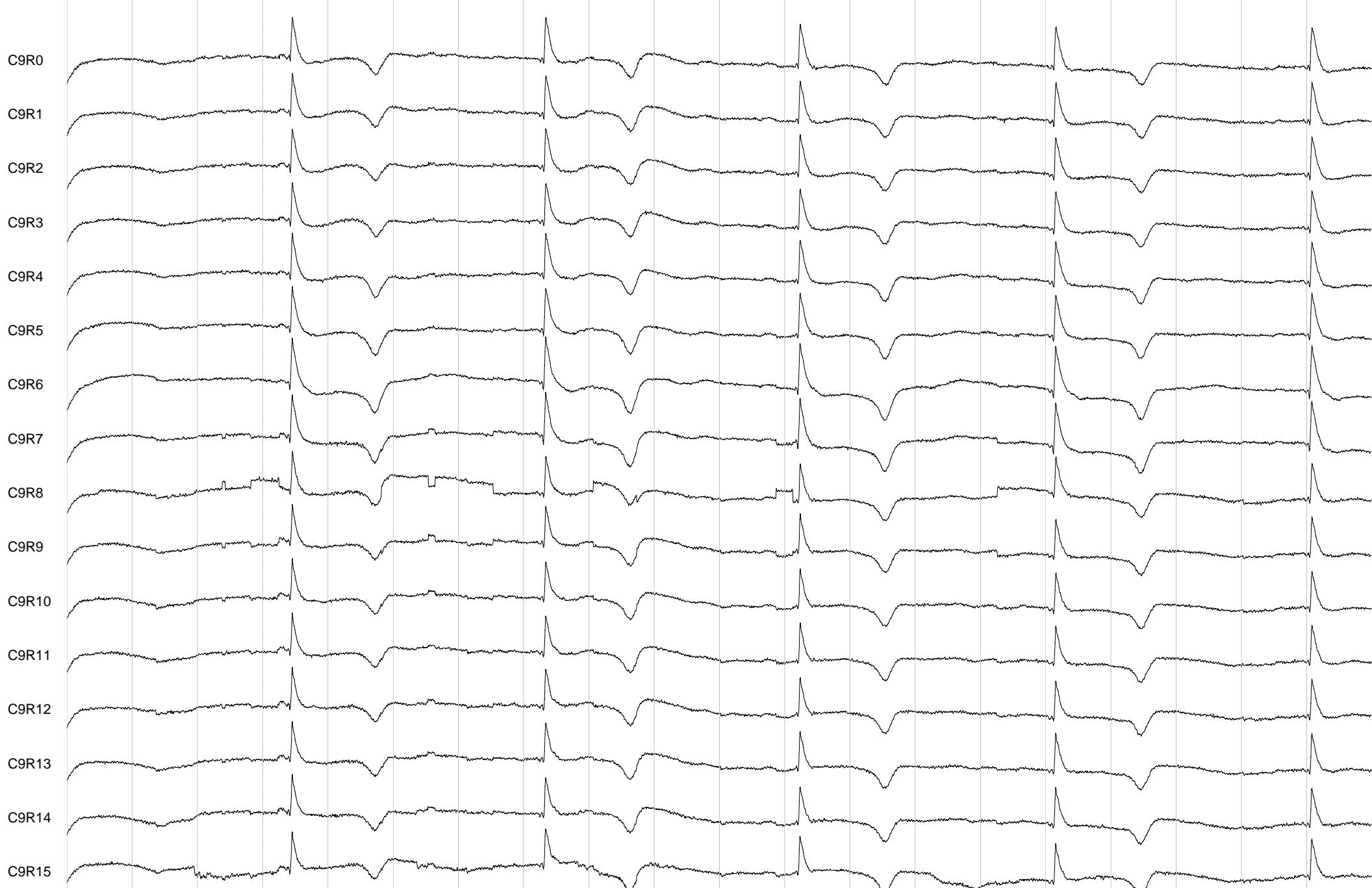
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Absolute time: 16:01:23

C10R0

C10R1

C10R2

C10R3

C10R4

C10R5

C10R6

C10R7

C10R8

C10R9

C10R10

C10R11

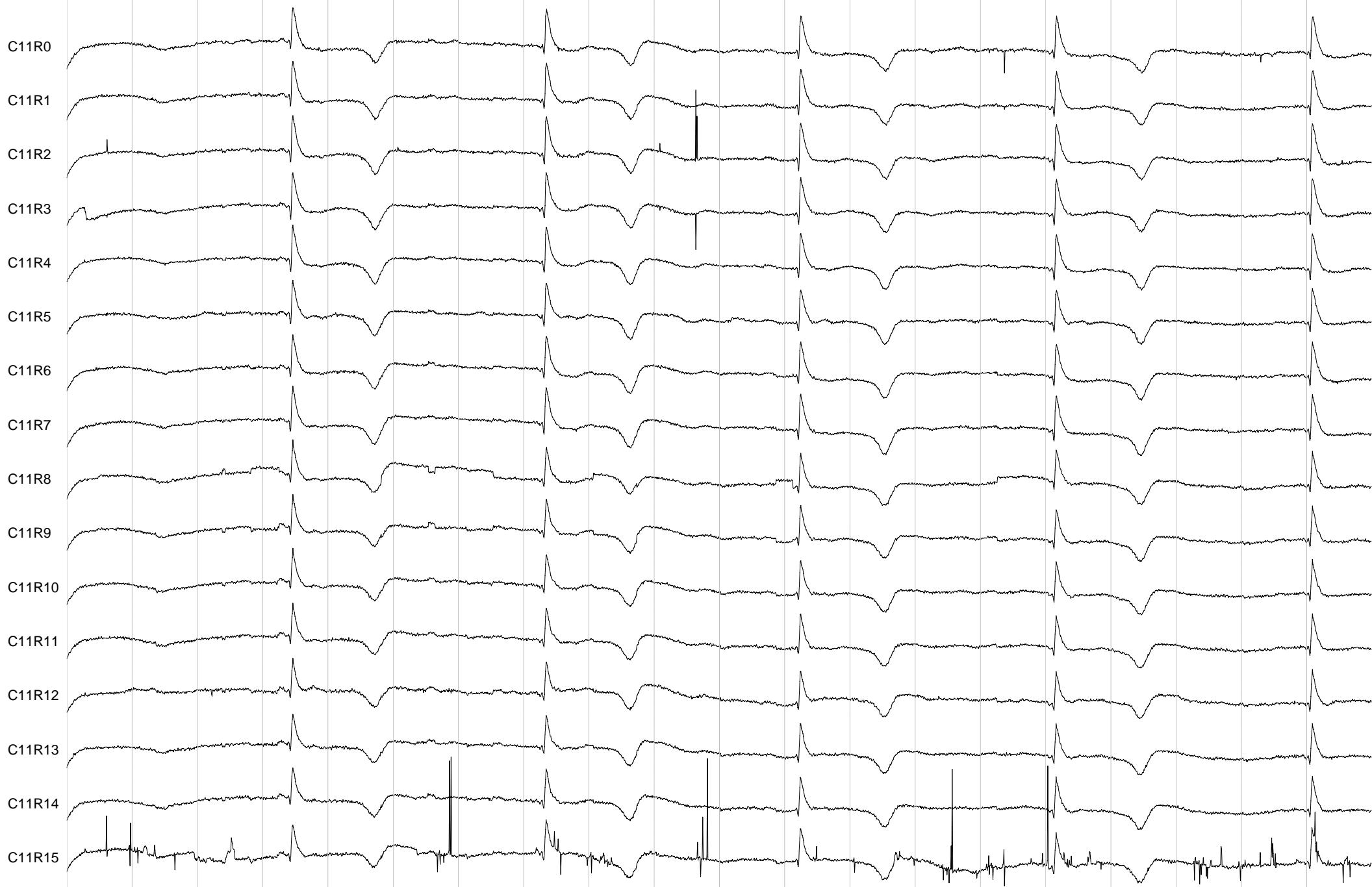
C10R12

C10R13

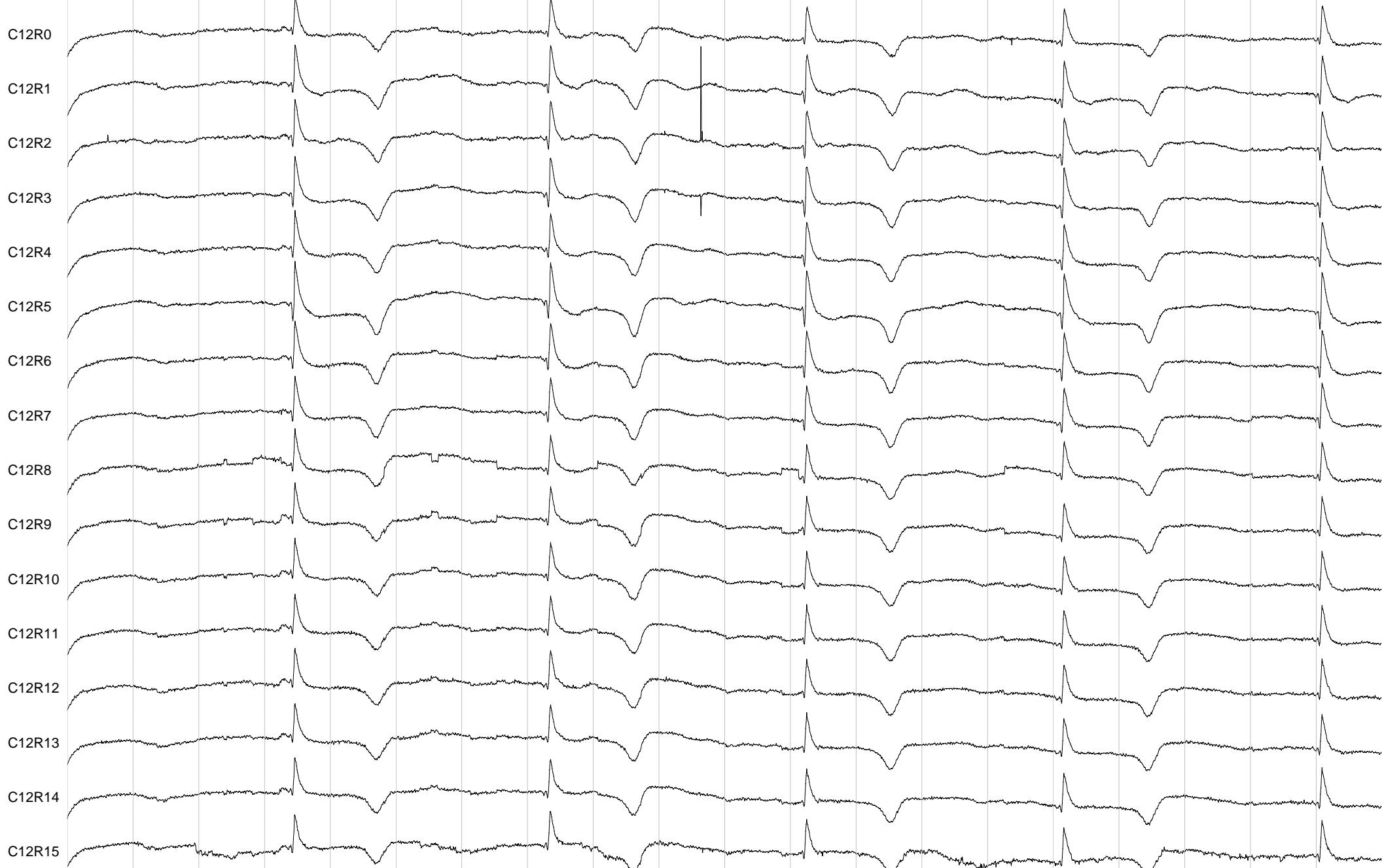
C10R14

C10R15

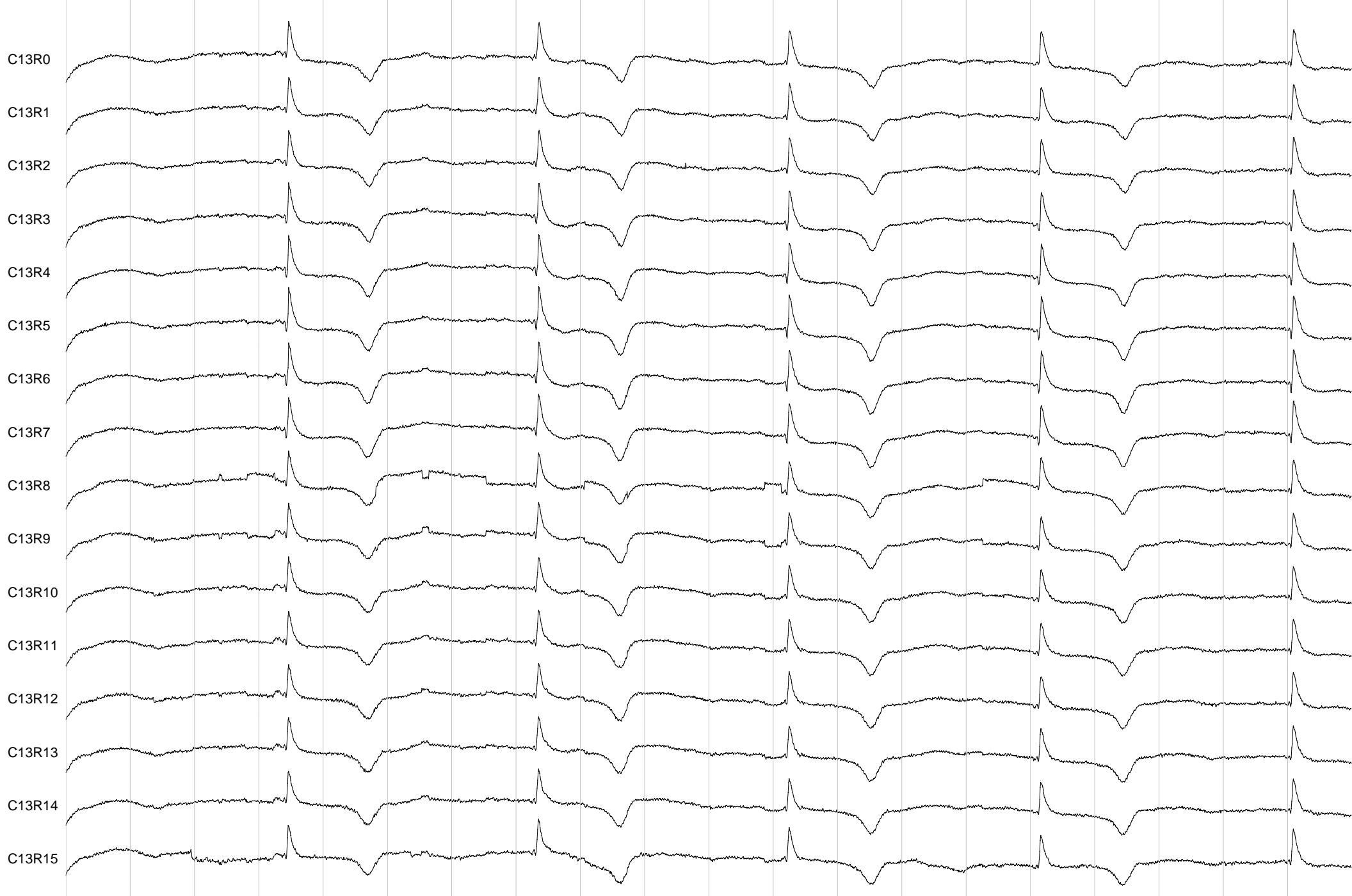
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C15R0

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C15R5

C15R6

C15R7

C15R8

C15R9

C15R10

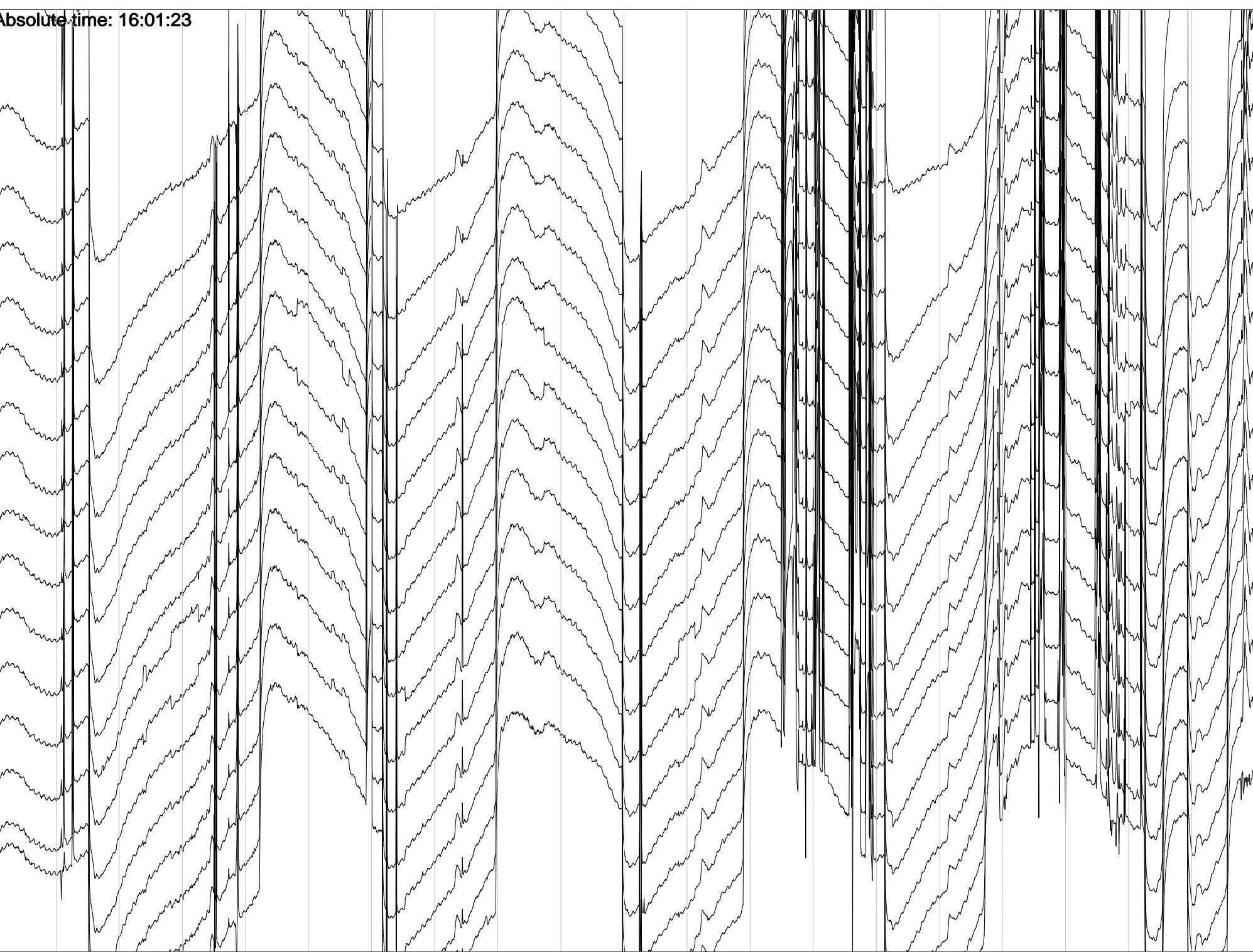
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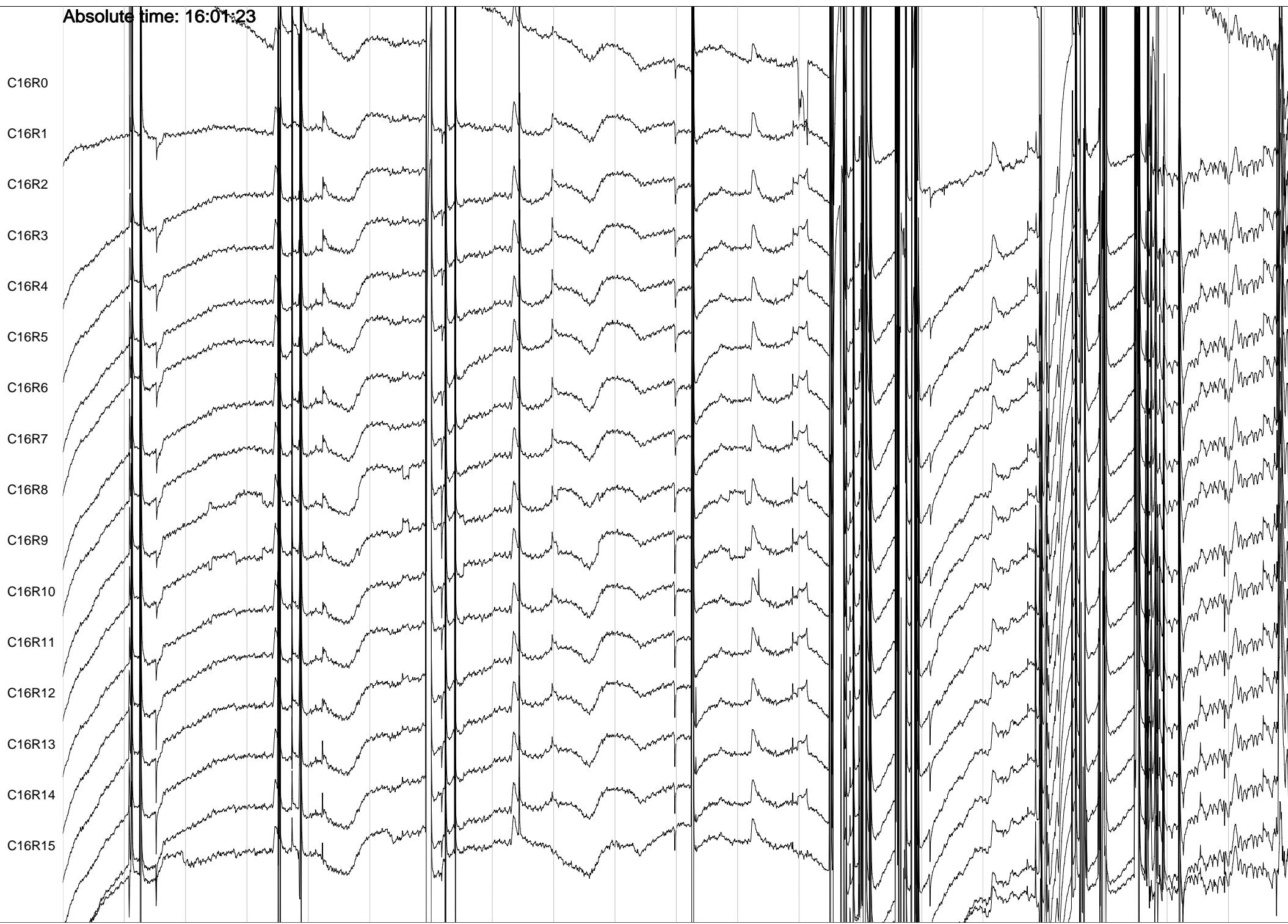
C15R12

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C15R14

C15R15





Absolute time: 16:01:23

C17R0

C17R1

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C17R5

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C17R7

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C17R10

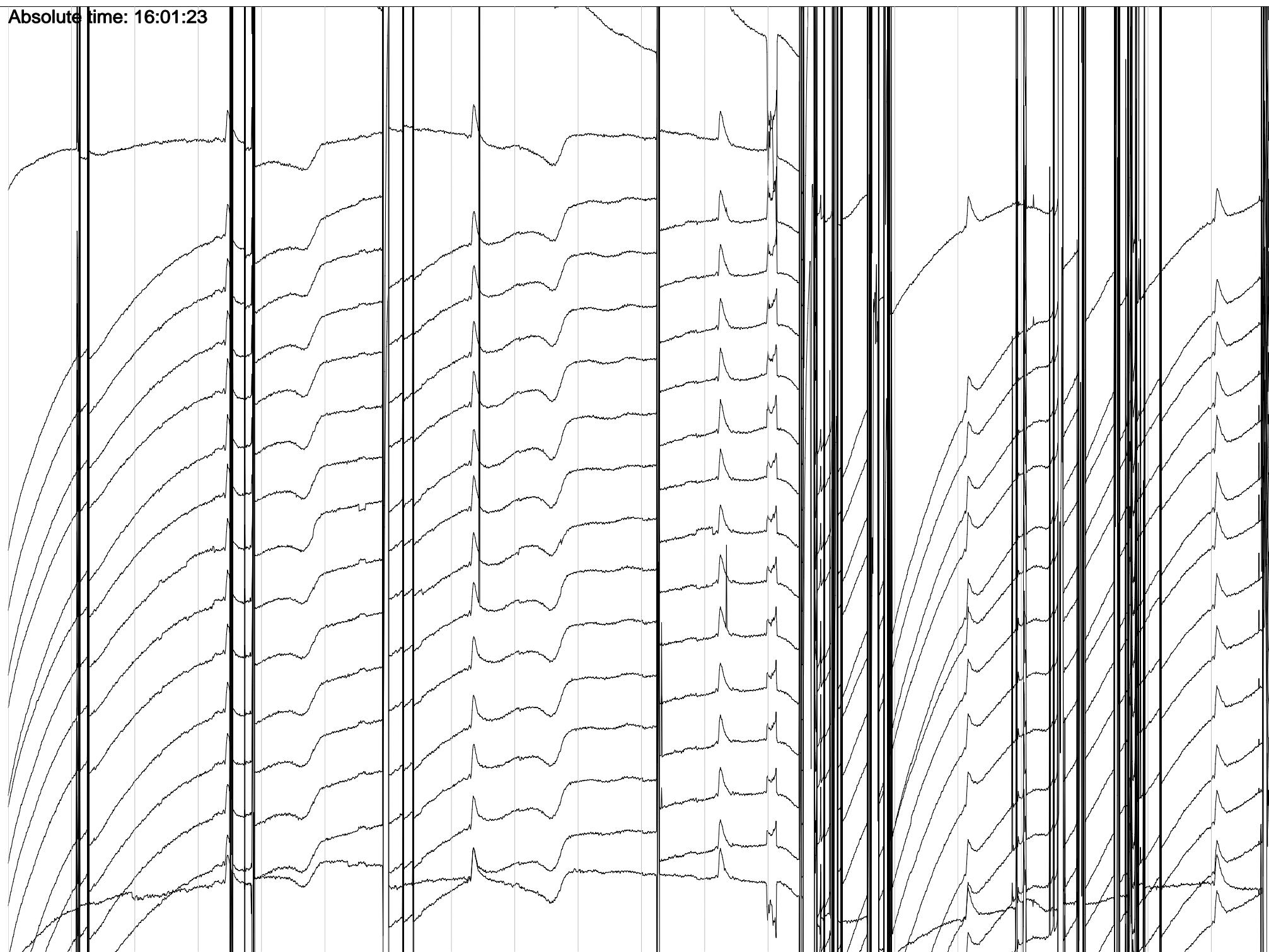
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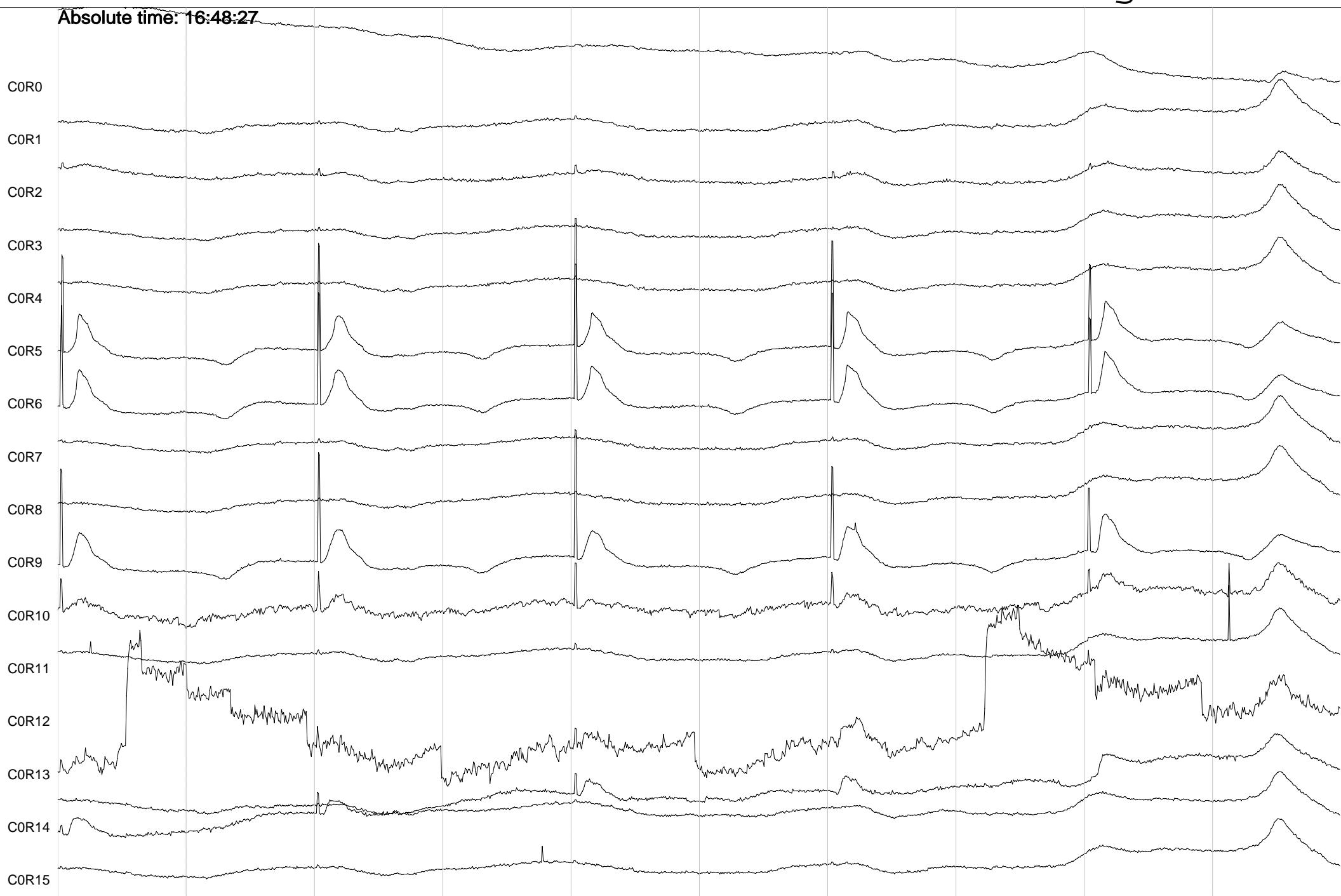
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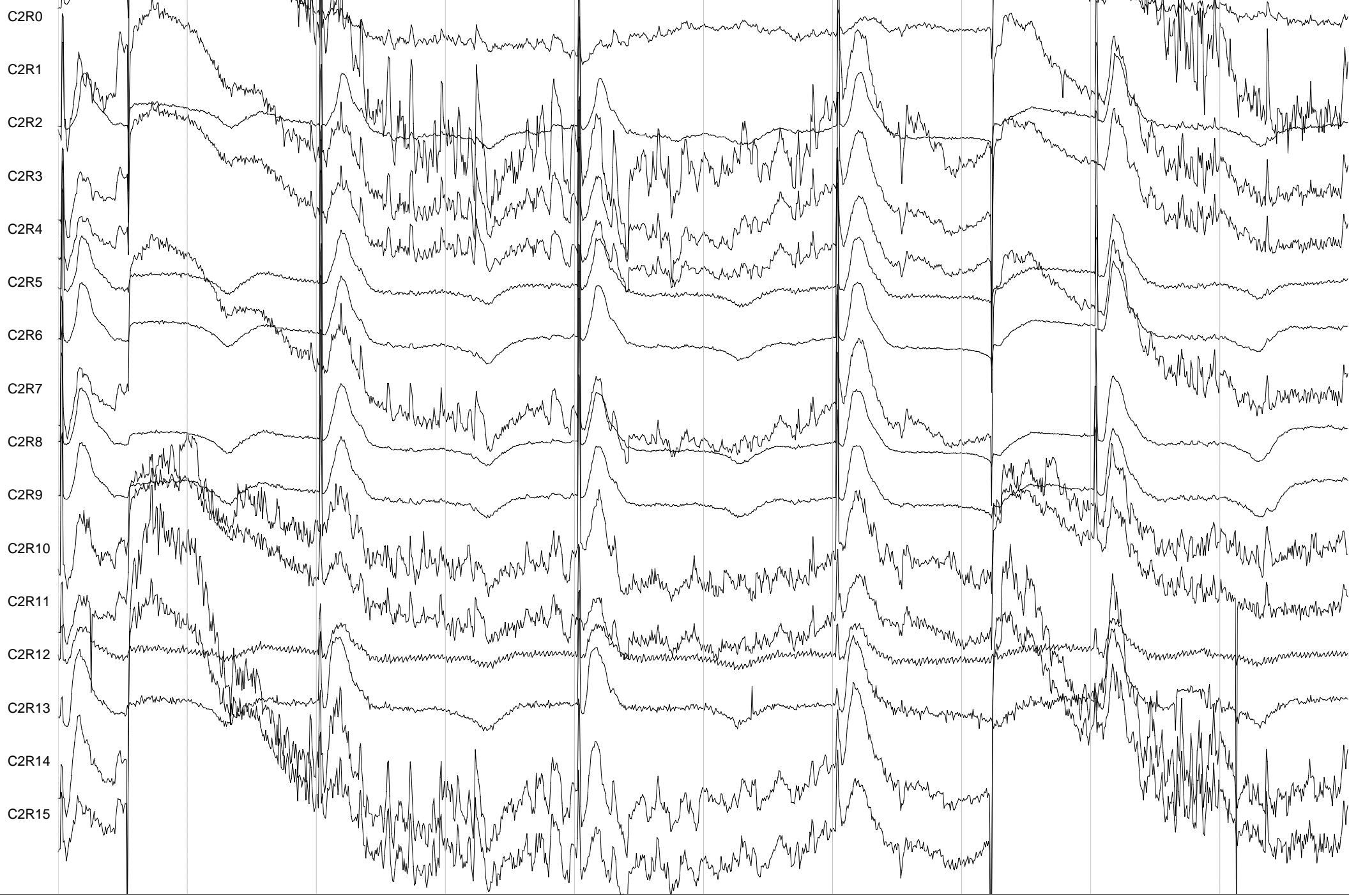




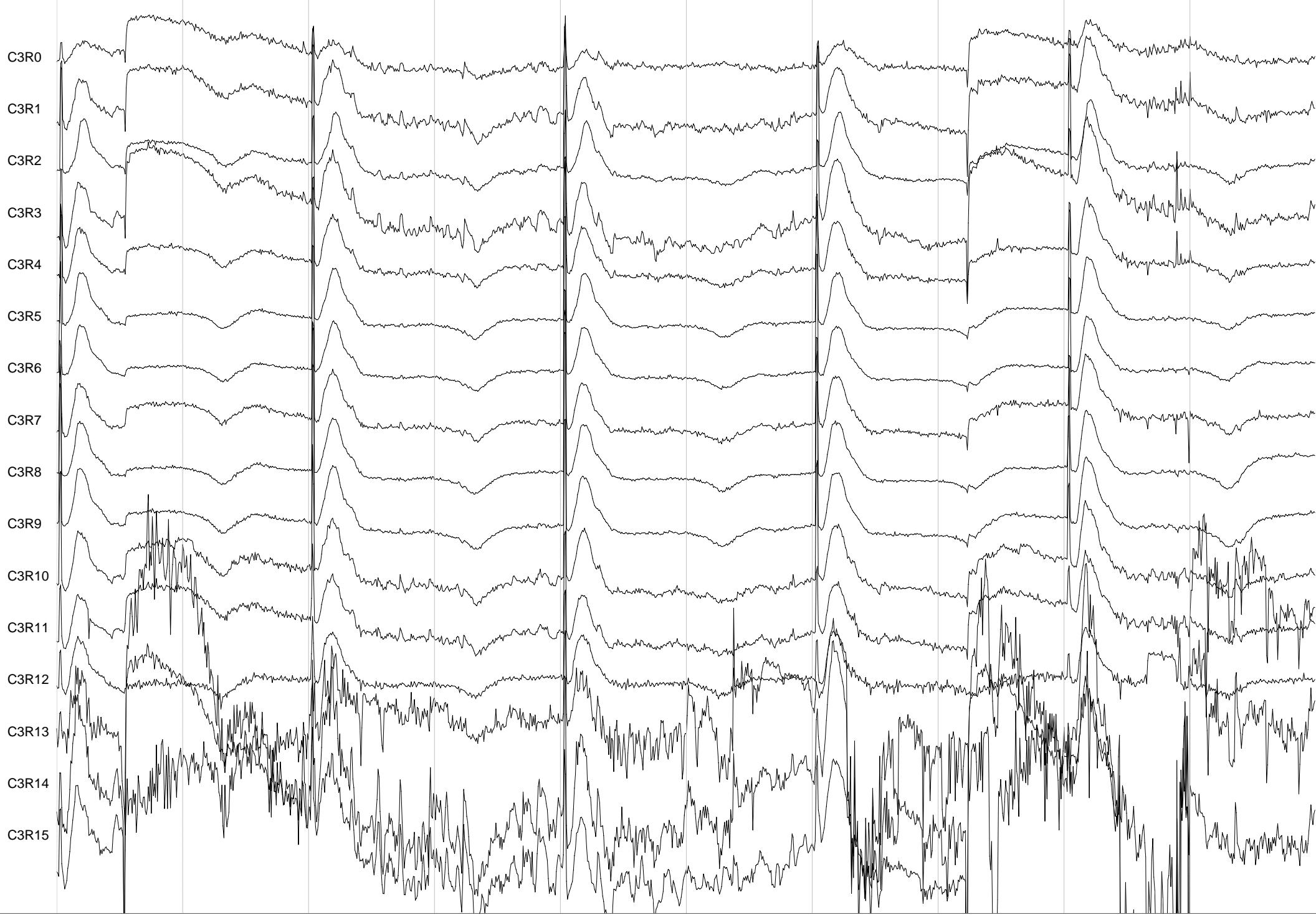
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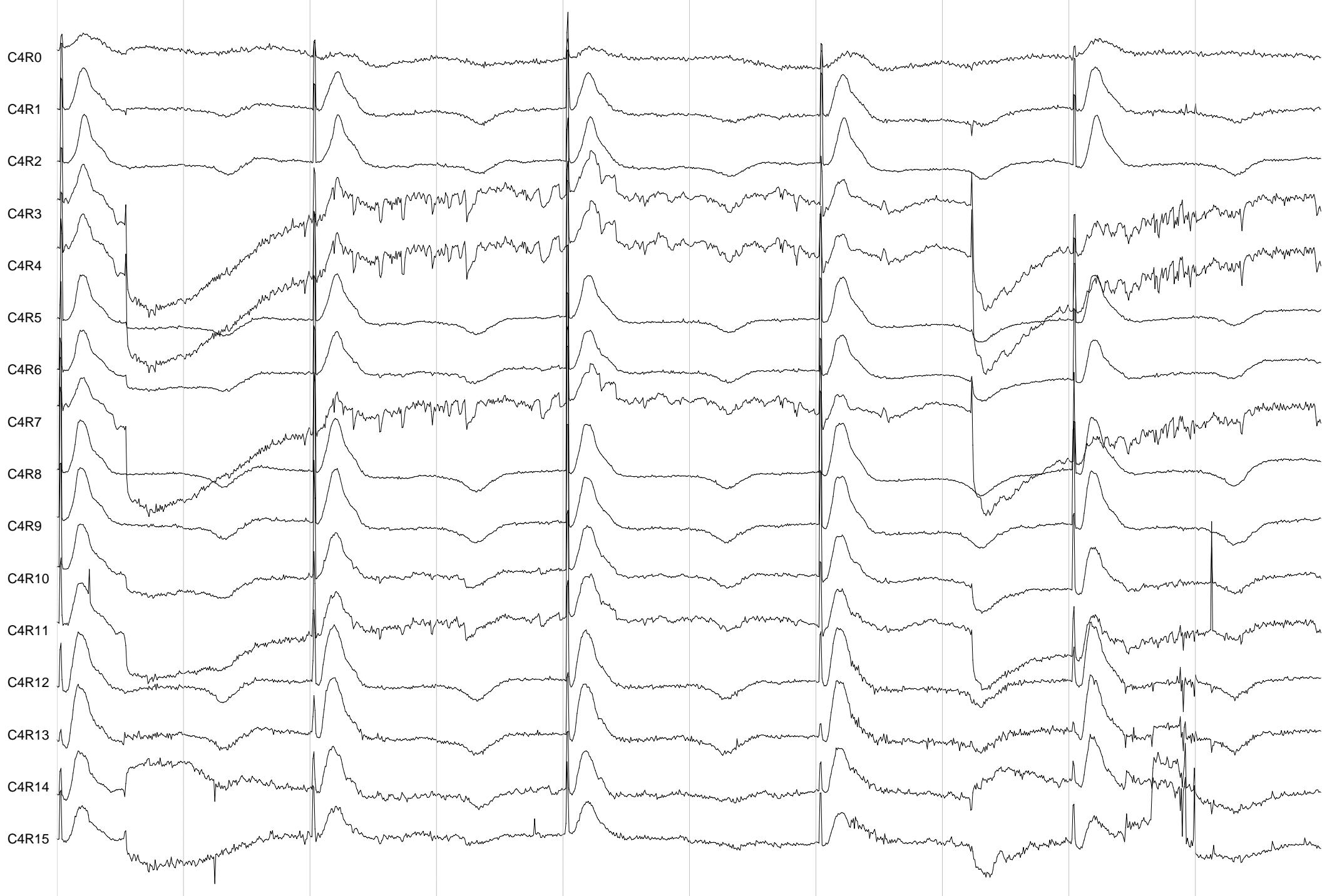
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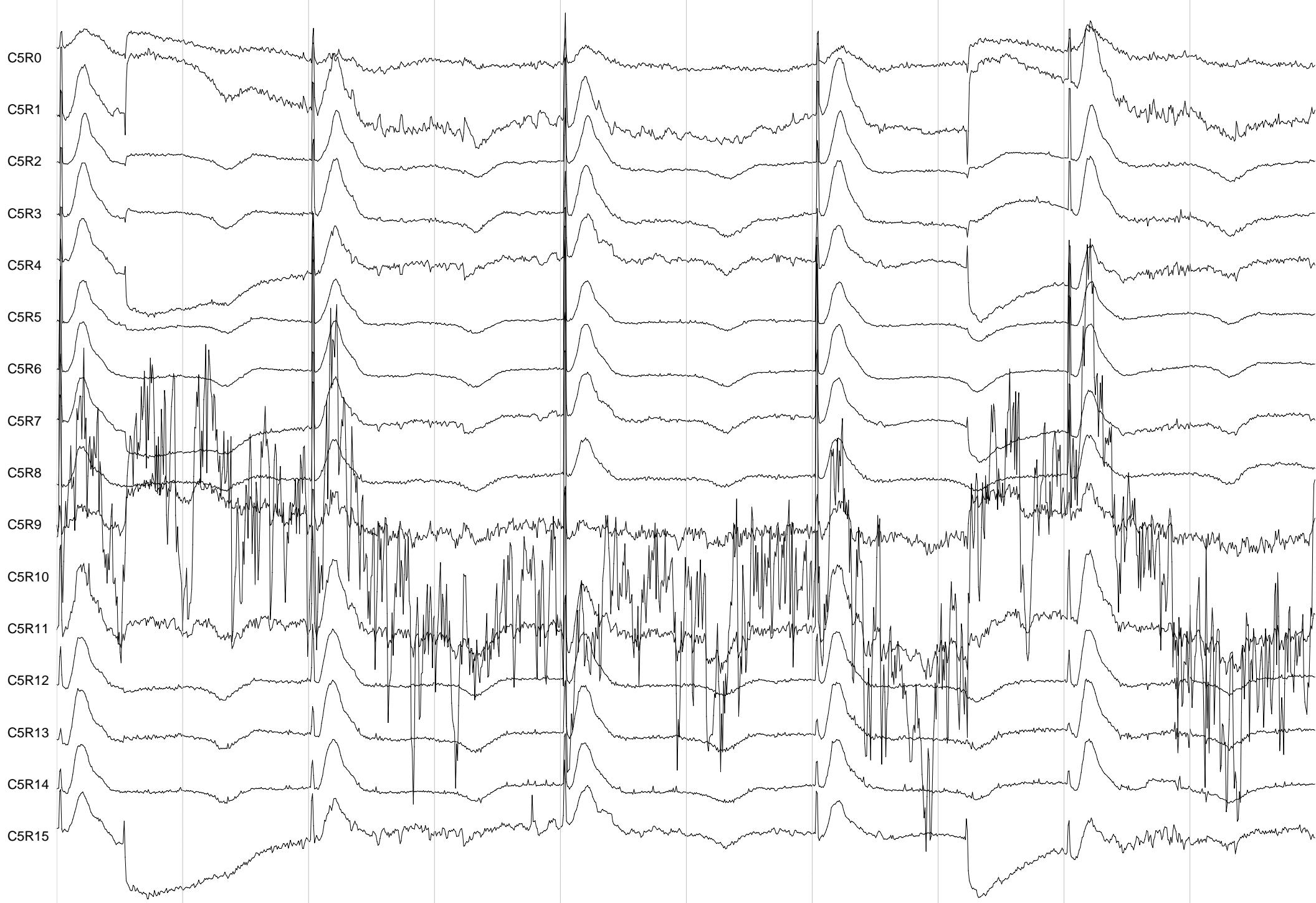
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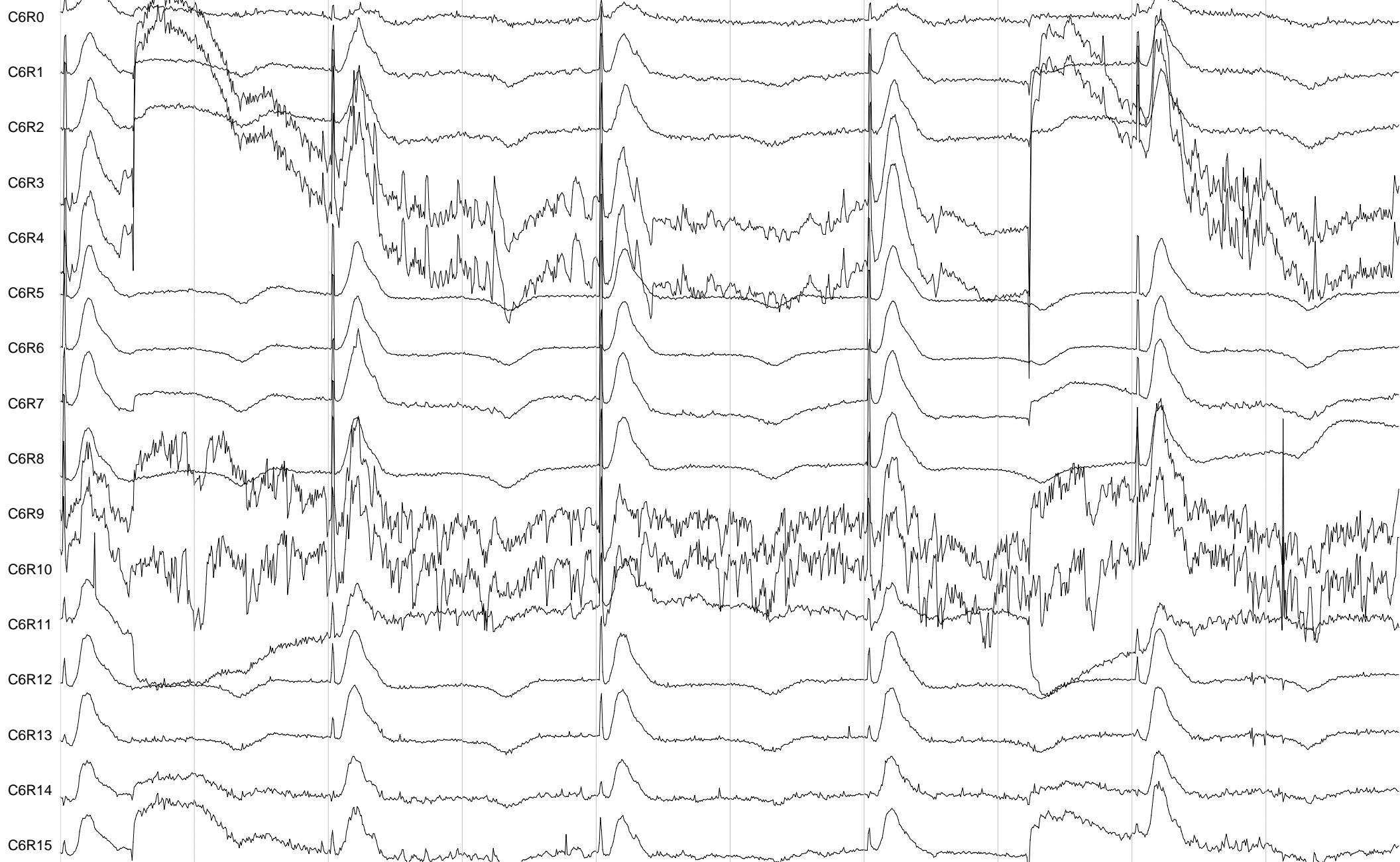
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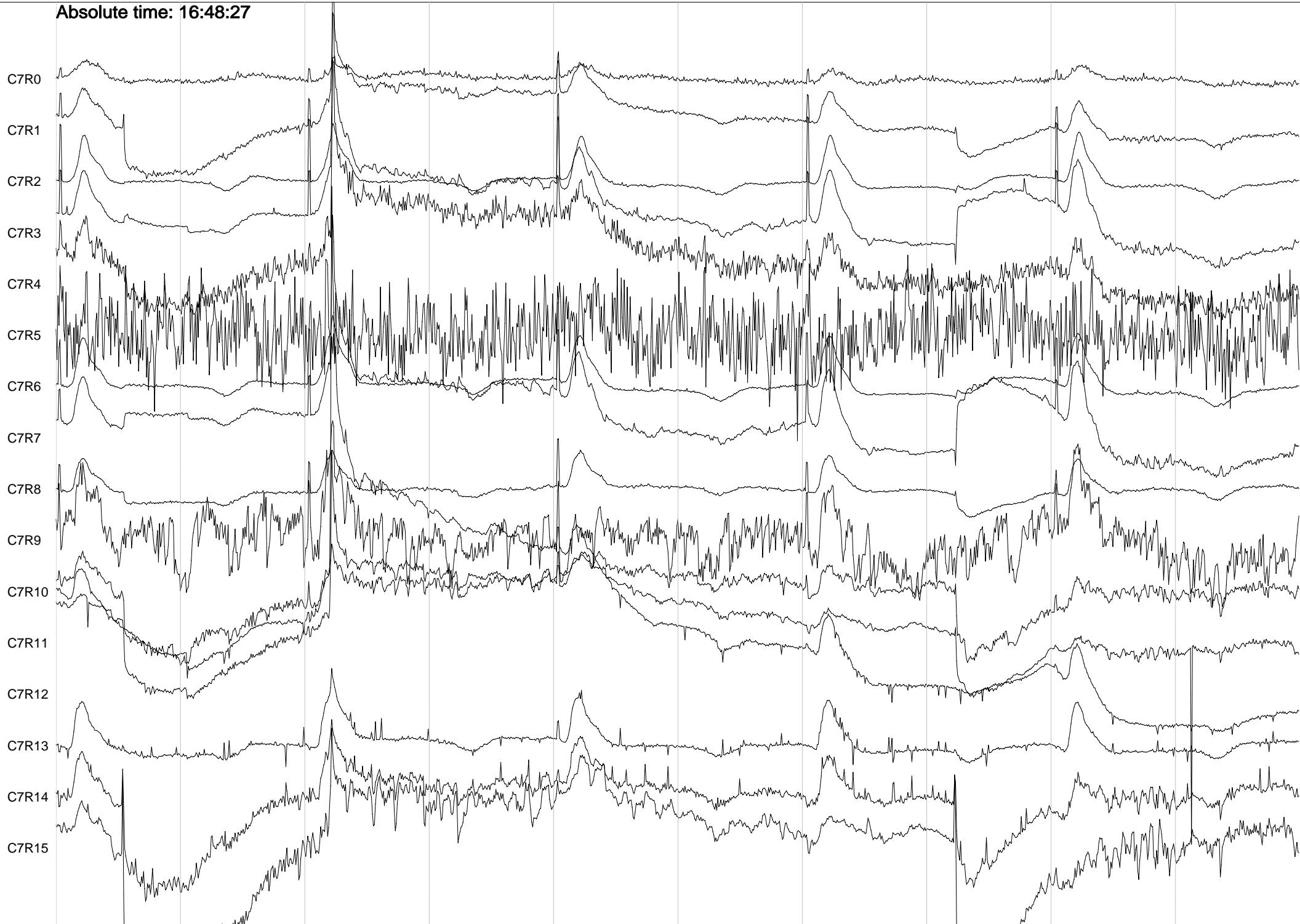
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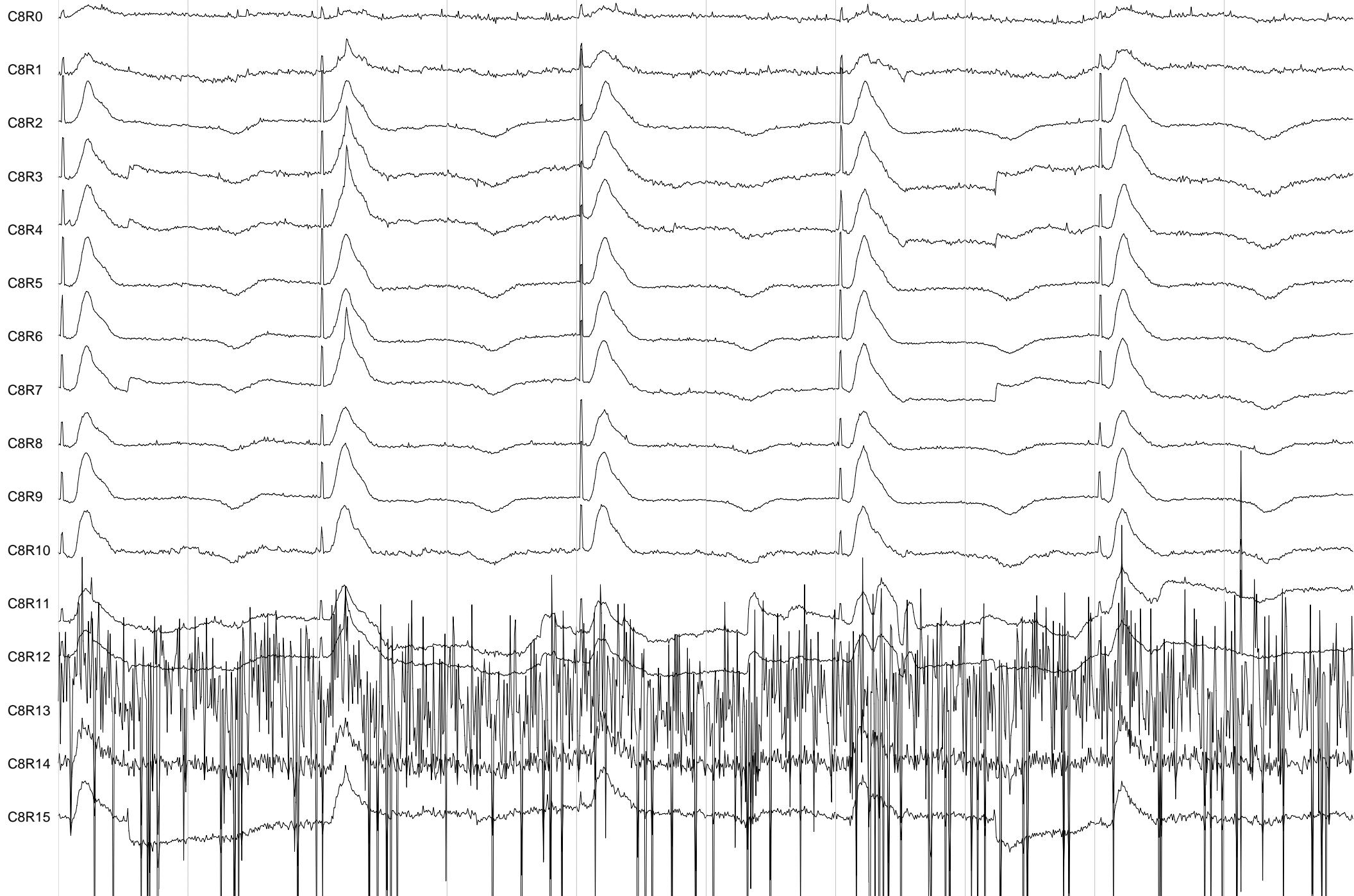
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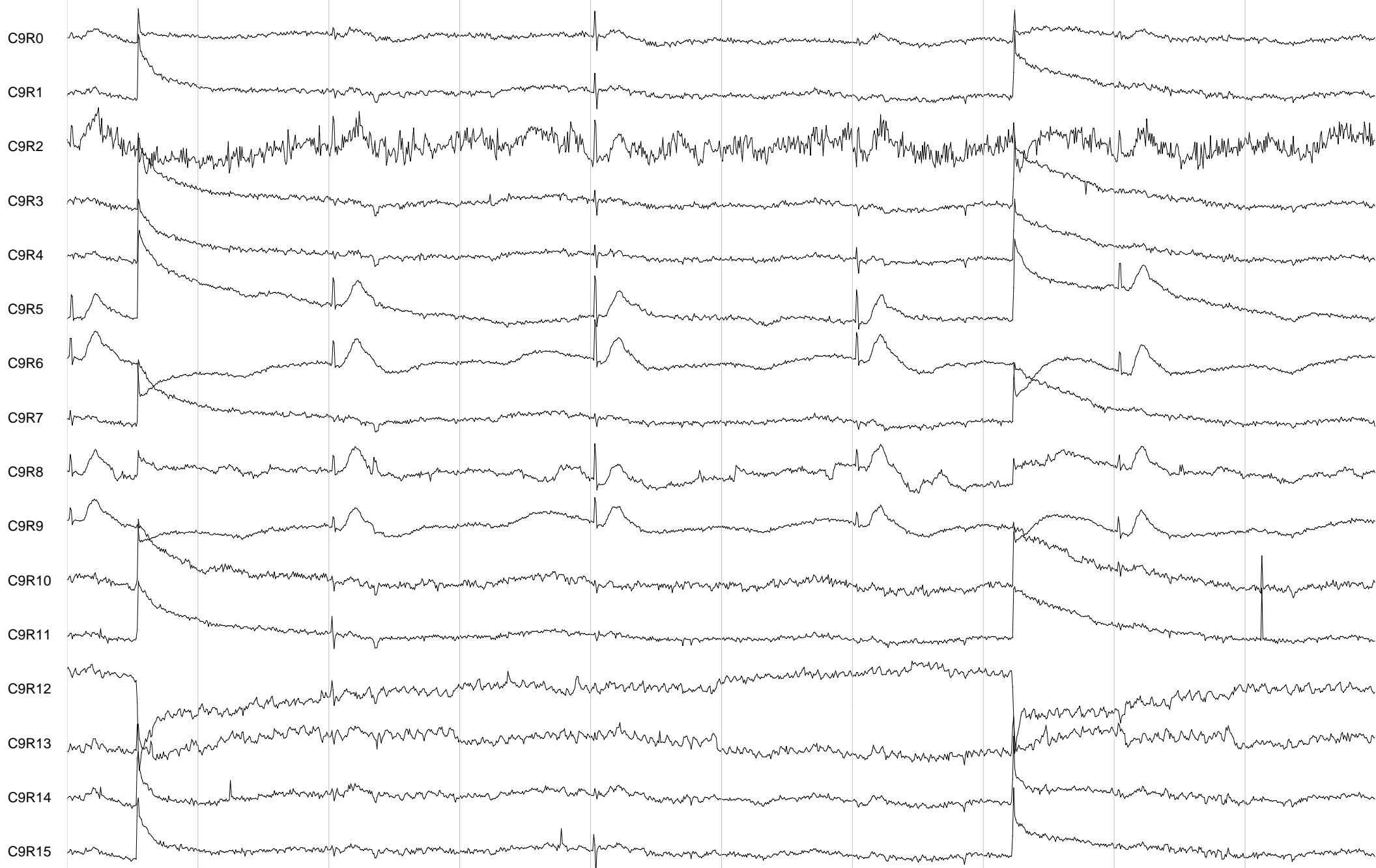
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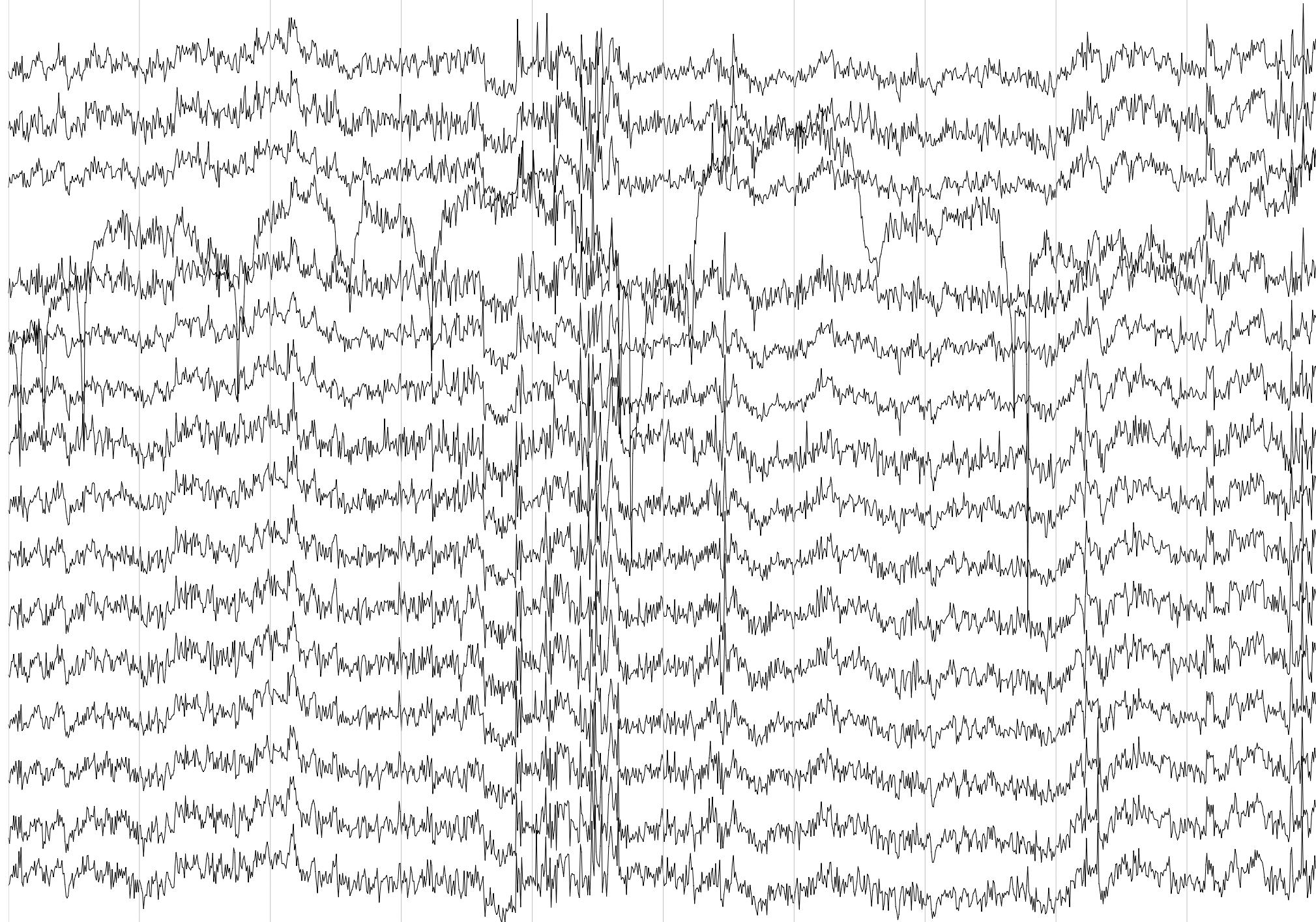


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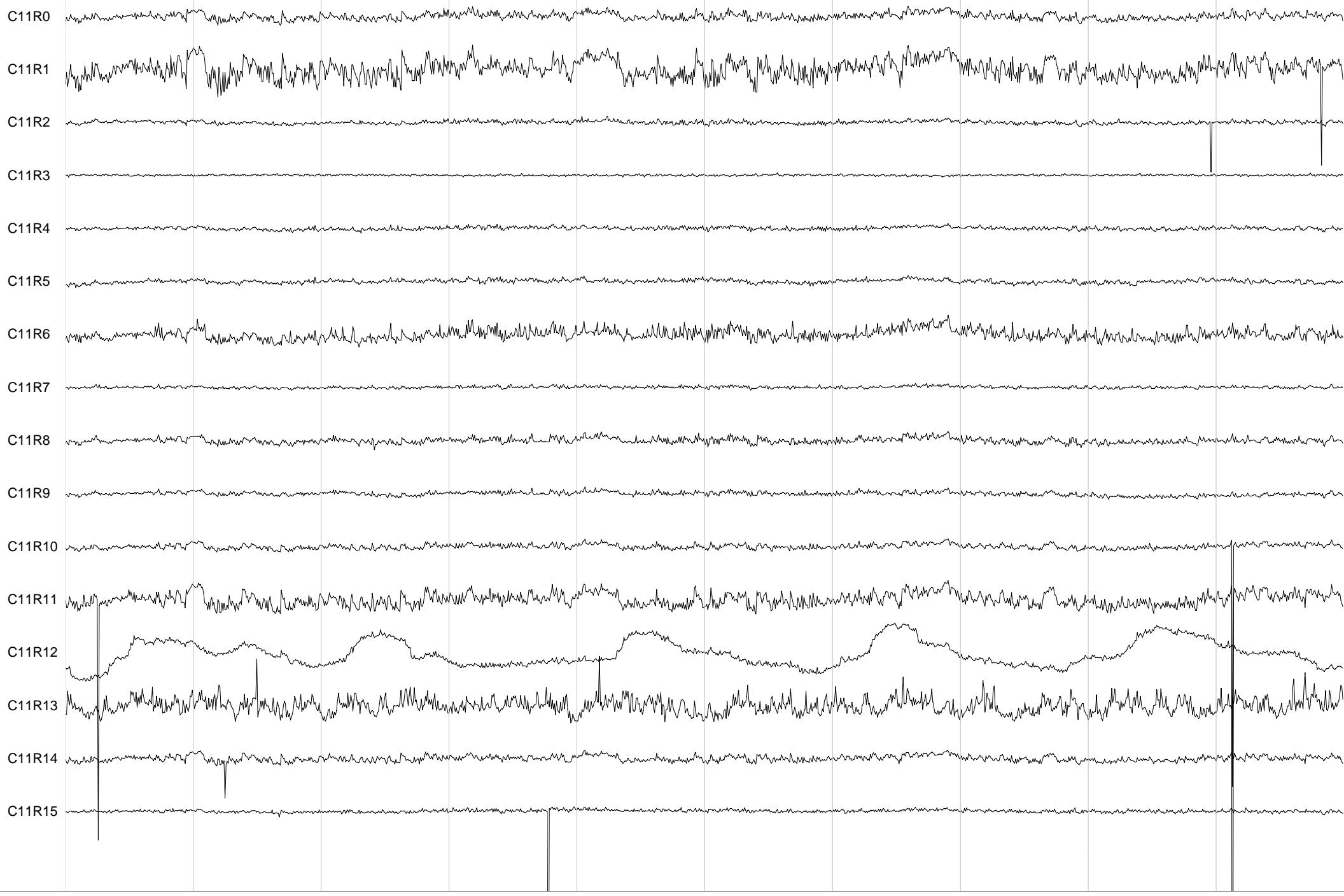


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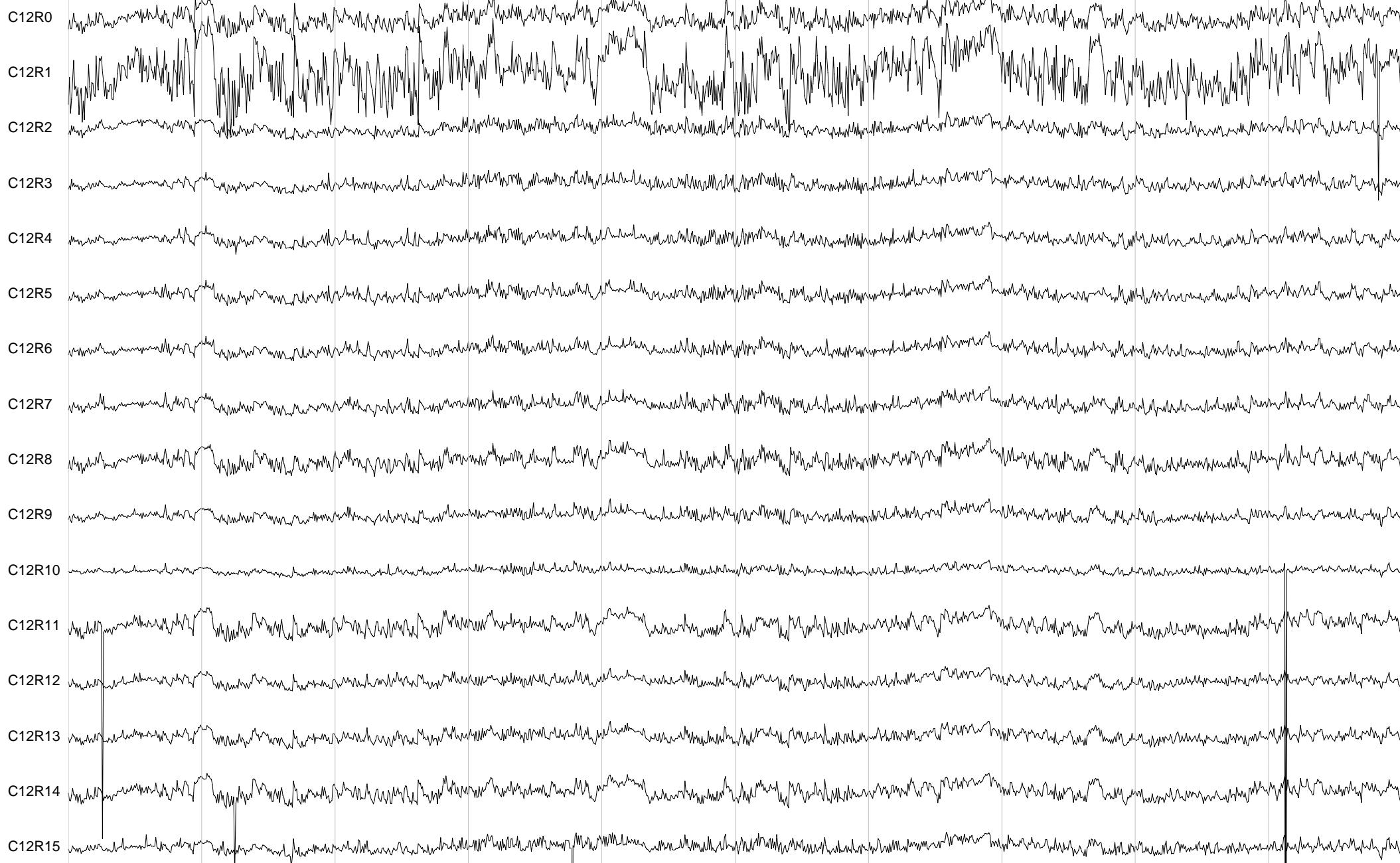
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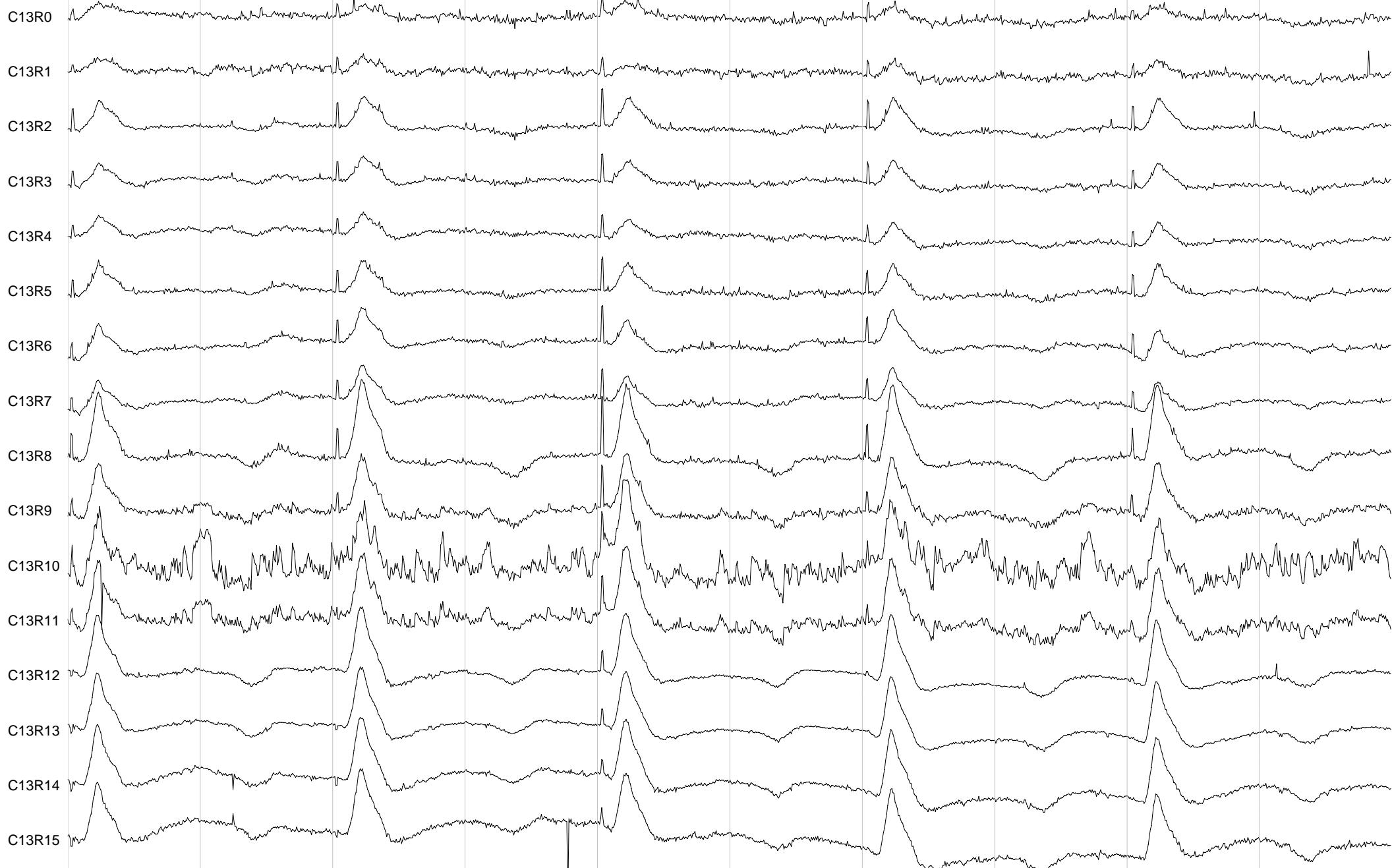
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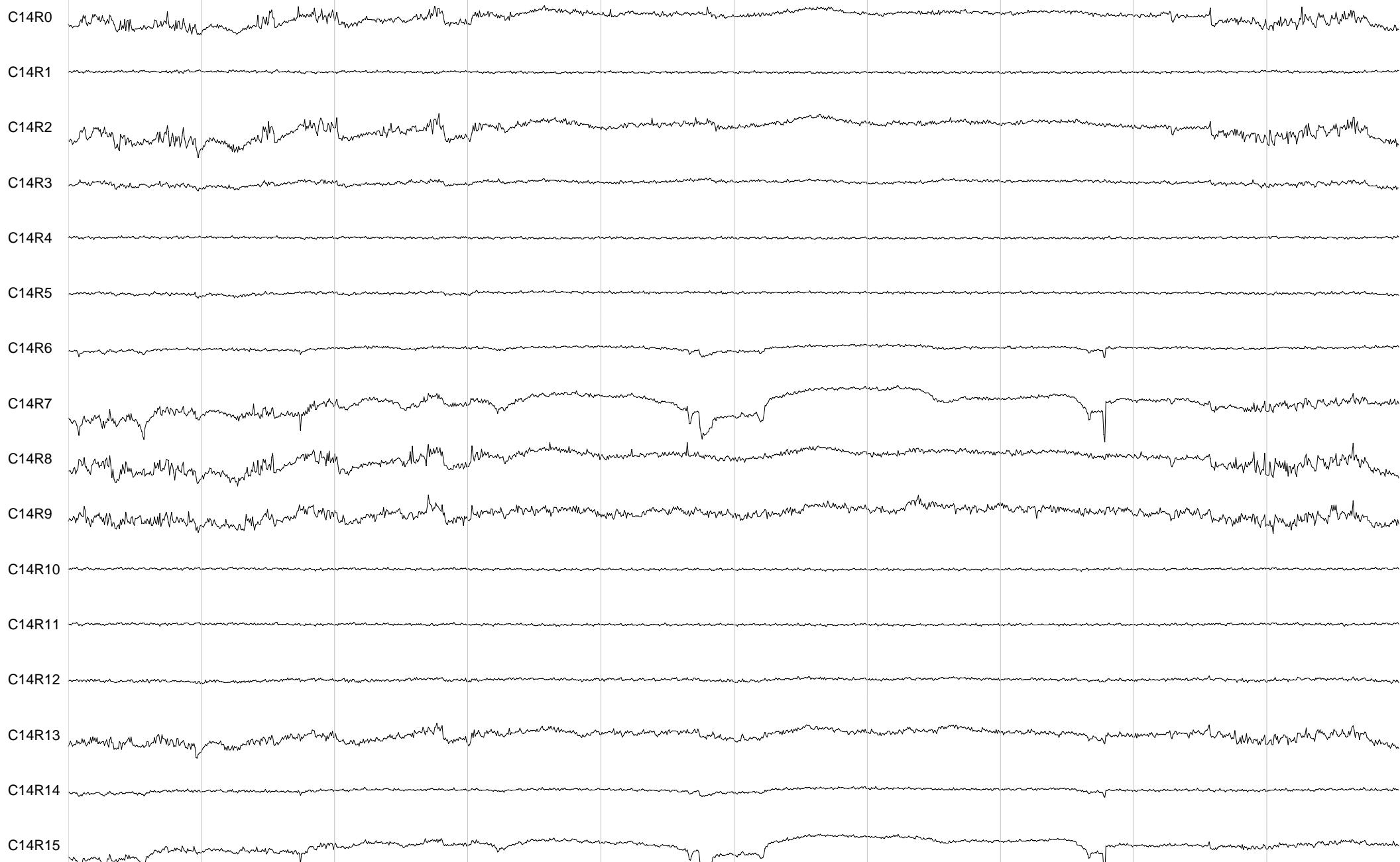
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C15R0

C15R1

C15R2

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C15R6

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C15R8

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C15R10

C15R11

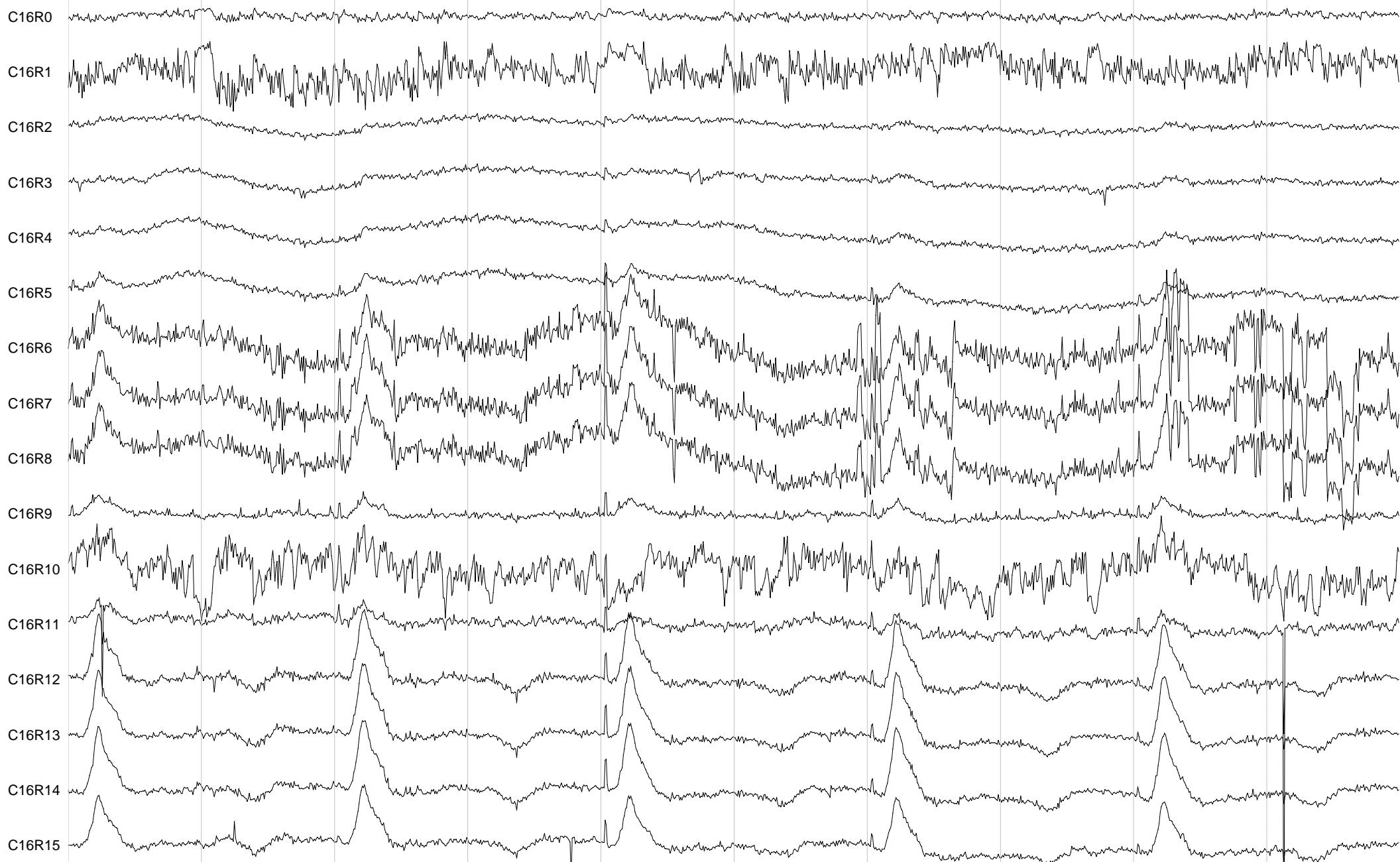
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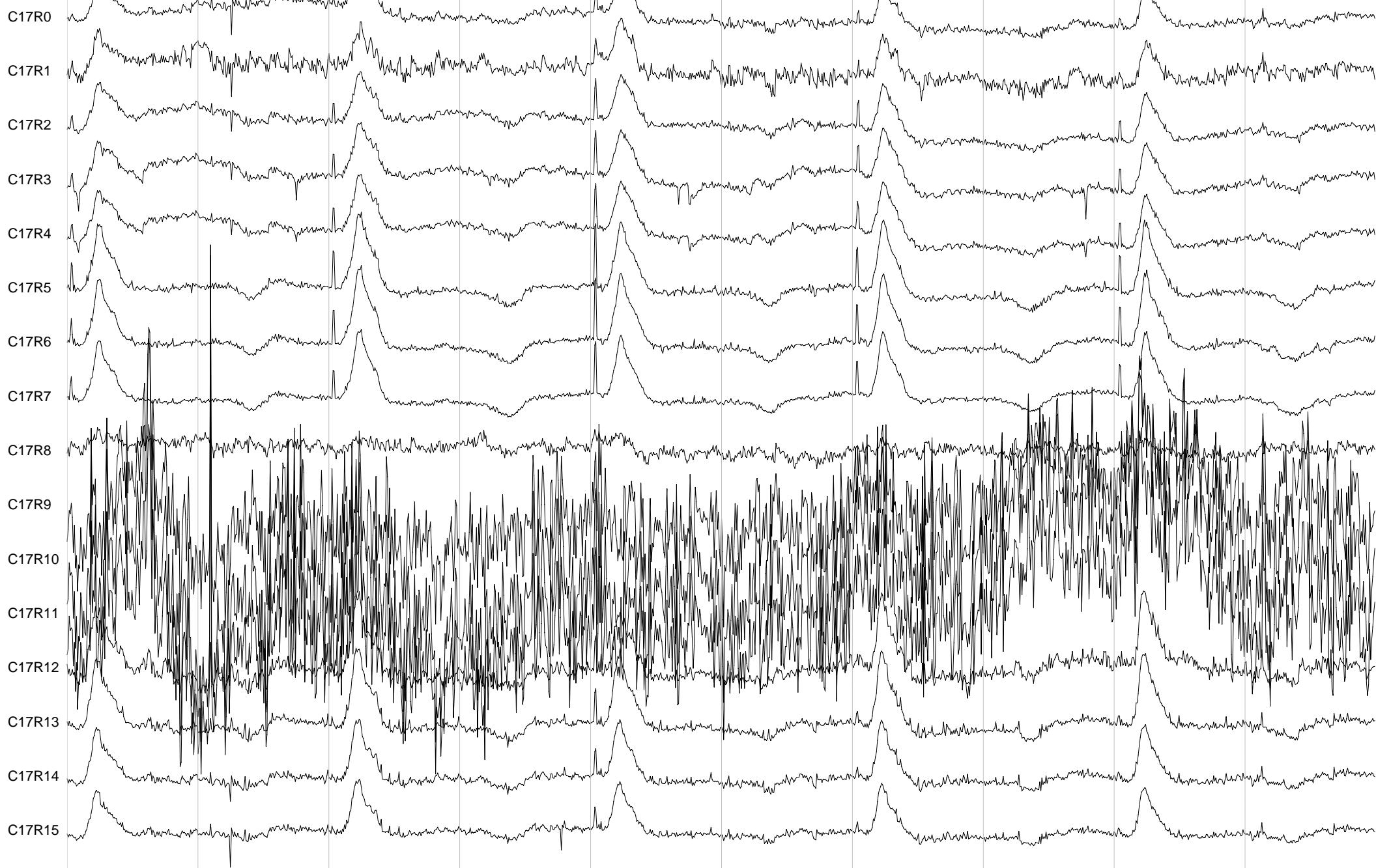
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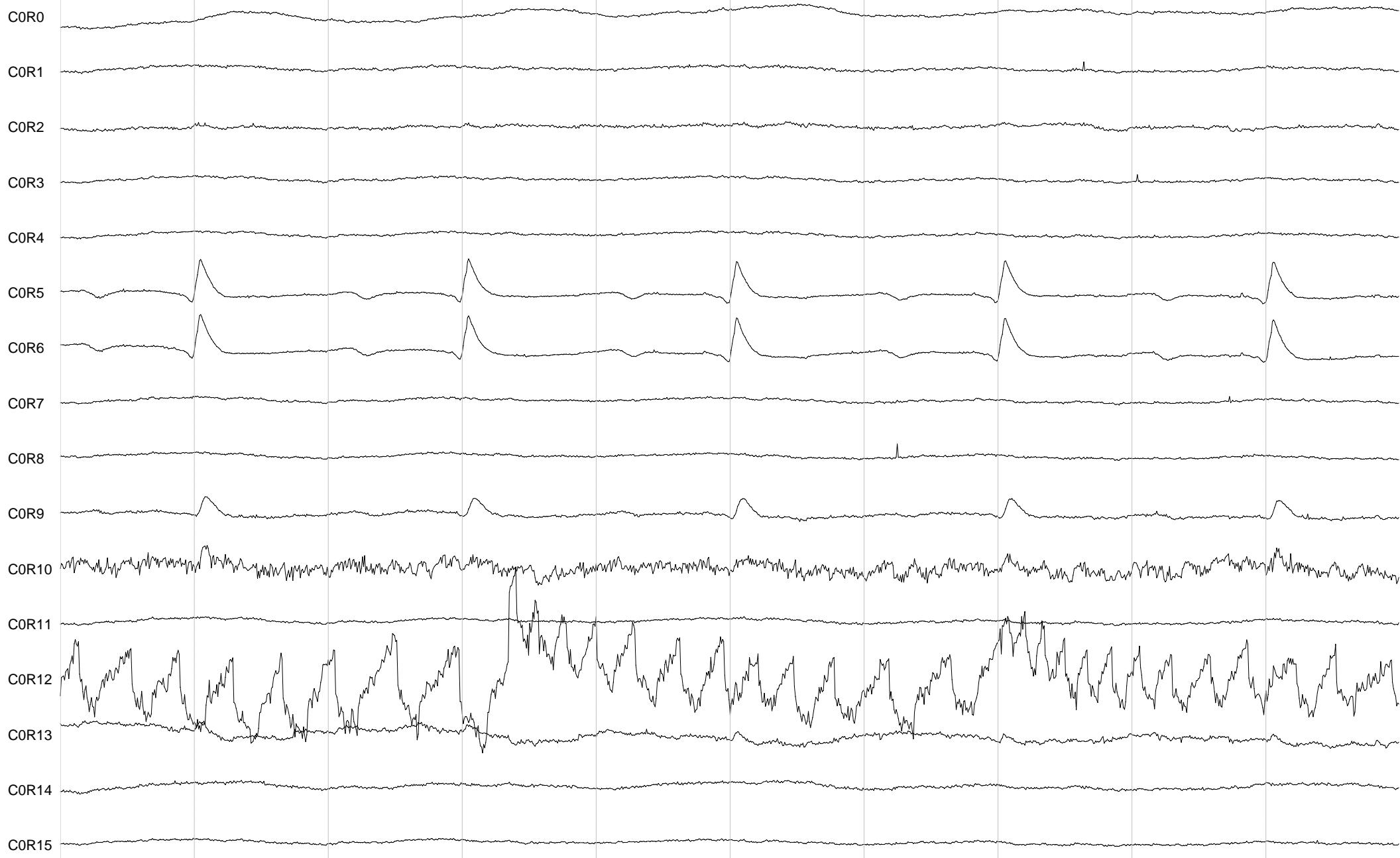
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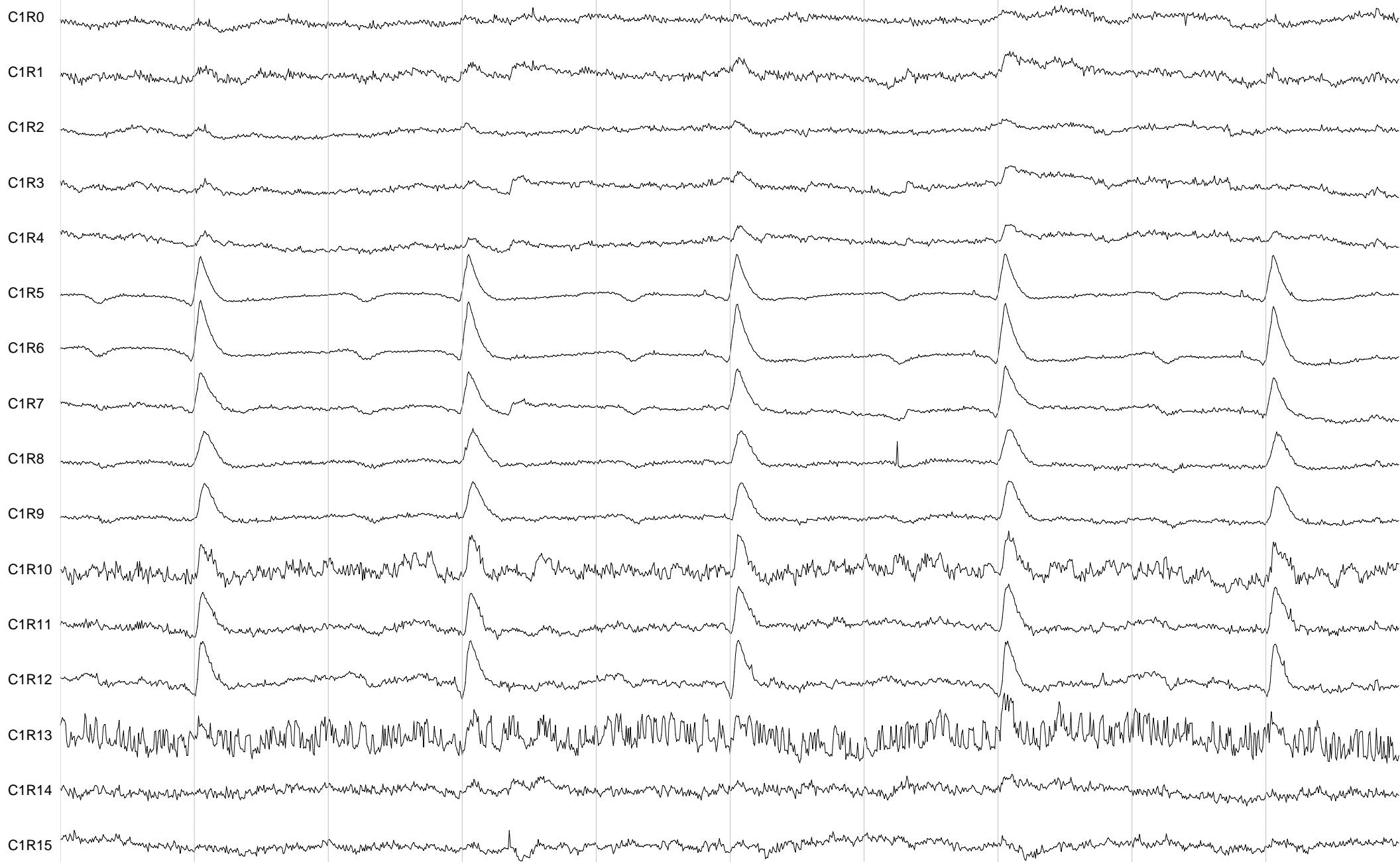
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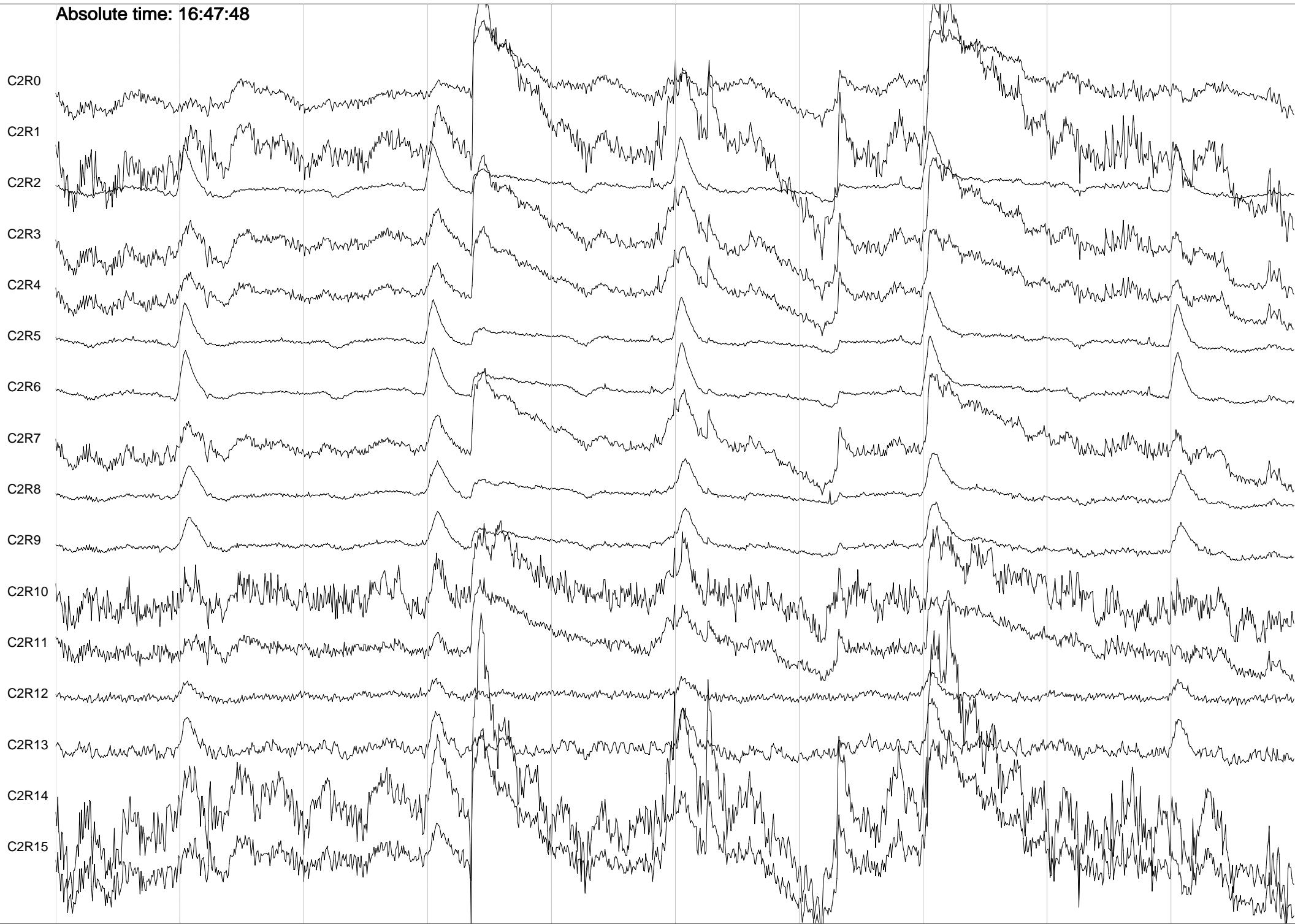
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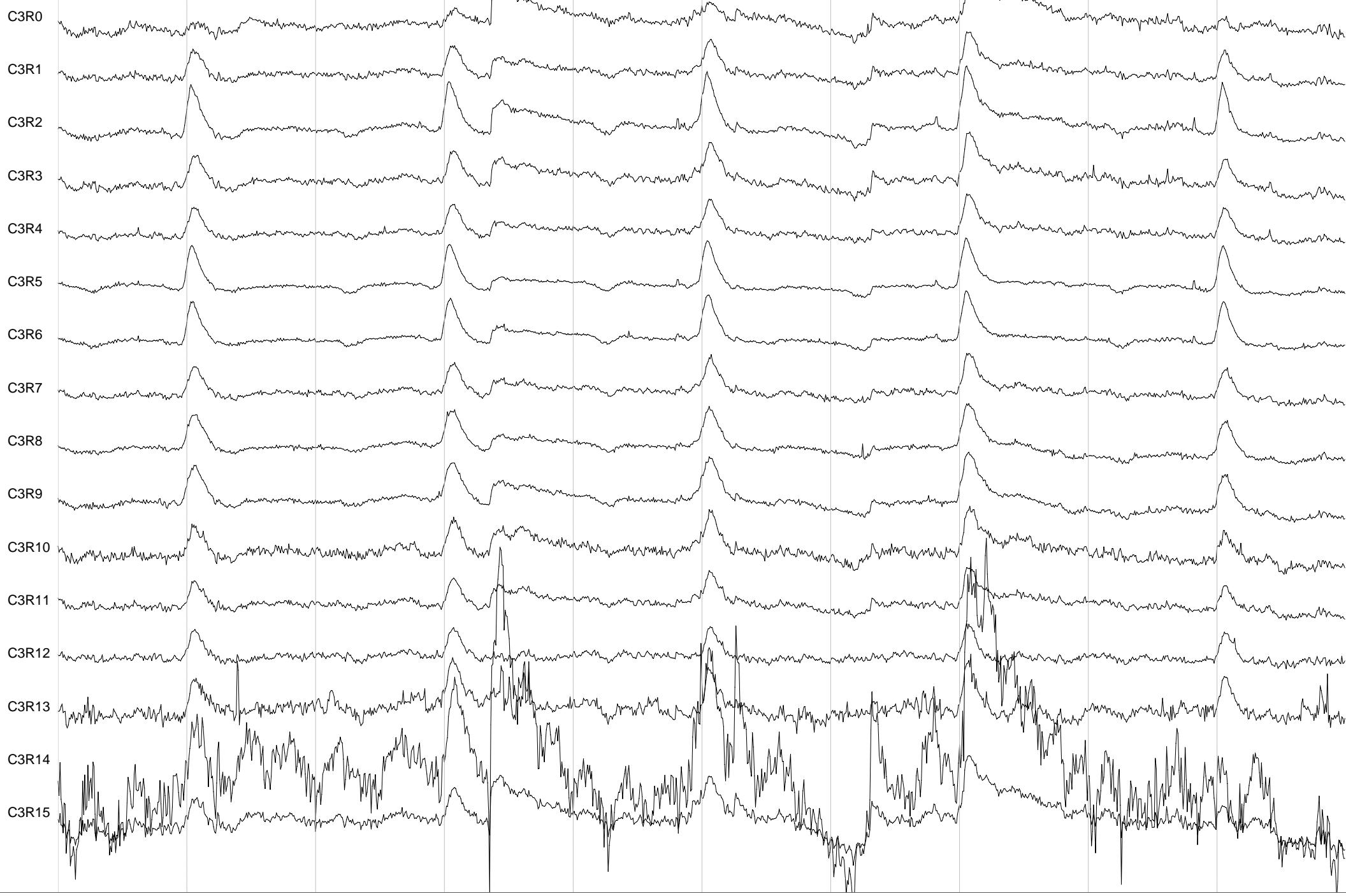
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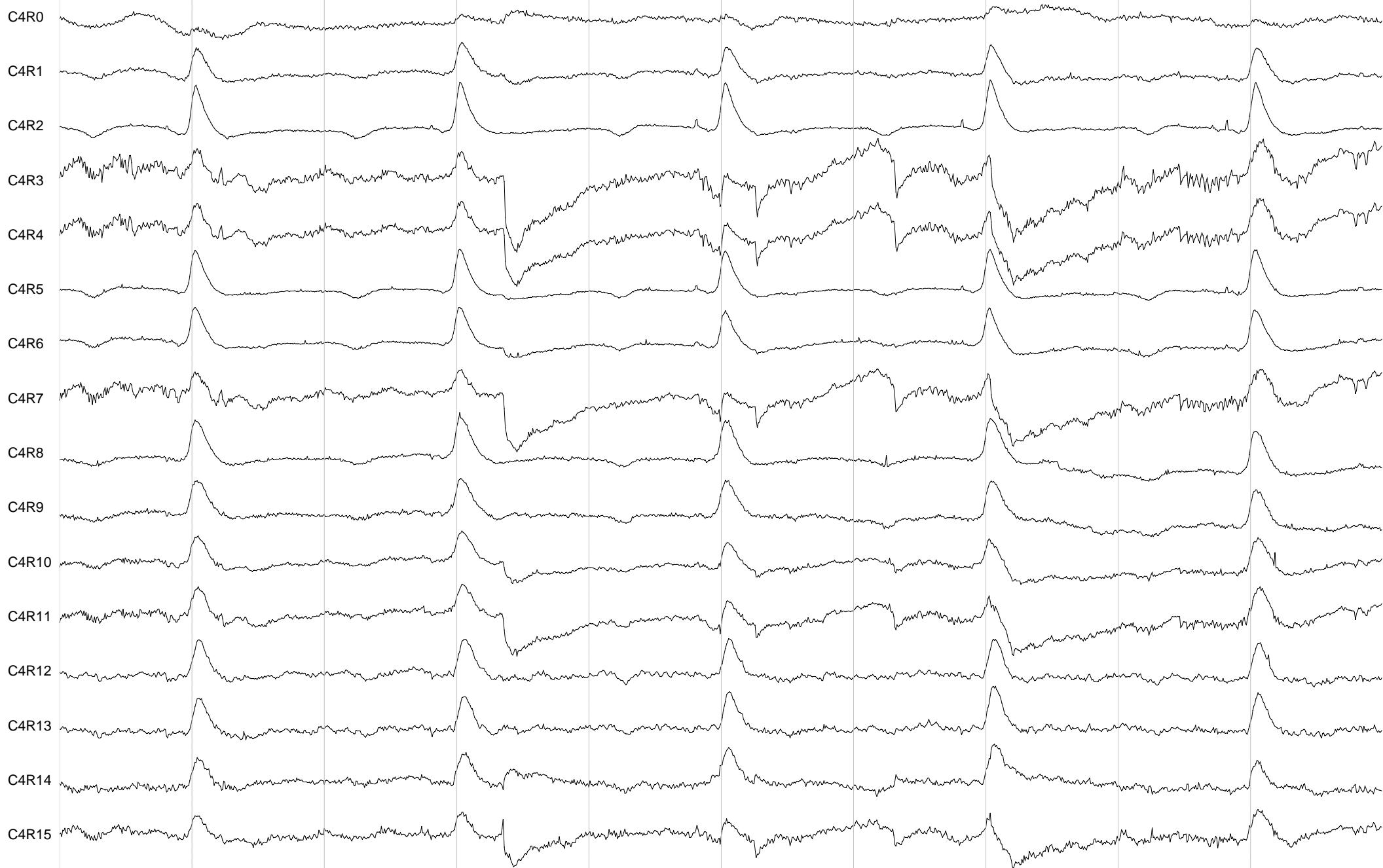
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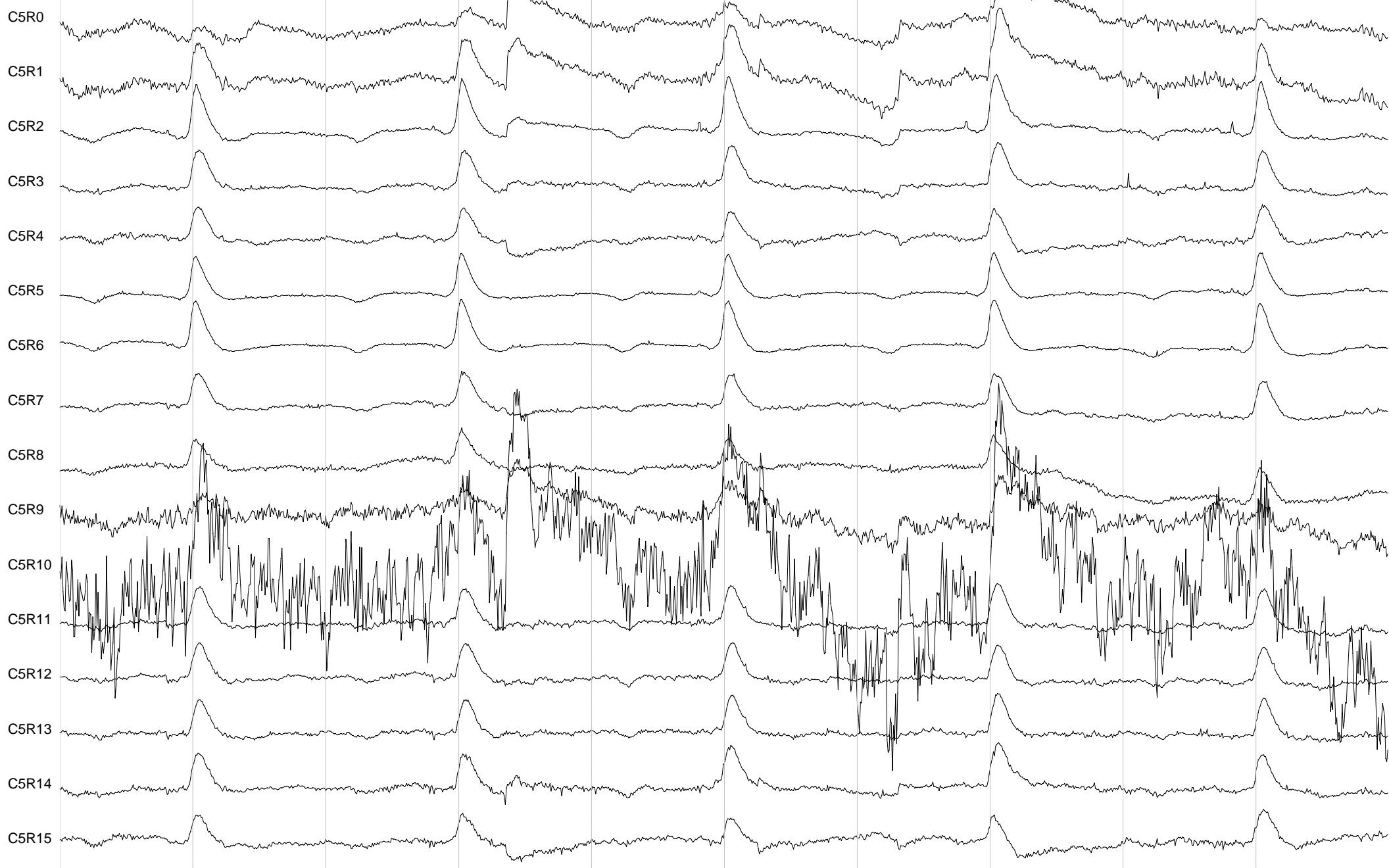
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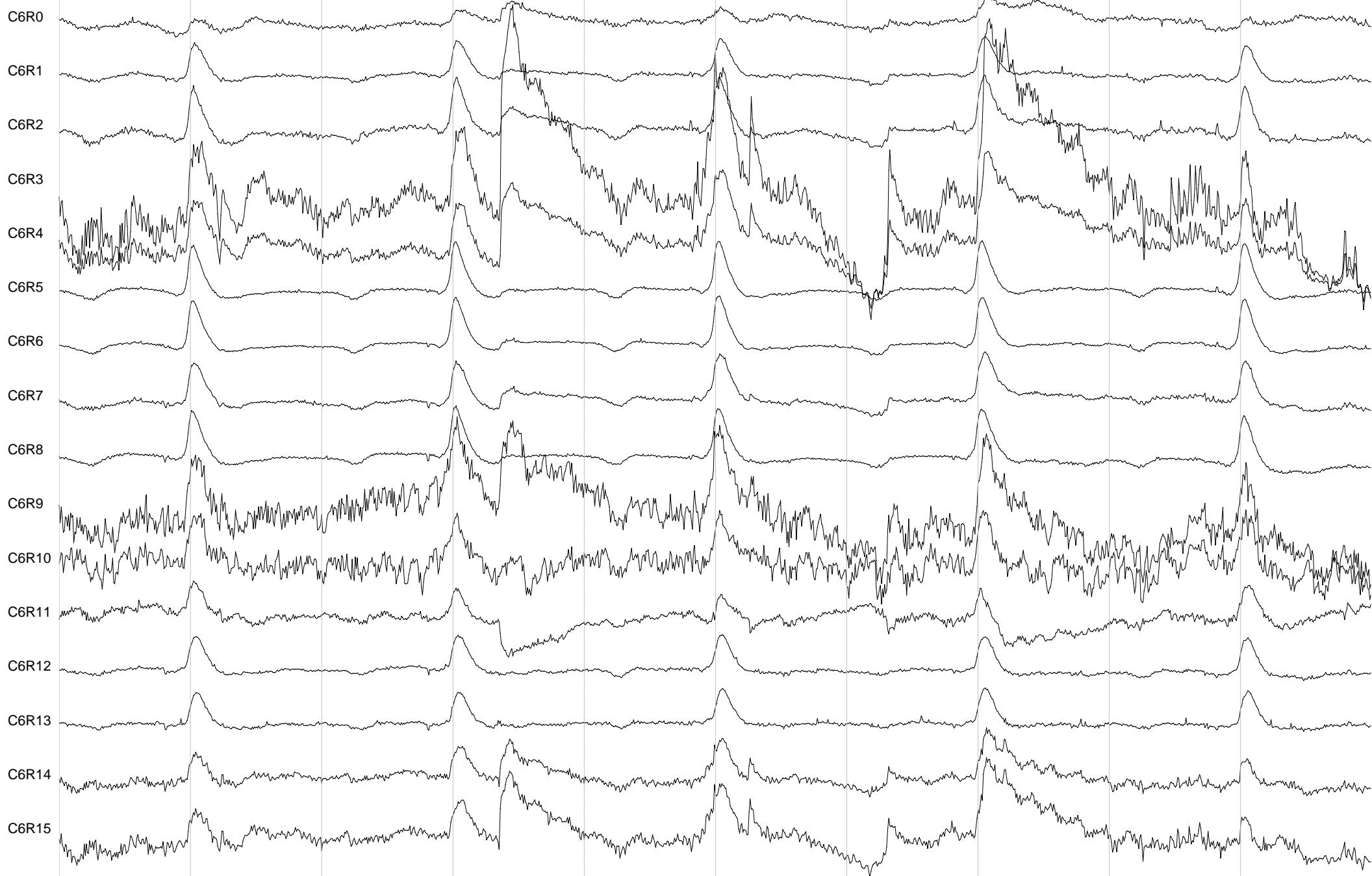
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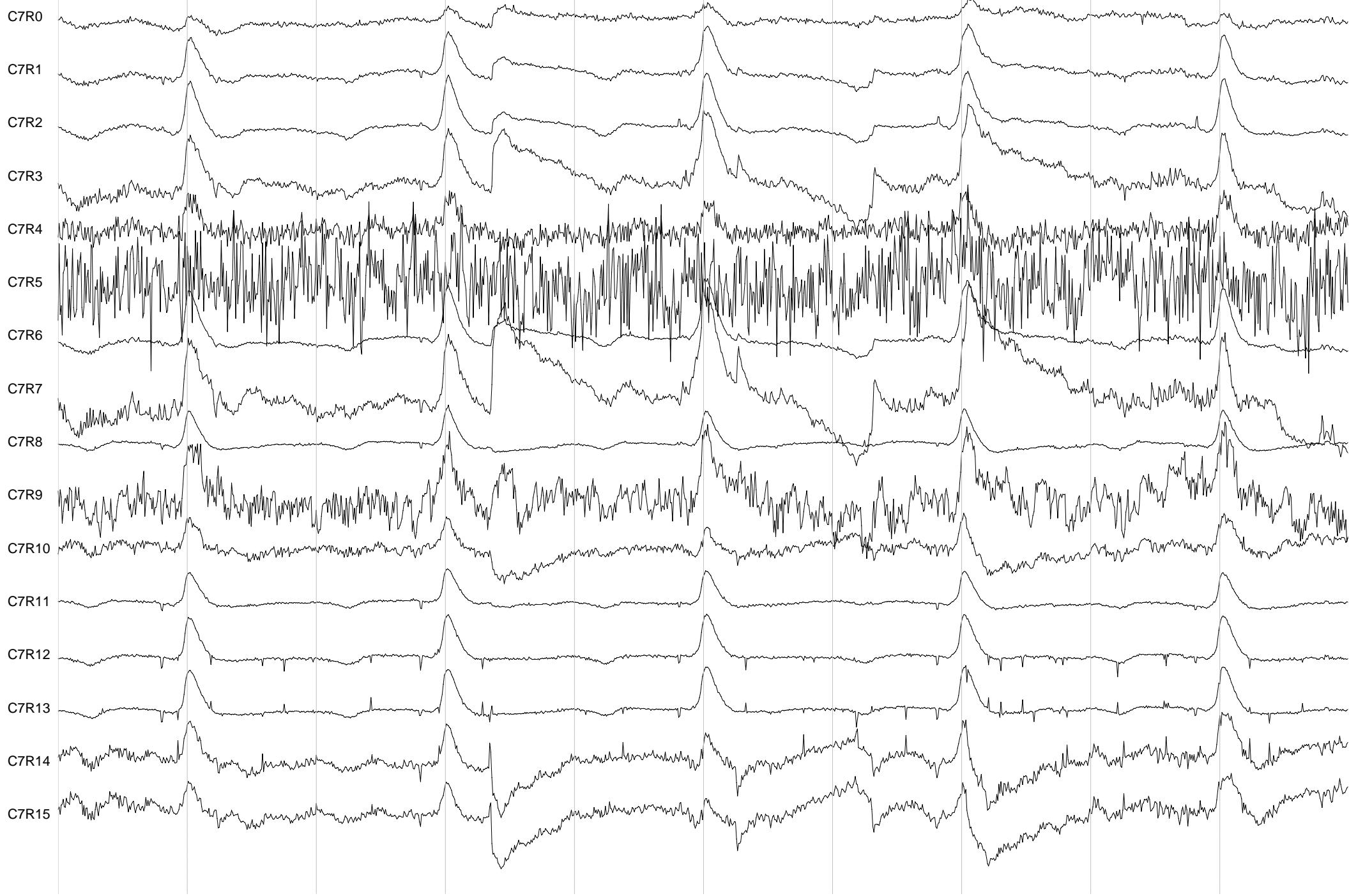
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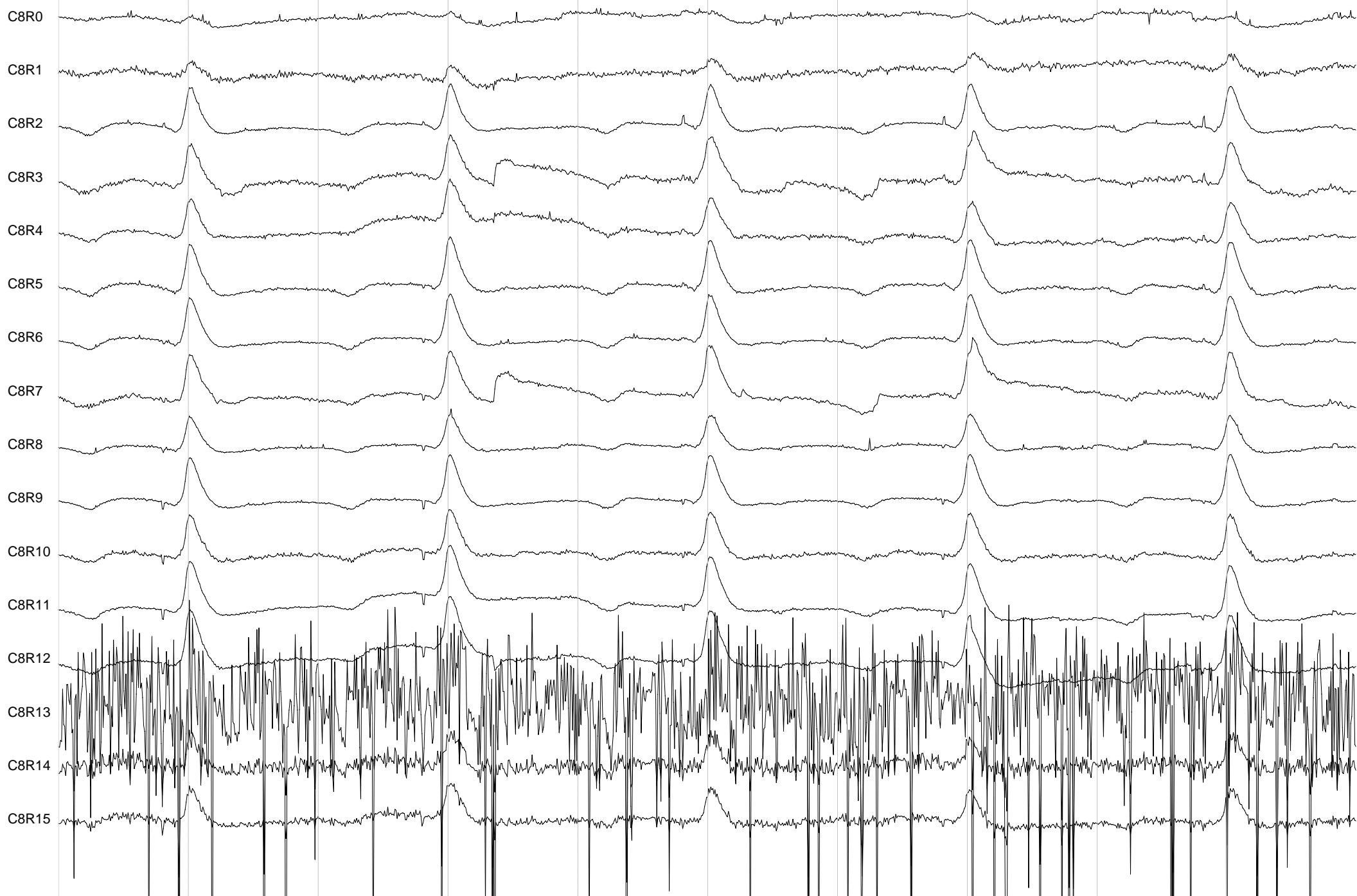
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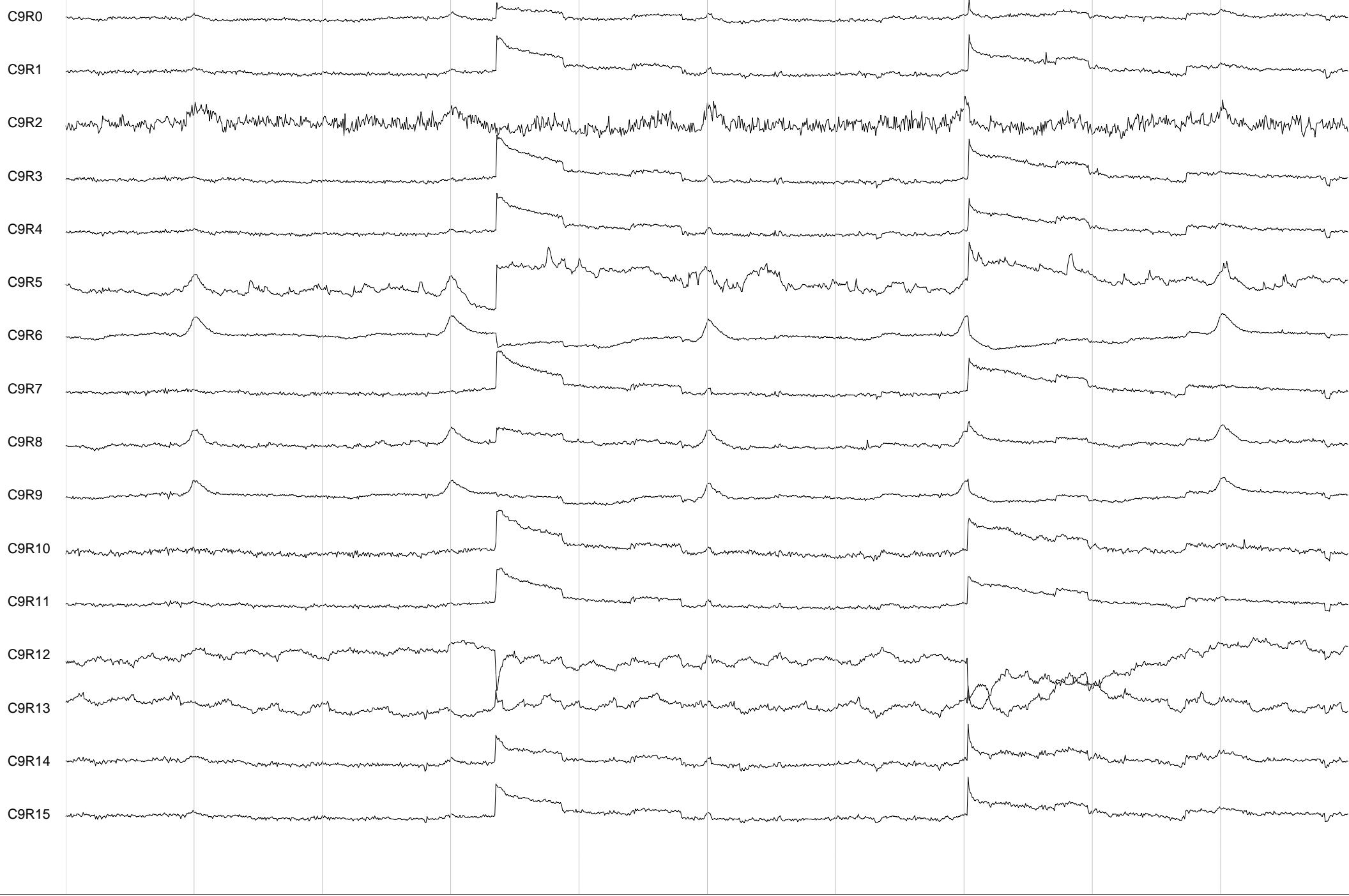
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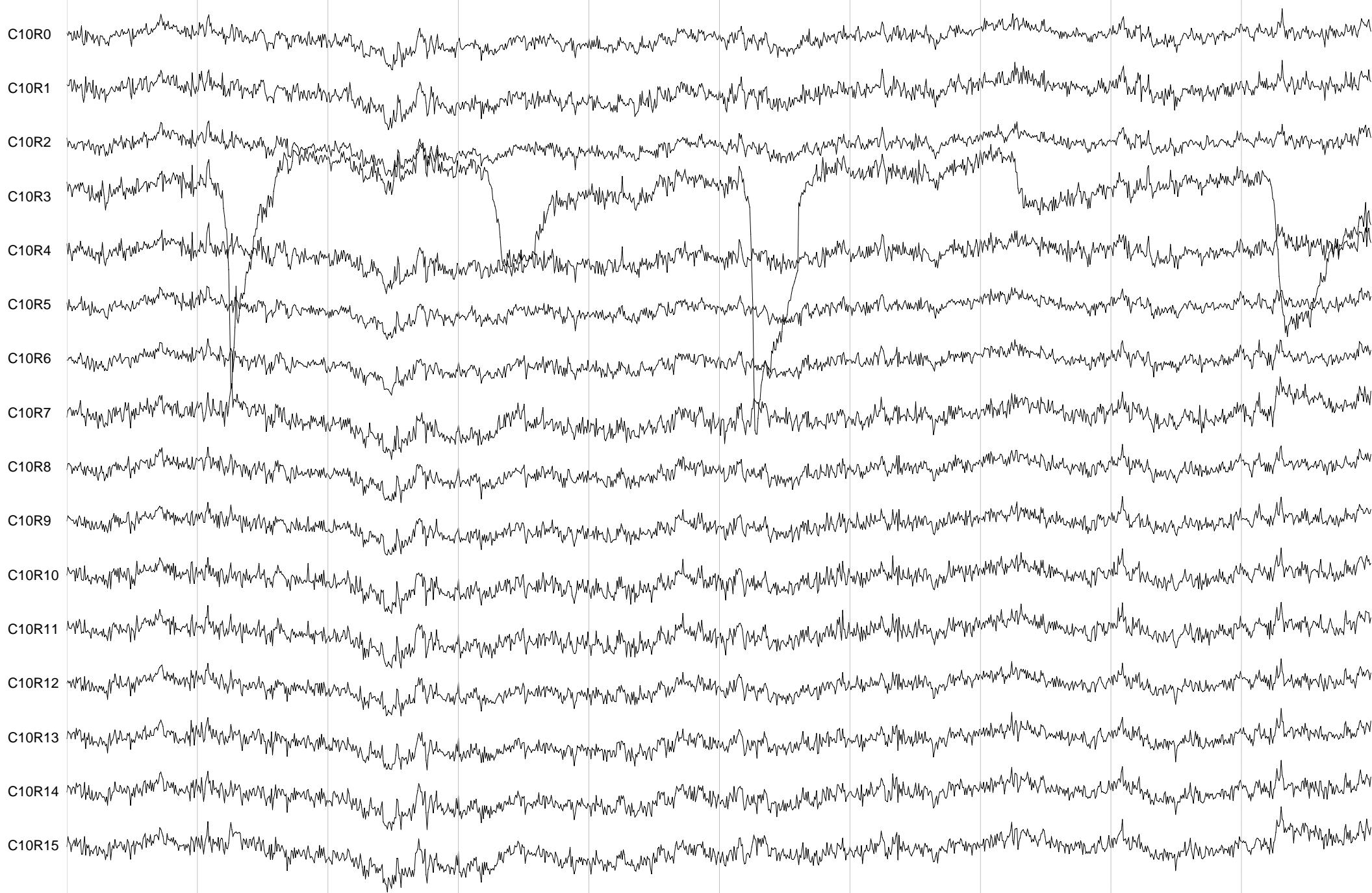
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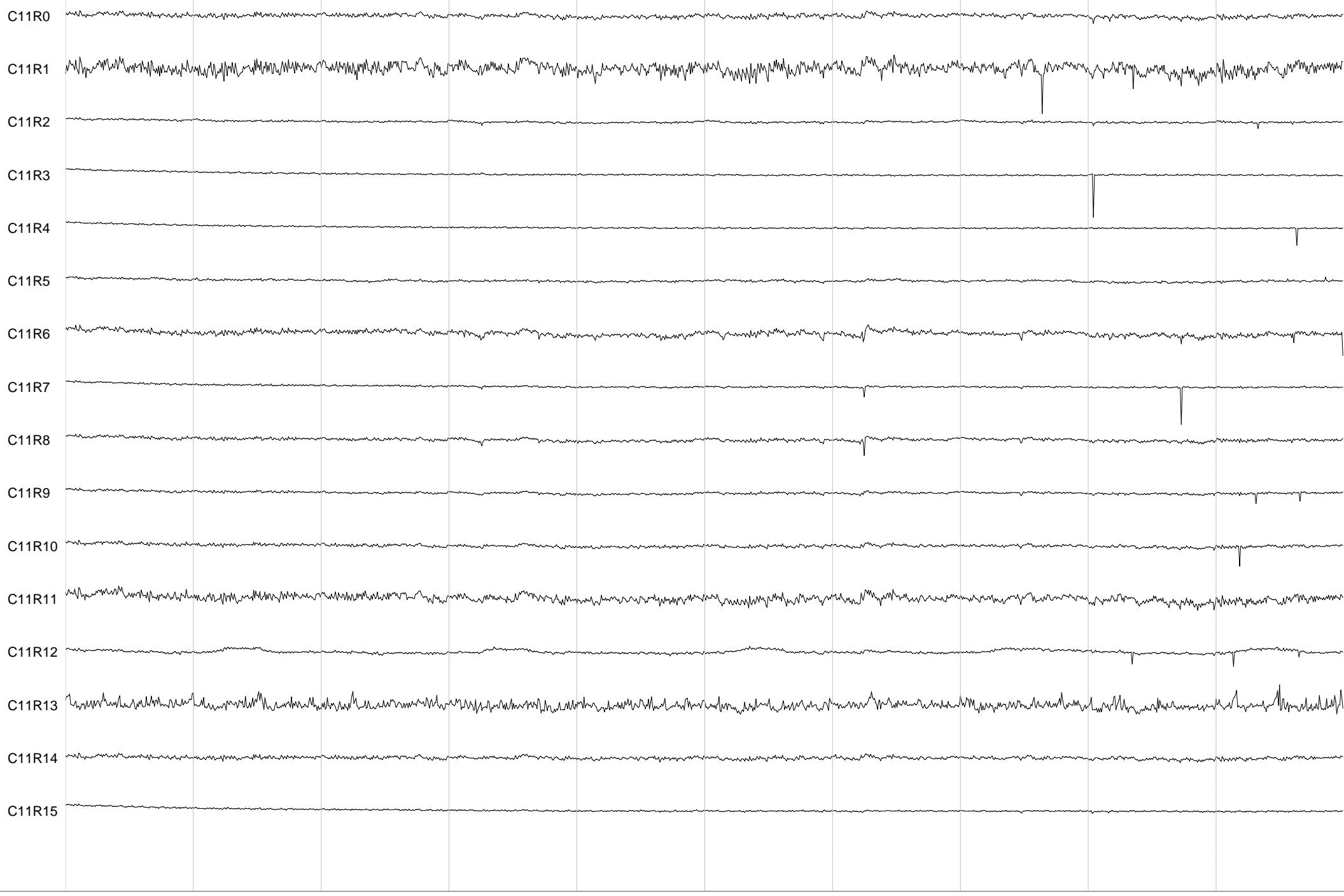
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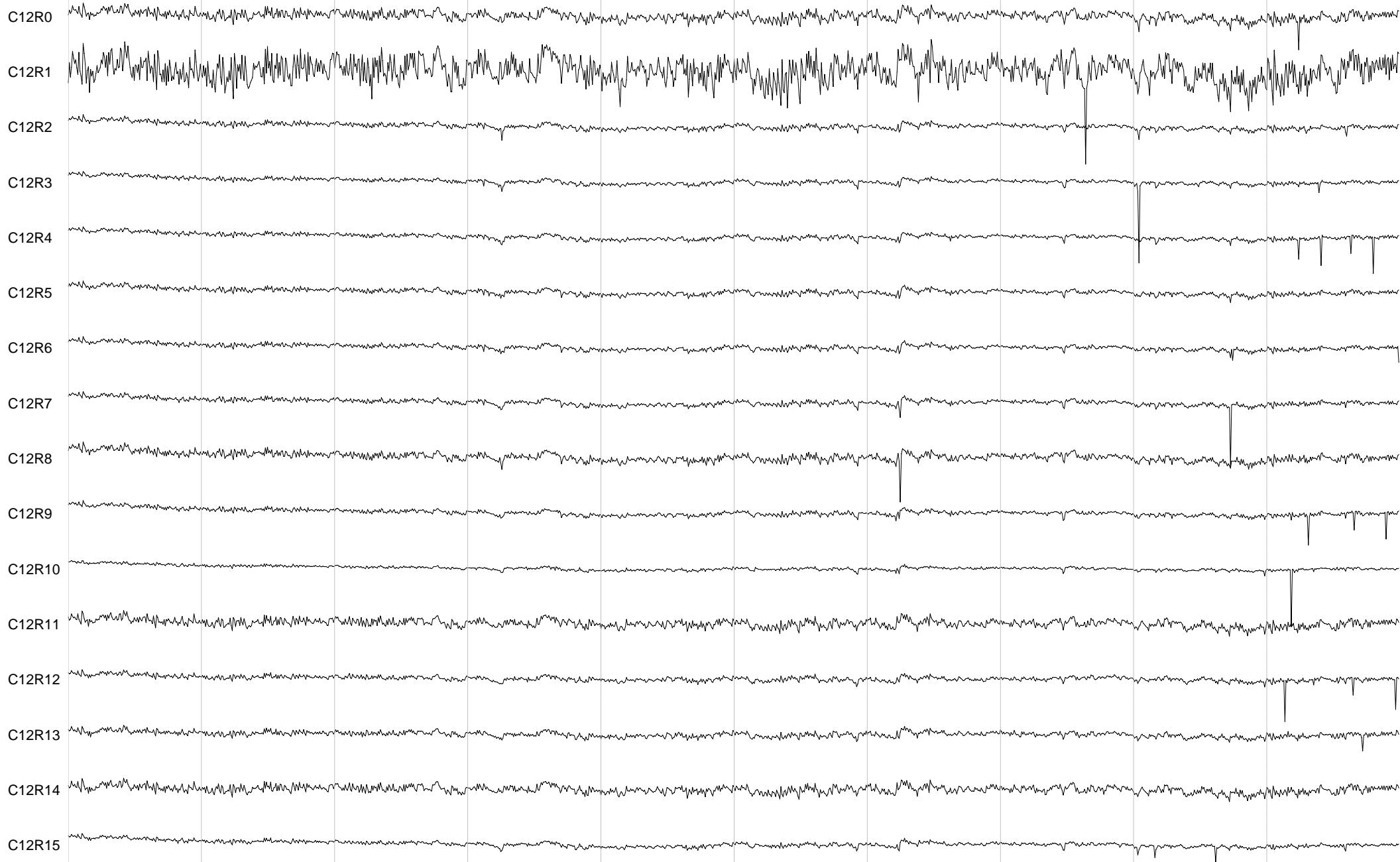
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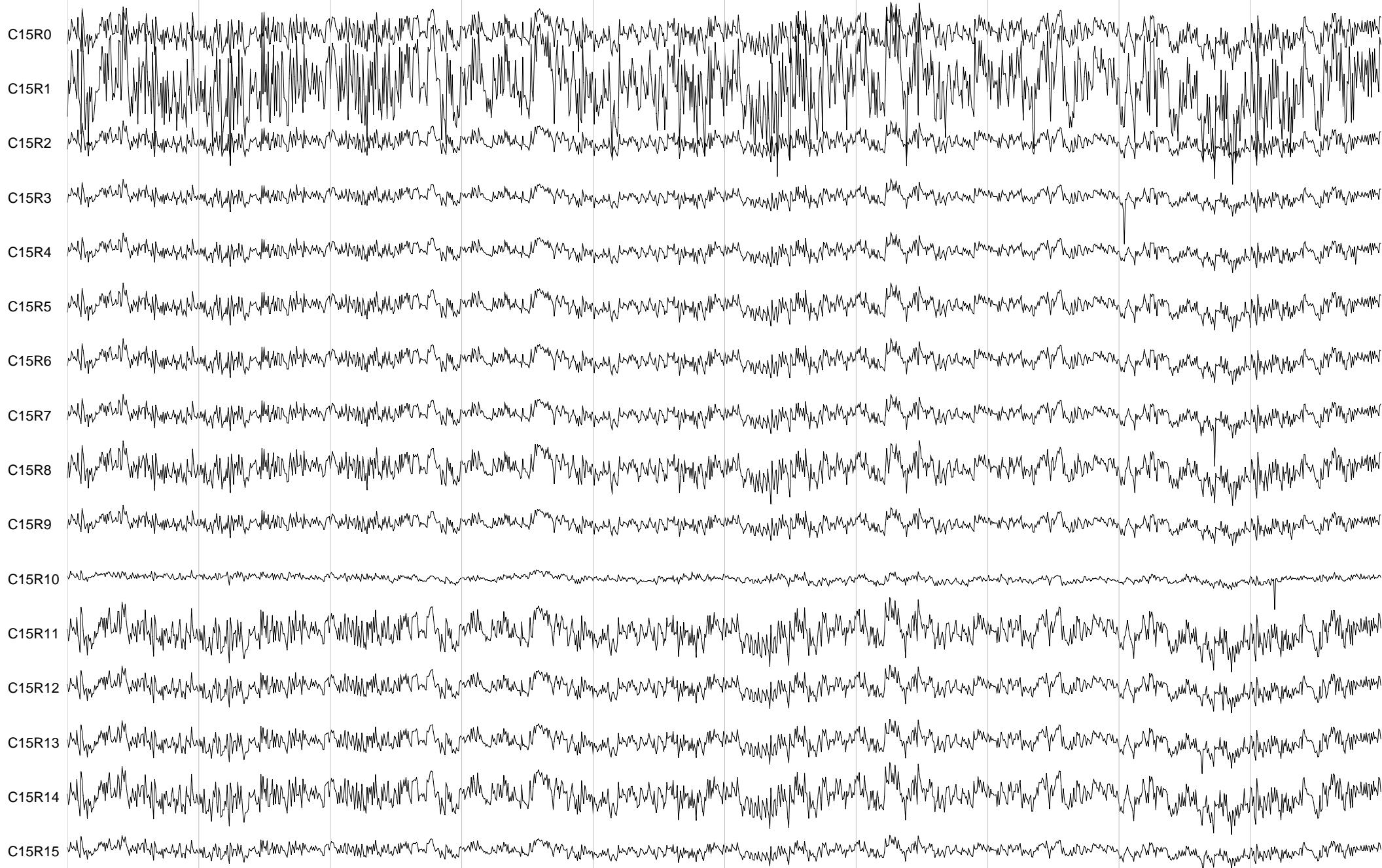
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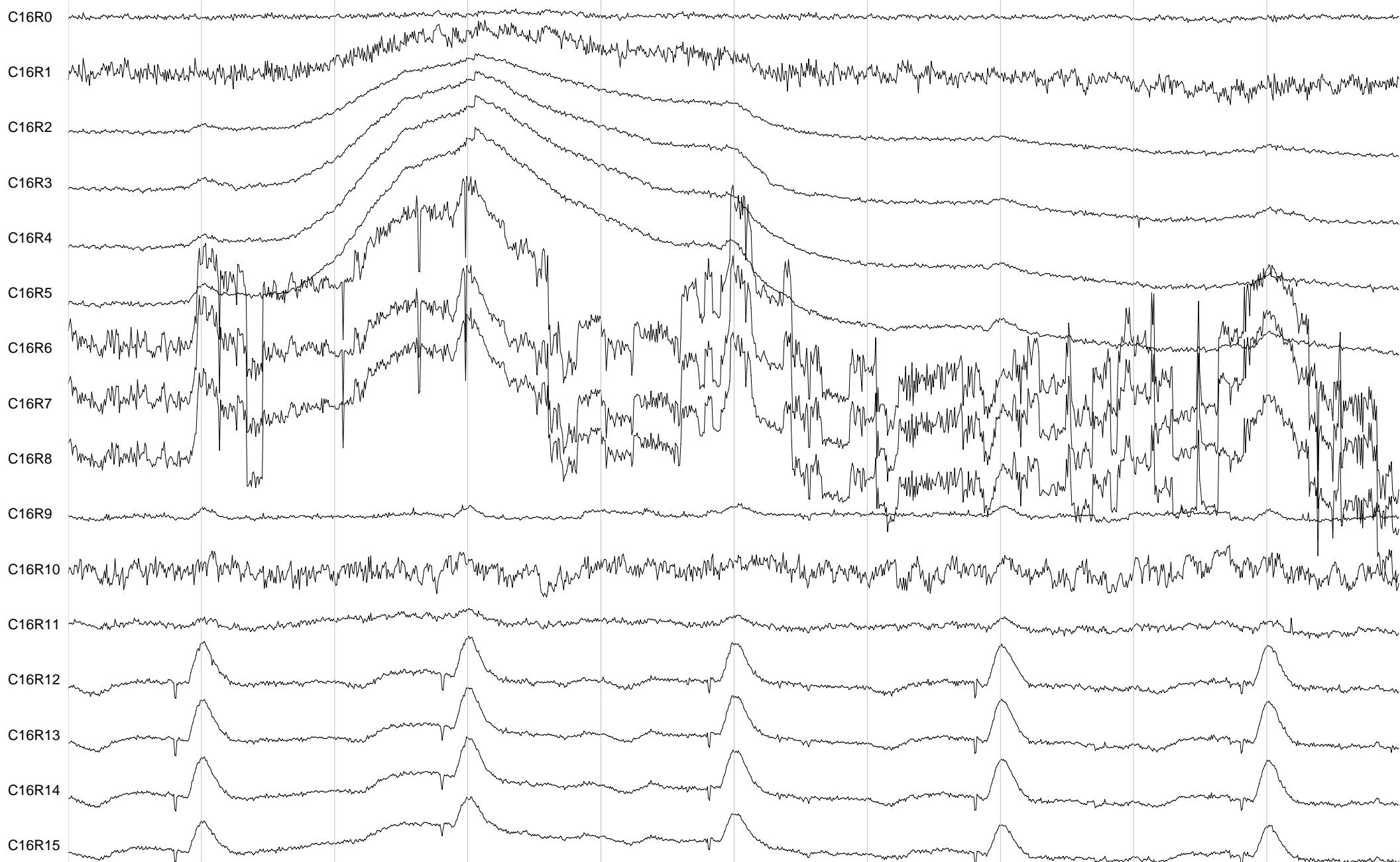
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