

SUPPORTING INFORMATION for

Silicon Field Effect Transistors as Dual-Use Sensor-Heater Hybrids

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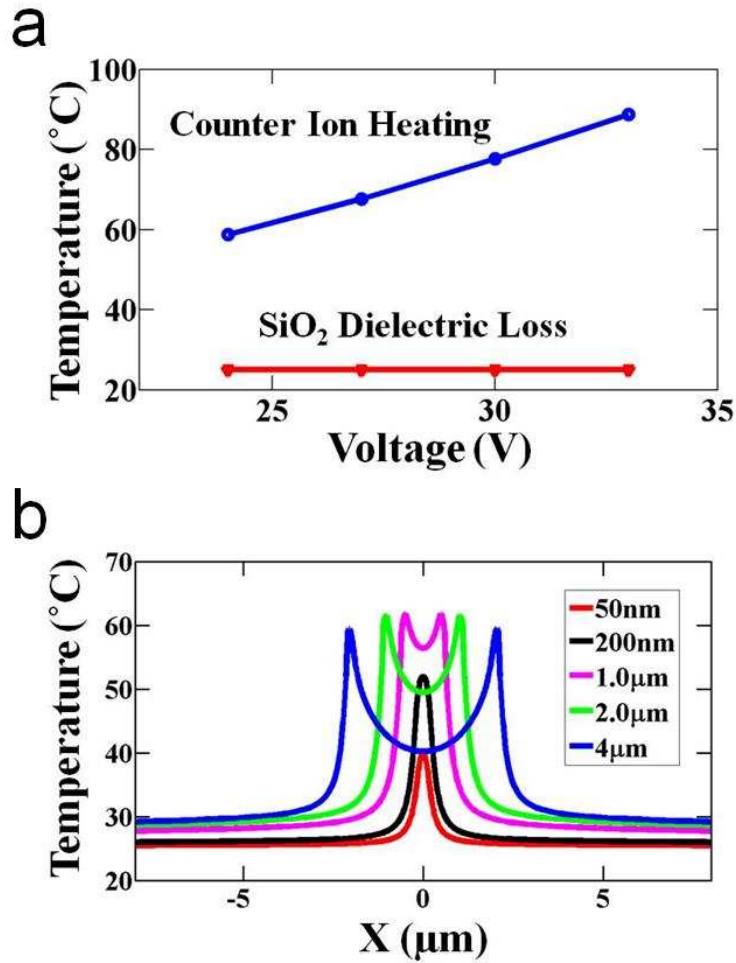


Figure 1: **a**, Simulation results showing that dielectric loss in SiO₂ accounts for negligible heating – the primary mechanism is dielectric relaxation of the counter ions in the fluid. **b**, Simulation results for Device 1 (at 27V) indicate that as device width is scaled, the average temperature increases initially as the regions influenced by fringing fields forms a greater portion of active region and eventually thermal diffusion limits achievable surface temperatures.

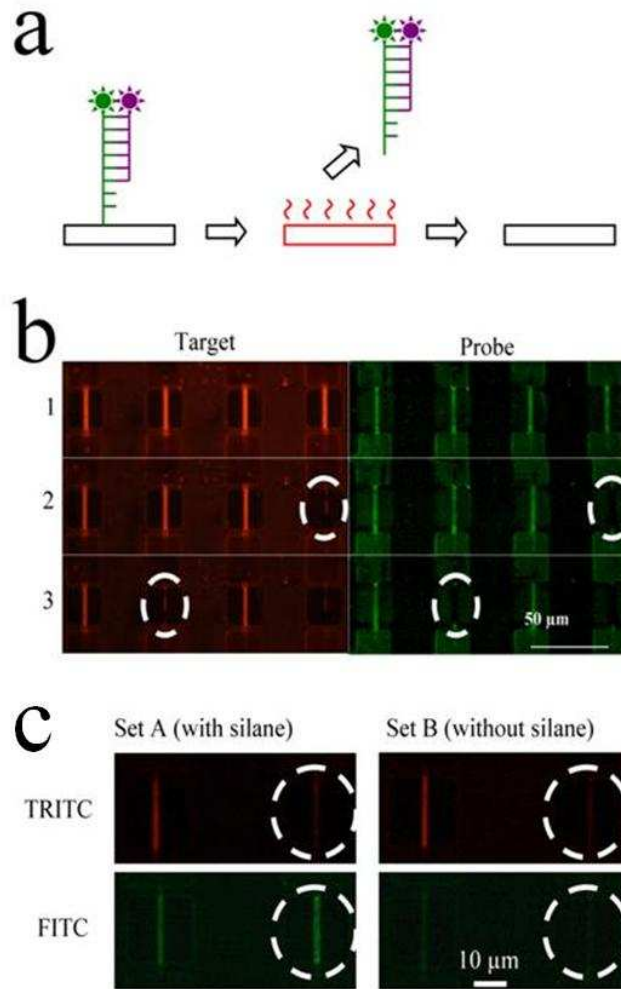


Figure 2: Control experiments for the desorption and refunctionalization experiments. **a**, Schematic summarizing the procedure for the heat mediated removal of the target-probe duplex from the surface. **b**, Fluorescent images showing the electrically addressable decomposition of the functional layer. The top image was taken before the experiment. Consecutive images are taken after heating the device encircled with dashed white lines. **c**, Images are shown depicting devices from Set A (left column) and Set B (right column) acquired with two different set of filters (TRITC – first row and FITC – second row) after the refunctionalization step. Devices on the right of each image were heated (encircled in dashed-white), and only set A (which was immersed in the silane solution) registers an improvement in the green fluorescence after refunctionalization with a sequence C modified with a FITC fluorophore. This lends evidence to the theory that the entire functionalization stack has lifted off after heating.