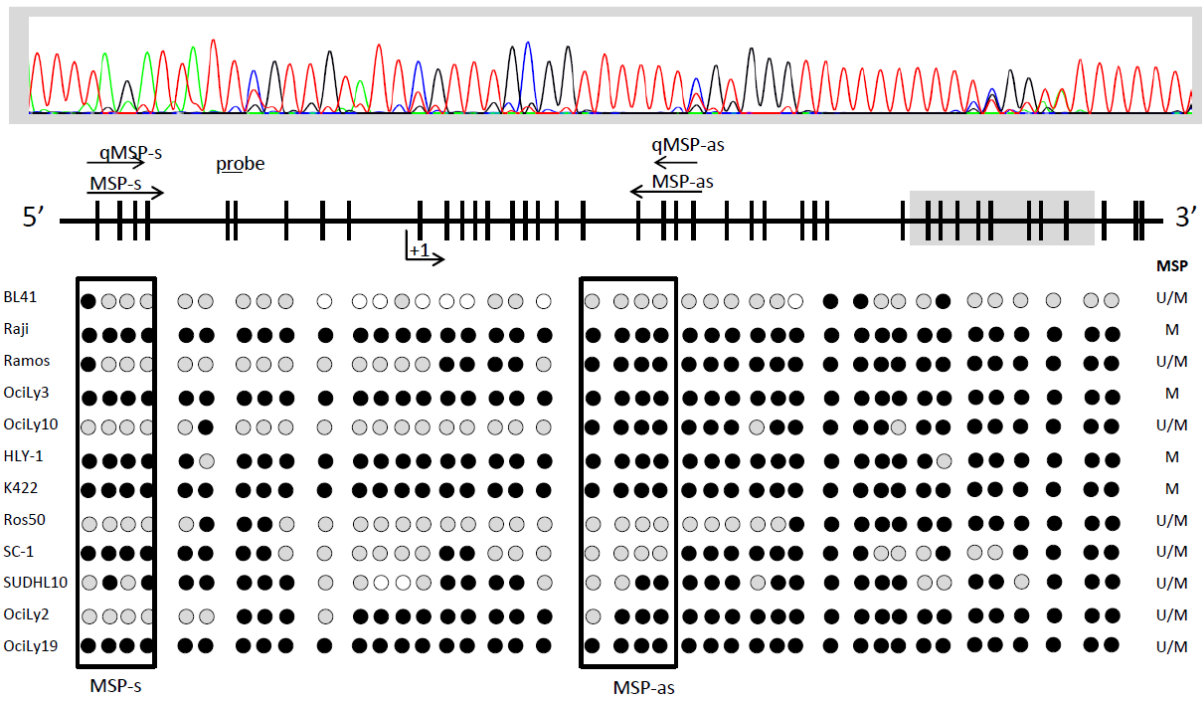


# Figure S2

## A. DSP



## B. FZD8

