

Fig 5

Fig. 5E Input

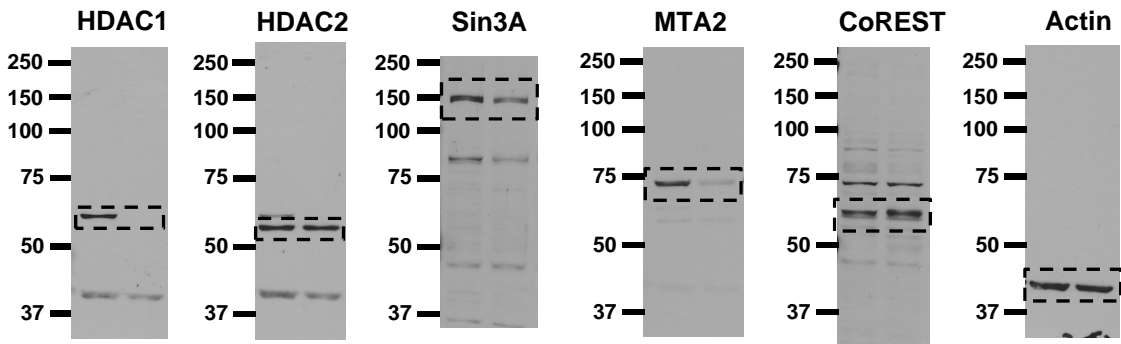


Fig. 5E Sin3A-IP

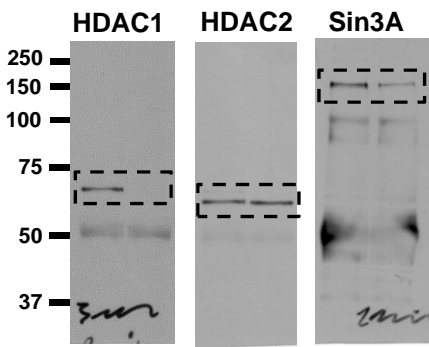


Fig. 5E MTA2-IP

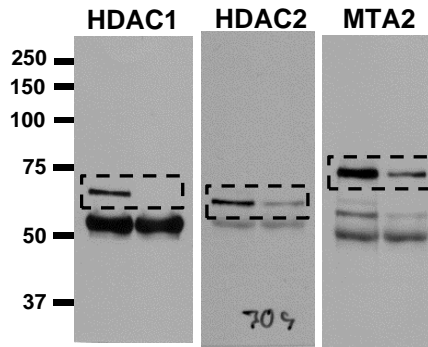


Fig. 5E CoREST-IP

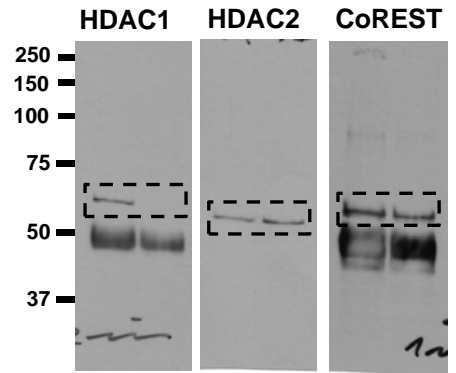


Fig. 5G Input

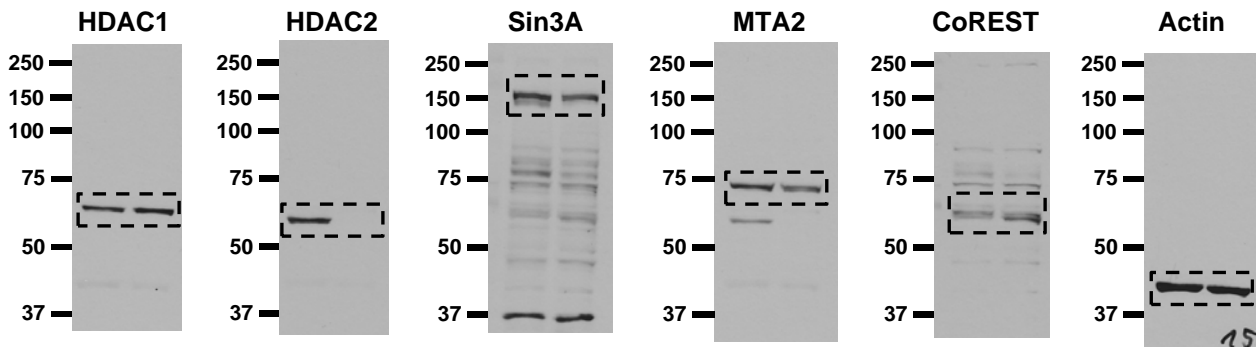


Fig. 5G Sin3A-IP

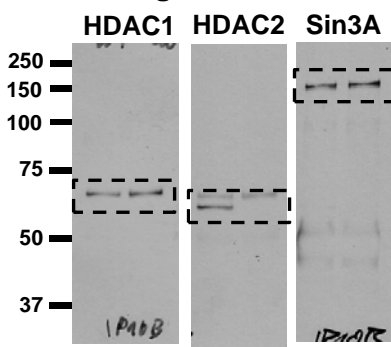


Fig. 5G MTA2-IP

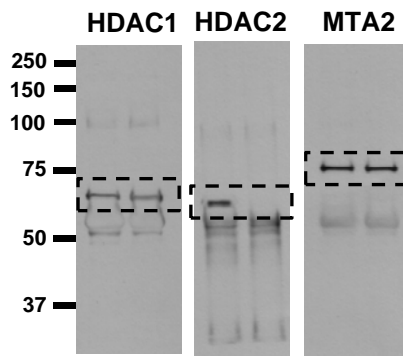


Fig. 5G CoREST-IP

