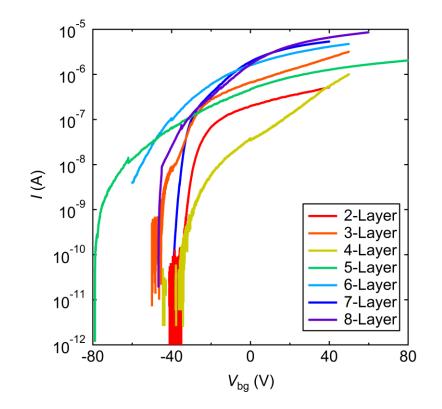
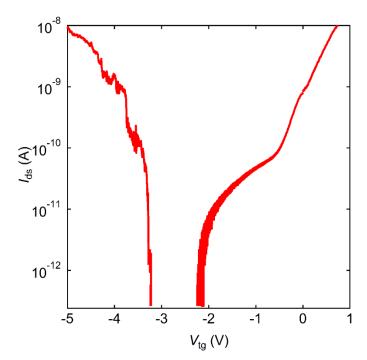
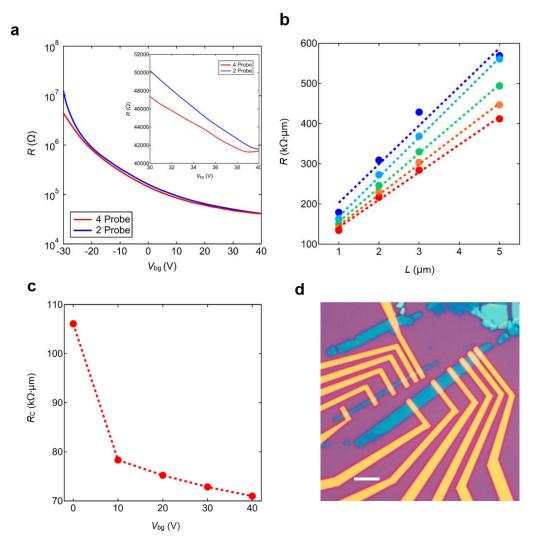
Supplementary Figures



Supplementary Figure 1: Transfer curves of few-layer ReS_2 FETs. The bias voltage V_{ds} was fixed at 100 mV.



Supplementary Figure 2: Transfer curve of an ambipolar ReS₂ EDLT. The source-drain voltage was fixed at 100 mV, and the back gate remained floated. The measurements were taken at 220 K.



Supplementary Figure 3: Contact of ReS_2 FETs. a) Comparison of the two-probe and four-probe measurements. The inset is the zoom-in data when V_{bg} varies from 30 V to 40 V. b) The channel resistance with different length under various back gate voltage. c) The contact resistance as a function of back gate voltage. d) The microscope image of the device. The scale bar is 5 µm.

Supplementary Notes

Supplementary Note 1: Few-layer ReS₂ FETs

We measured over 40 ReS₂ FETs with thicknesses ranging from 0.8 to 7.5 nm (1-10 layers with an interlayer spacing of approximately 0.7 nm). All of the FETs behaved similarly as excellent n-type FET devices with the back gate swept between -80 V and +80 V. Some of the data from few-layer devices are summarized and shown in Supplementary Figure 1. The current on/off ratio can reach up to 10^7 - 10^8 , which is comparable to that of MoS₂ devices [1]. All measurements were carried out in a vacuum (approximately 10^{-5} mbar) at room temperature.

Supplementary Note 2: Ambipolar behavior of ReS₂ electric double-layer transistor

To further explore the field effect of mono- and few-layer ReS_2 devices, an electric double-layer transistor (EDLT) using ionic liquid gating is introduced. In our experiments, the EDLT was fabricated by dropping a droplet of an ionic liquid, N,N-diethyl-N-(2-methoxyethyl)-N-methylammonium

bis(trifluoromethylsulfonyl)imide (DEME-TFSI), onto the surface of a ReS₂ FET device. For more efficient tuning of carrier density, the side electrode pad of the ionic liquid was designed to have a larger area than the ReS₂ channel of the device. To avoid possible chemical reactions between the ionic liquid and ReS₂, all measurements were performed at 220K and in a vacuum environment of approximately 10^{-5} mbar. An ambipolar behavior was observed when we swept the ionic liquid gate voltage (V_{LG}), with a typical transfer curve measured from a seven-layer ReS₂ device shown in Supplementary Figure 2. When V_{LG} is above -2 V, the device behaves as an n-type transistor. When V_{LG} is below -3.2 V, a p-type transistor behavior appears, indicating the shift of Fermi-level $E_{\rm F}$ to access the valence band.

Supplementary Note 3: Contact of ReS₂ FETs

During the fabrication of mono- and few-layer ReS₂ FETs, we used 5 nm Ti covered by 50 nm Au as electrodes to make the contact. These devices have shown good contact behavior. To estimate the influence of contact resistance, we compared the measurement results obtained from both two-probe and four-probe methods. As shown in Supplementary Figure 3a, the resistance measured by the two-probe method (R_{2P}) is slightly larger than that of the four-probe method (R_{4P}) . The resistance difference (ΔR) of R_{2P} and R_{4P} should be mainly attributed to the contact resistance. If we define $\alpha = \frac{\Delta R}{R_{2P}}$ as the weight of ΔR in R_{2P} , we found α is highly gate-tunable. In the n-doped regime as our work focused on, α is less than 10% (when $V_{bg} = -20$ V) and decreases notably as the doping level increases, to be as low as 1% when $V_{bg} = 40$ V.

We also used the transfer length method to estimate the contact resistance of the ReS₂ FETs. As shown in Supplementary Figure 3b, we measured the channel resistance with different length under various back gate voltage V_{bg} . The extrapolated contact resistance $R_{\rm C}$ versus V_{bg} is shown in Supplementary Figure 3c. The contact resistance decreases with the increasing V_{bg} , ranging from 71 k Ω to 106 k Ω , which is similar to the results reported [2]. Supplementary Figure 3d shows the optical image of the multi-terminal device.

Supplementary Note 4: Carrier mobility calculation

Our carrier mobility calculation was performed using the VASP (Vienna ab-initio Simulation Package) code [3, 4]. The results presented in the following were obtained by using the generalized gradient approximation (GGA)-Perdew-Becke-Erzenhof (PBE) function [5], a $10\times5\times1$ mesh for the Brillouin zone sampling and a cut-off of 500 eV for the plane-wave basis set. An orthogonal supercell was created for ReS₂ sheets, with the atomic plane and its neighboring image separated by a 36 Å vacuum layer. All of the structures were relaxed until the Hellmann–Feynman forces became less than 0.01 eVÅ⁻¹.

In 2D materials, the carrier mobility is given by the following formula [6, 7]:

$$\mu_{2D} = \frac{2e_0\hbar^3 C_q}{3k_B T E_1^2 m^{*2}}$$
(1)

where m^* is the effective mass and *T* is the temperature (set to be 300K in our calculation). The term E_1 is the deformation potential constant, which denotes the shift in the band edges along the transport directions induced by strain ε (calculated using a step of 1%). It is defined as $E_1 = \frac{\partial E_M}{\partial \varepsilon}$, where E_M is the energy of the conduction band minimum for electrons and the valence band maximum for holes. C_q is the stretching modulus caused by ε , which can be calculated by $C_q = \frac{1}{S_0} \frac{\partial^2 E_{\text{total}}}{\partial \varepsilon^2}$, where E_{total} is the total energy and S_0 is the lattice volume at equilibrium for a 2D system.

Supplementary References

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