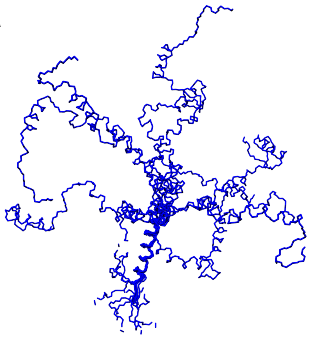
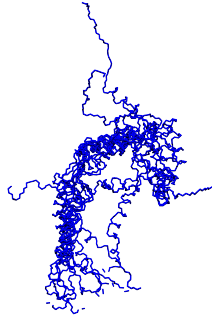


A



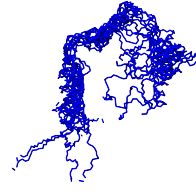
+ CS
+ DC

B



+ CS
+ DC
+ PRE: L41C MTSL

C



+ CS
+ DC
+ PRE: L41C MTSL
+ PRE: MnEDTA