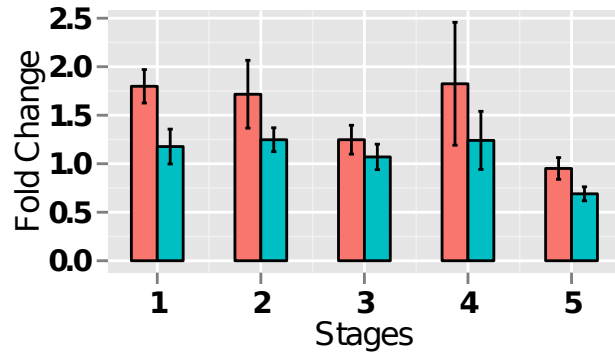
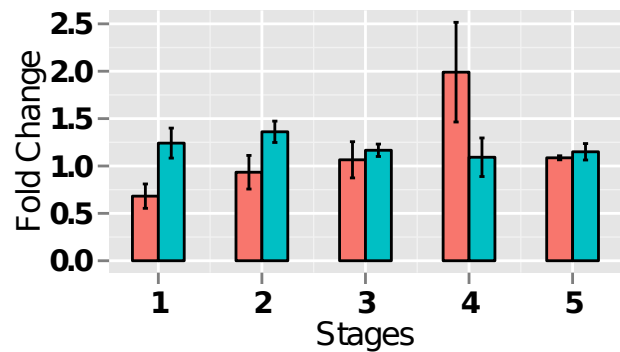


(a) MIPS1



(b) MRP-L



(c) MRP-N

