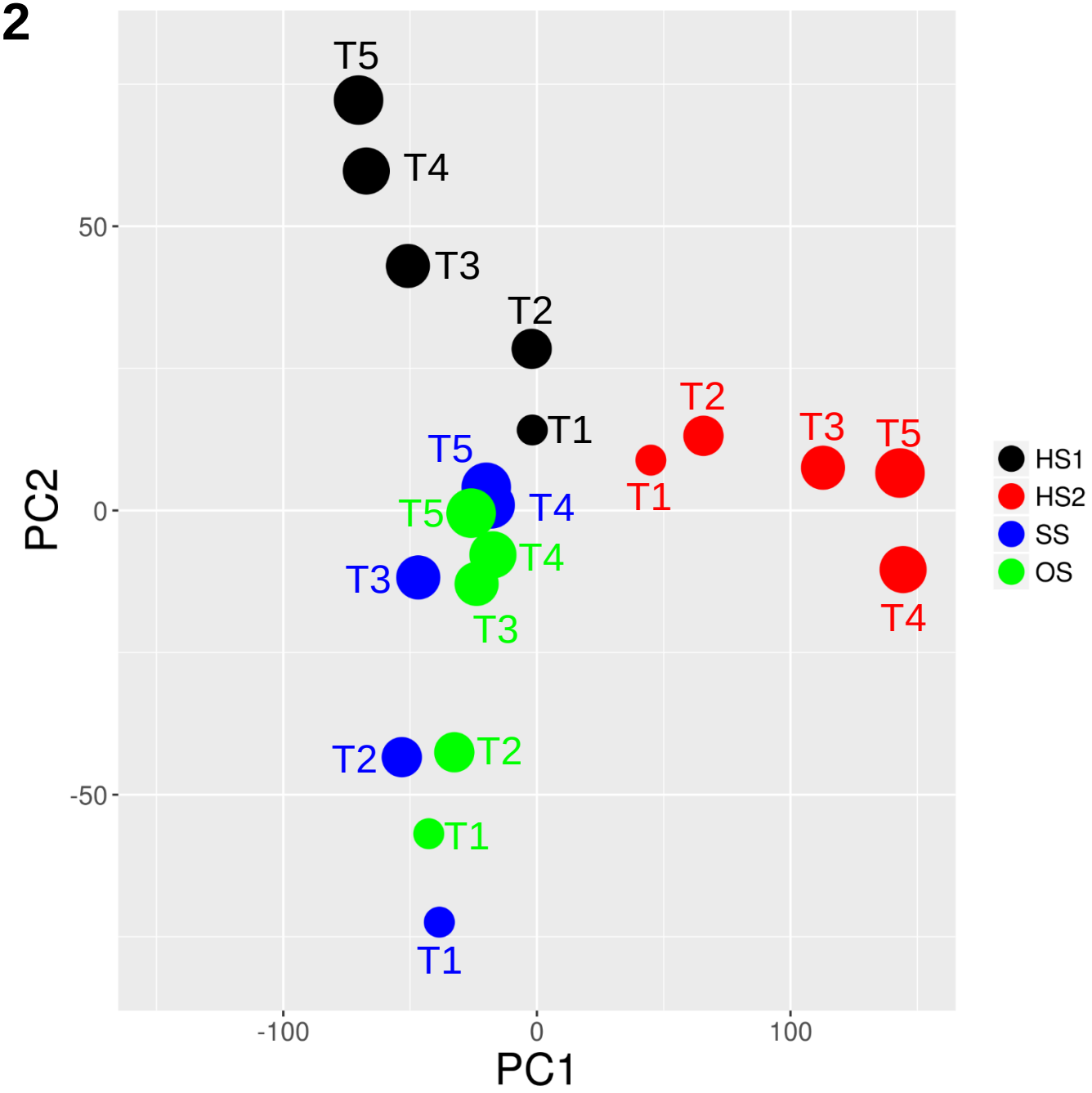
Figure S2


T5
T4
T3
T2
T1
T2
T1
T3
T5
T4
T5
T5
T4
T4
T3
T3
T2
T2
T1
T1
50
0
-50
PC2
-100
0
100
PC1
HS1
HS2
SS
OS