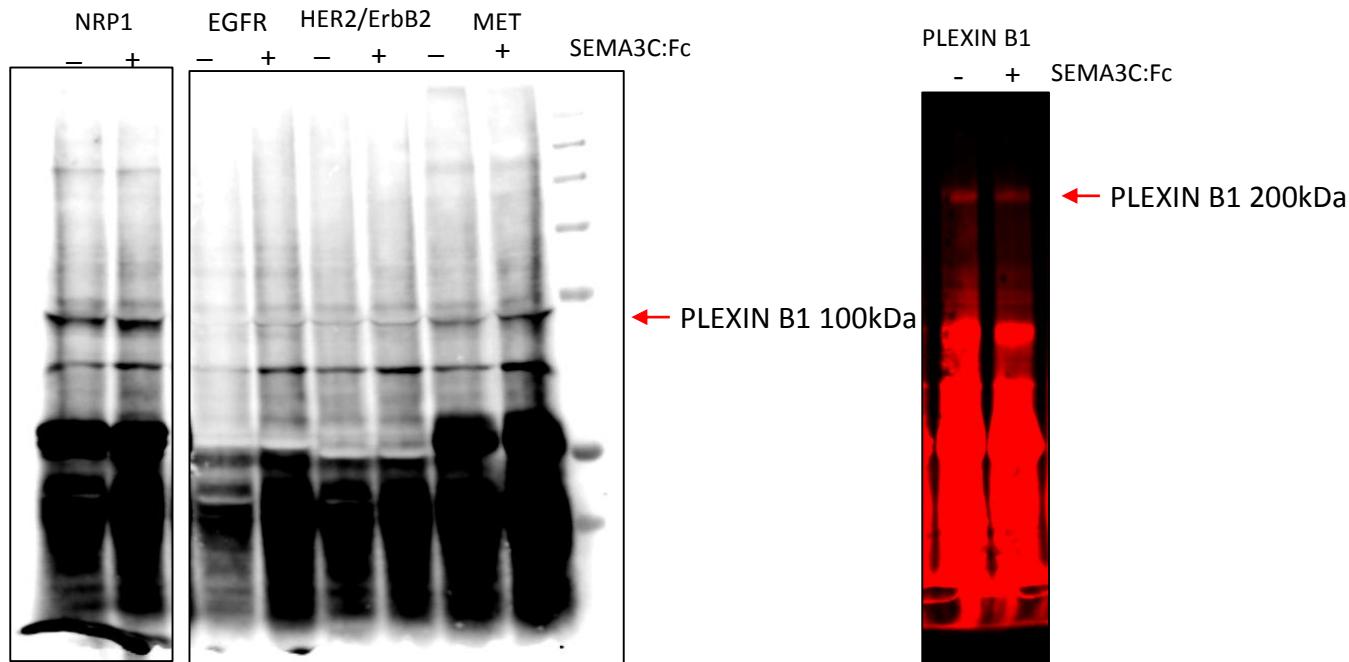


Figure 2 E

IP



inputs

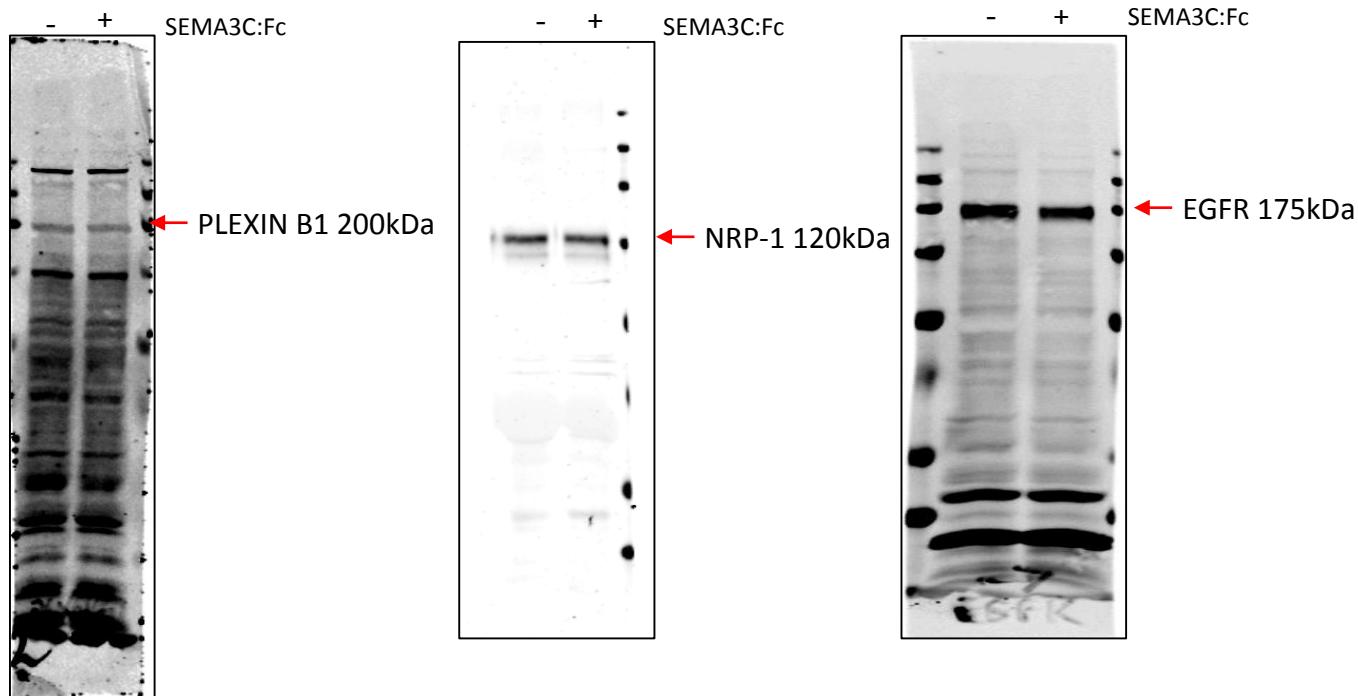


Figure 2 E

