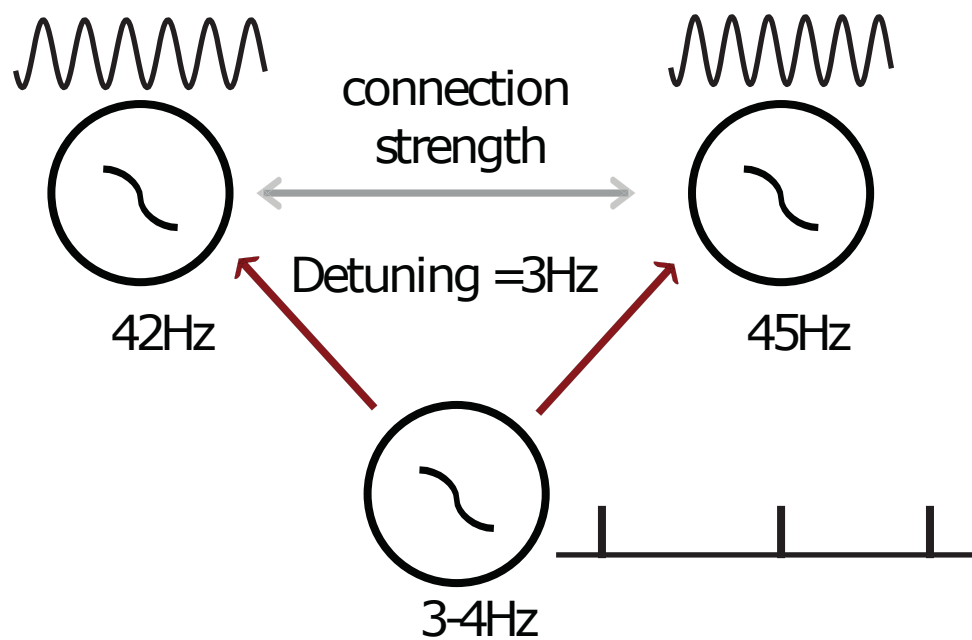
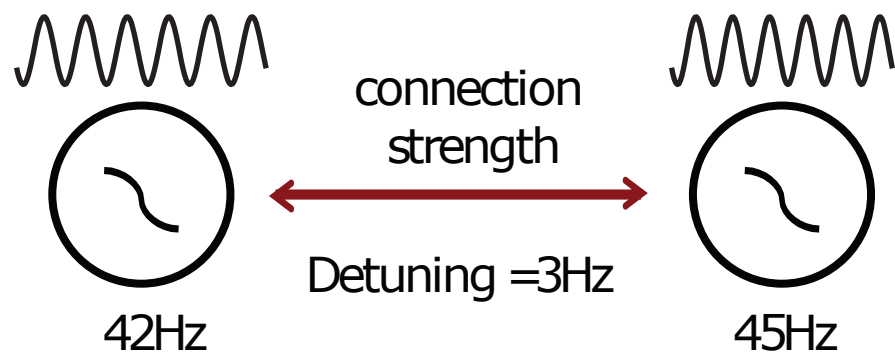


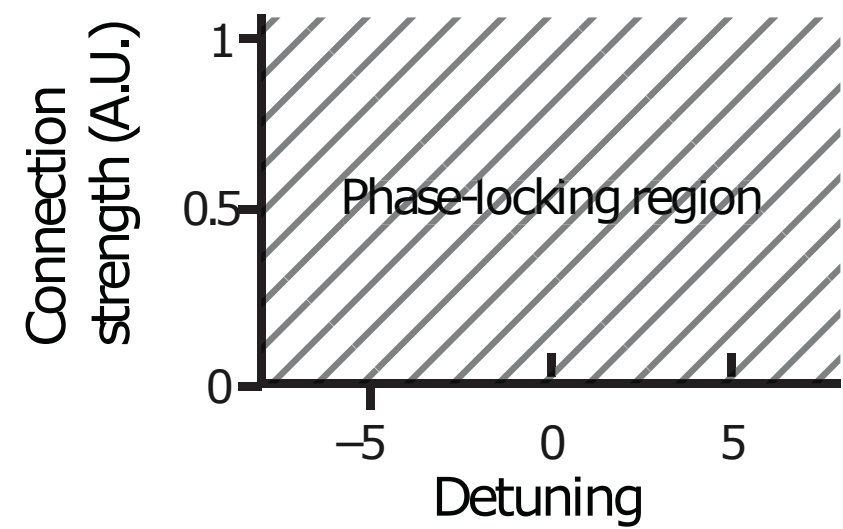
A External phase-reset



B Mutual synchronization



C



D

