

Supplementary Information for

Wafer-recyclable, environment-friendly transfer printing for large-scale thin-film nanoelectronics

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Supplementary Figure Legends Figs. S1 to S20

Other supplementary materials for this manuscript include the following:

Supplementary Movie Legends Movies S1 to S3

SI Appendix Figure Captions

Fig. S1. SEM images of various kinds of nanomaterials used as model systems in this report, including Si NM (top left), MoS₂ monolayer (top right), Si NRs (bottom left), and Ag NW (bottom right). The scale bars are 60 μ m, 150 μ m, 160 μ m, and 12 μ m, respectively.

Fig. S2. Representative microscope images (scale bars: 400 μ m) of the thin film nanoelectronics after the debonding process.

Fig. S3. Optical images (scale bar: 2.5 cm) of a SiO₂/Si wafer that is recycled multiple times after the transfer printing processes.

Fig. S4. Optical images (scale bar: 2.5 cm) of a representative thin film nanoelectronics transferred from their fabrication SiO₂/Si wafer onto the surface of a building window (A) and covered with a commercial sticker to conceal the electronics (B).

Fig. S5. Optical images (scale bar: 1.8 cm) of a donor SiO₂/Si wafer (A) and a receiver thermally releasable tape (B) after the interfacial debonding process under dry air condition. The magnified views (scale bar: 350 μ m) appear in the images on the bottom frame.

Fig. S6. A magnified view (scale bar: 1.5 cm) of a testbed sample placed in the loading stage of a custom-modified mechanical peeling apparatus used in this study.

Fig. S7. Experimental data of peeling strength as a function of peeling distance in water for varied peeling angles from 30° to 120° (A) and Ni thicknesses from 30 nm to 2.4 μ m (B).

Fig. S8. Experimental data of steady-state peeling strength as a function of peeling rate in water.

Fig. S9. FEA results of maximum principle strain distribution in thin films at peeling distance of 1, 6, and 11 mm in water (A) and dry air conditions (B).

Fig. S10. FEA results of maximum principle strain distribution in thin films at peeling distance of 1, 6, and 11 mm in water for varied Ni thicknesses from 30 nm to 2.4 μ m.

Fig. S11. (A) An optical image (scale bar: 8 mm) of a testbed sample that includes residues of materials after the interfacial debonding process in dry air condition. (B) The corresponding peeling strength-peeling distance curve.

Fig. S12. The corresponding linear and logarithmic scale of the transfer curves in Fig. 3B.

Fig. S13. Circuit diagrams of AND (left), OR (middle), and NOT (right) logic gates.

Fig. S14. The corresponding I-V and transfer curves to the n-MOSFETs in Fig. 4E.

Fig. S15. Calibration curves (A) and measured temperatures (B) for Si NRsbased p-i-n diode and control IR sensor.

Fig. S16. Raman spectra of as-prepared MoS₂ monolayer.

Fig. S17. Cross sectional schematic illustration of the hybrid photodiode that is comprised of Si NM and MoS₂ monolayer.

Fig. S18. Characterizations of a control hybrid photodiode built on a SiO₂/Si wafer.

Fig. S19. FEA results of maximum principle strain distribution for the hybrid photodiode at peeling distance of 1, 6, and 11 mm in water.

Fig. S20. Schematic illustrations of bend deformation (A) and the corresponding moment-curvature relation for a peeling process of thin films (B).

SI Appendix Movies

Movie S1. An experimental demonstration showing the interfacial debonding of a 4-inch wafer-size of thin film nanoelectronics in a water bath at room temperature.

Movie S2. An experimental demonstration showing the interfacial debonding process by using a custom-modified peeling apparatus.

Movie S3. An experimental demonstration displaying letters and images on a LED screen in a pre-programmed manner by exploiting the transferred Si NM-based MOSFETs-based switches on the side wall.



Fig. S1



Fig. S2



Fig. S3



Fig. S4



Fig. S5



Fig. S6



Fig. S7



Fig. S8



Fig. S9



In Water

Fig. S10



Fig. S11



Fig. S12



Fig. S13



Fig. S14



Fig. S15



Fig. S16



Fig. S17



Fig. S18



Fig. S19



Fig. S20