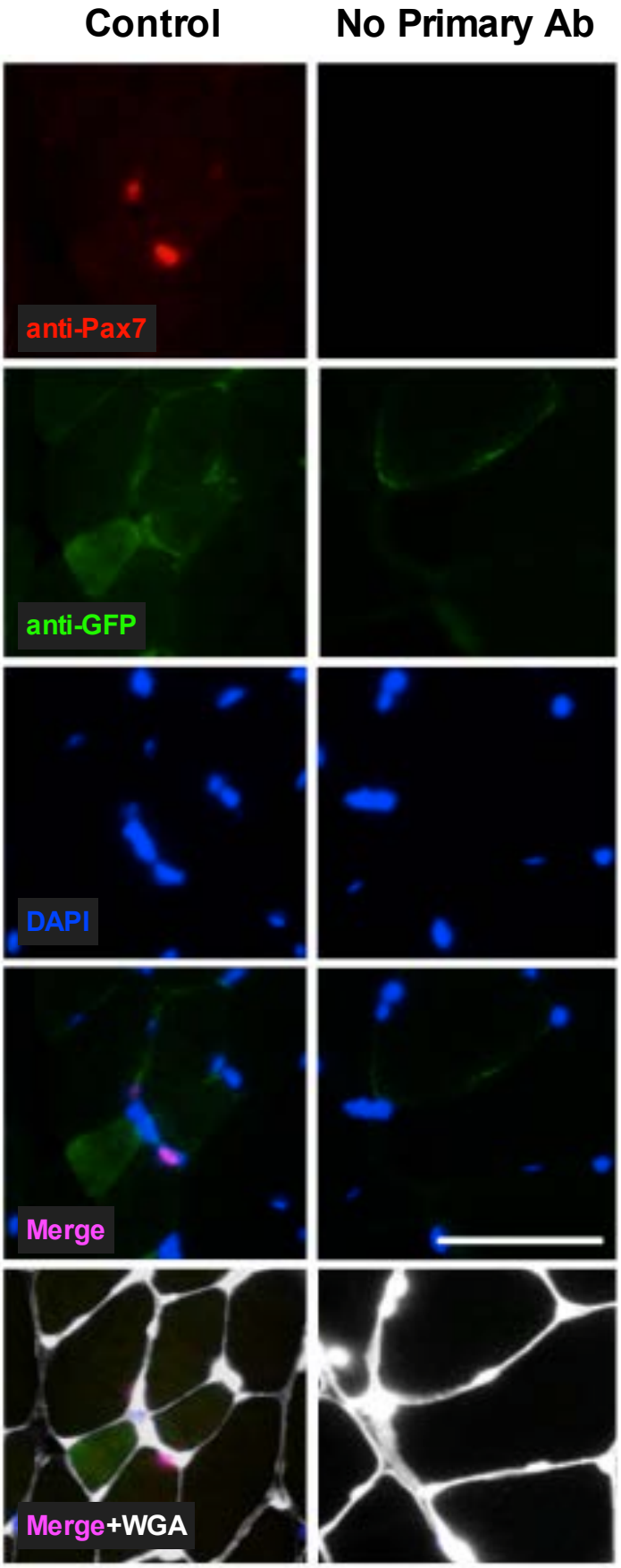
Figure S6


Control
No Primary Ab
anti-Pax7
anti-GFP
DAPI
Merge
Merge+WGA