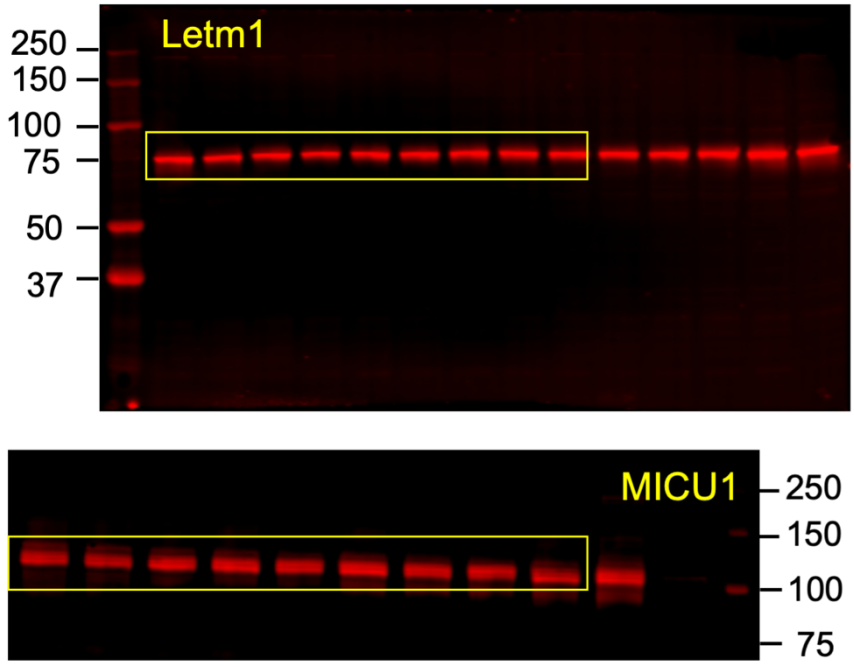
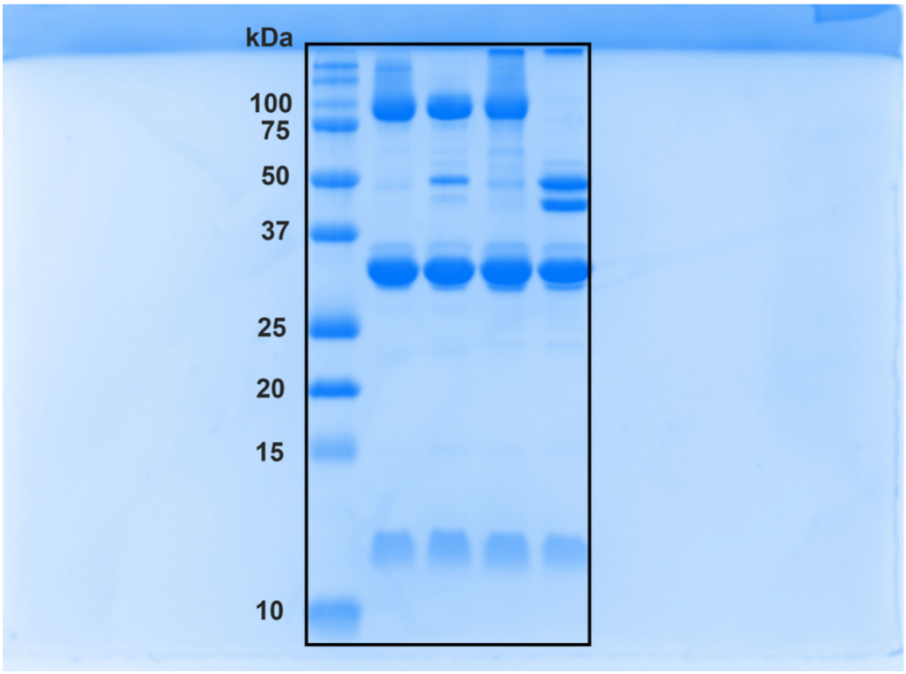
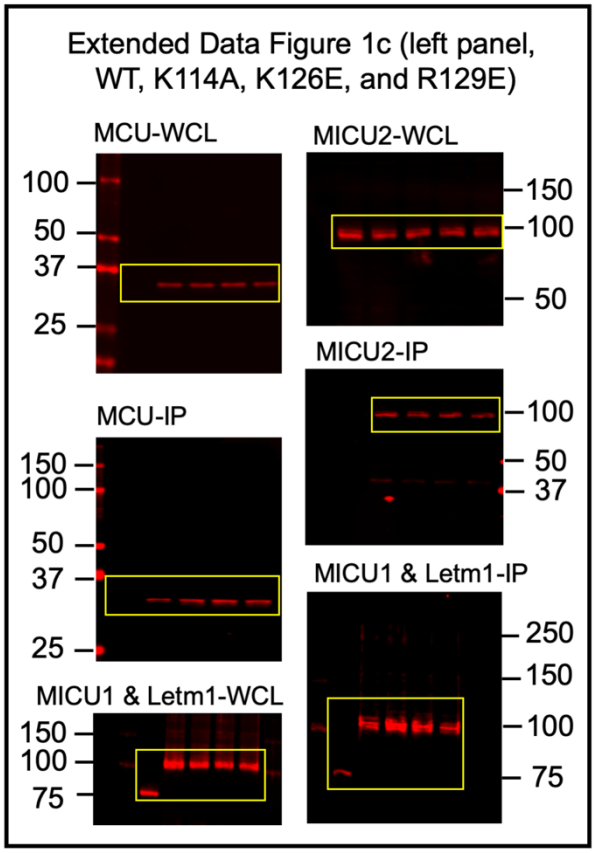
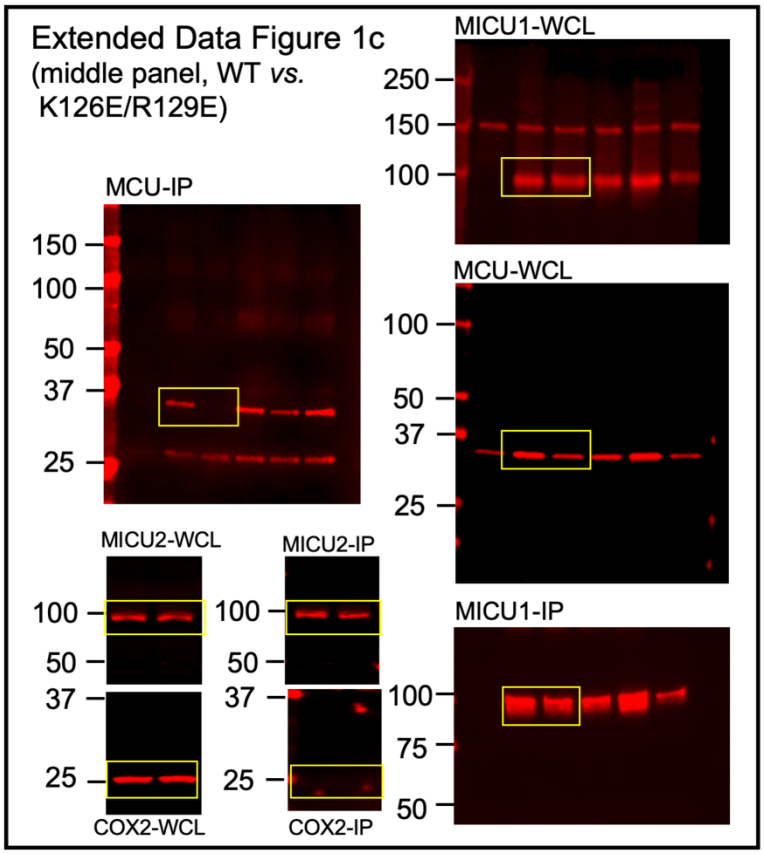
Figure 2c

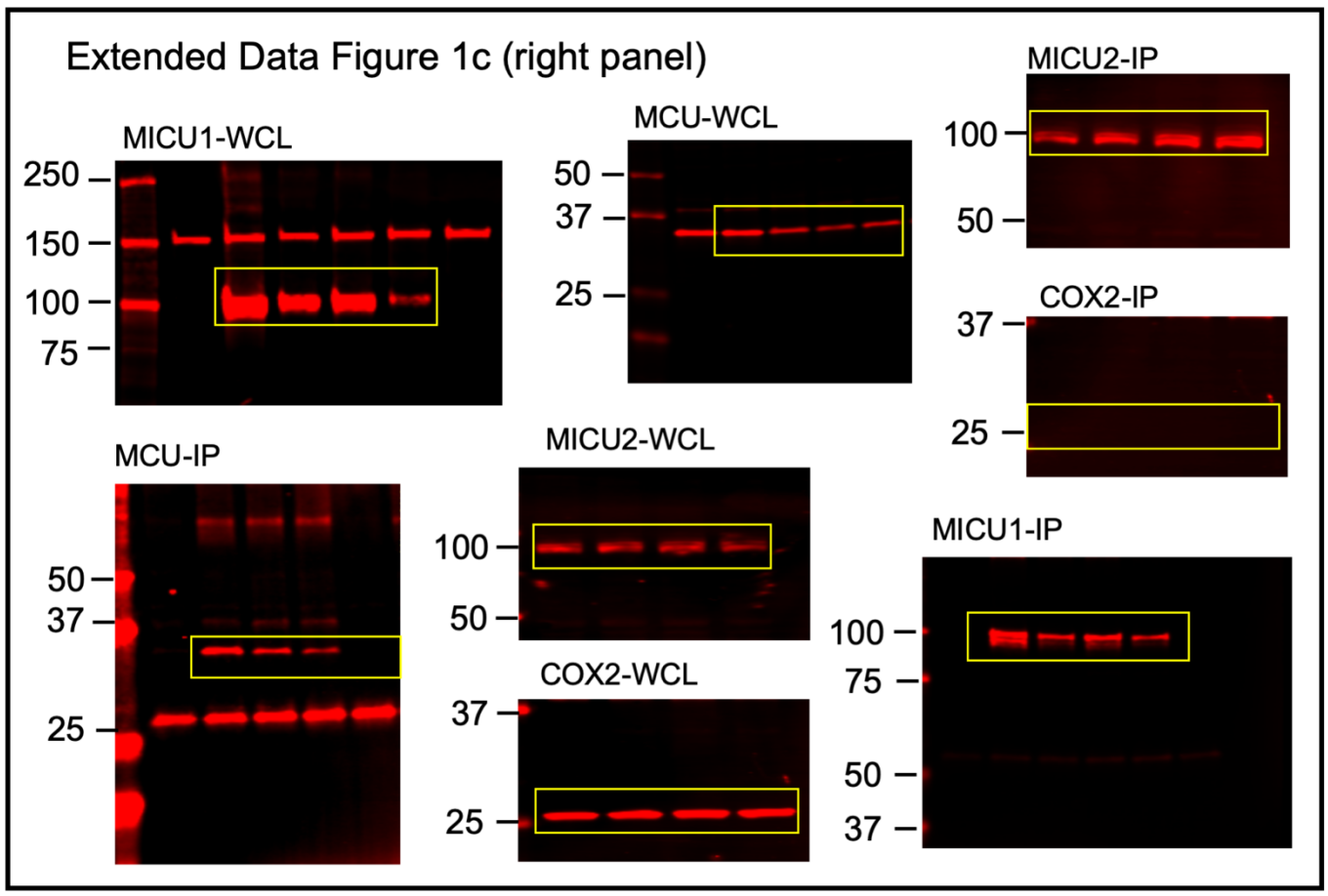


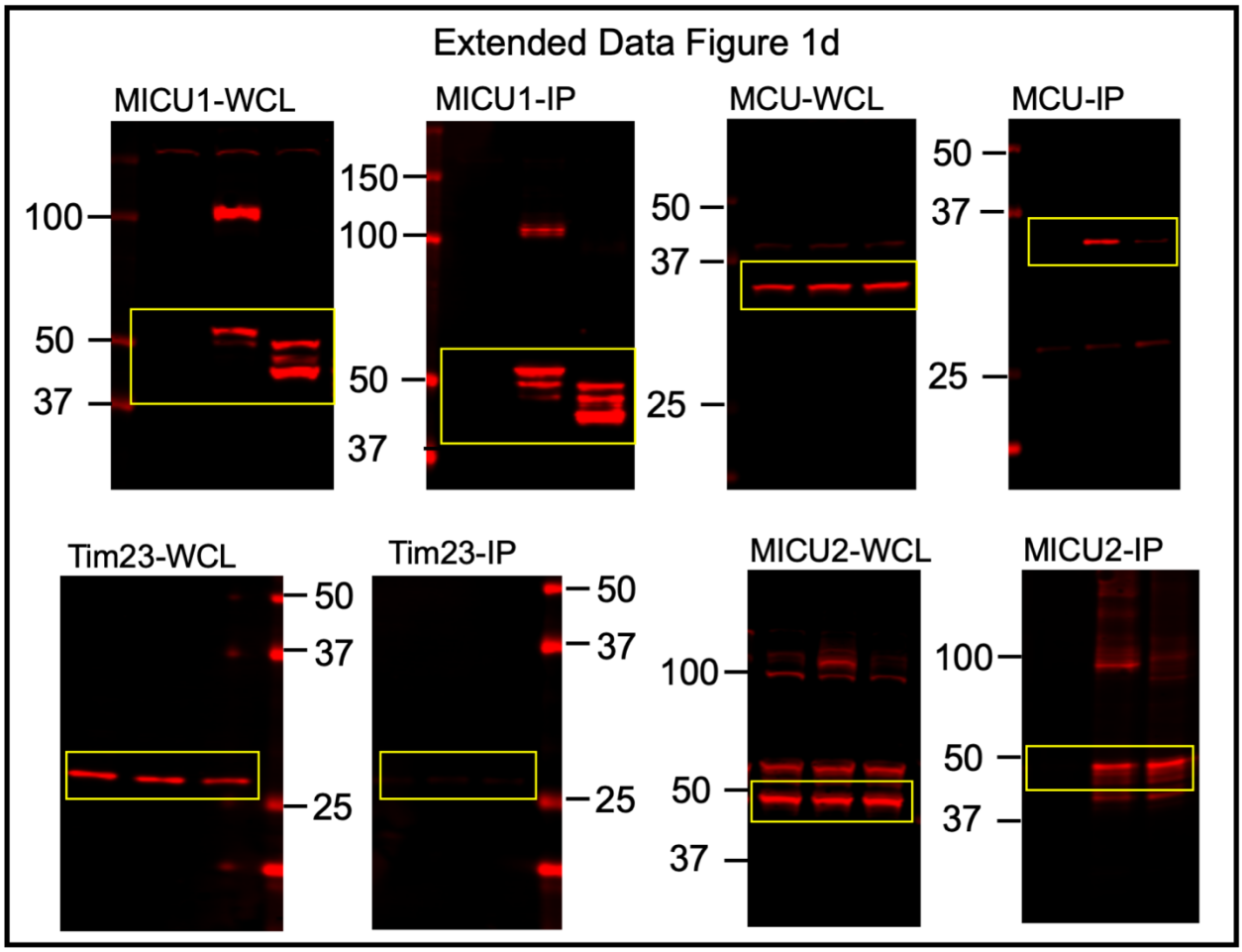
Extended Data Figure 1b

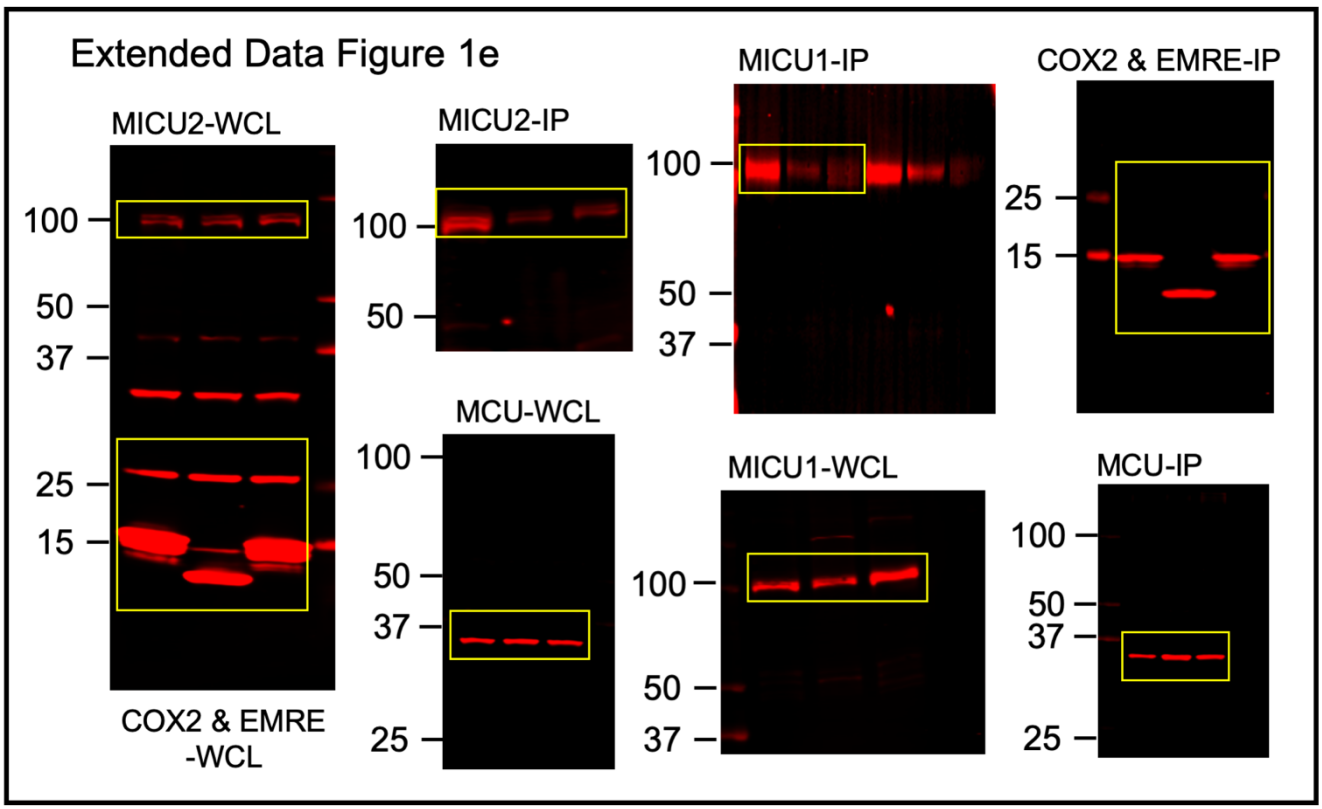


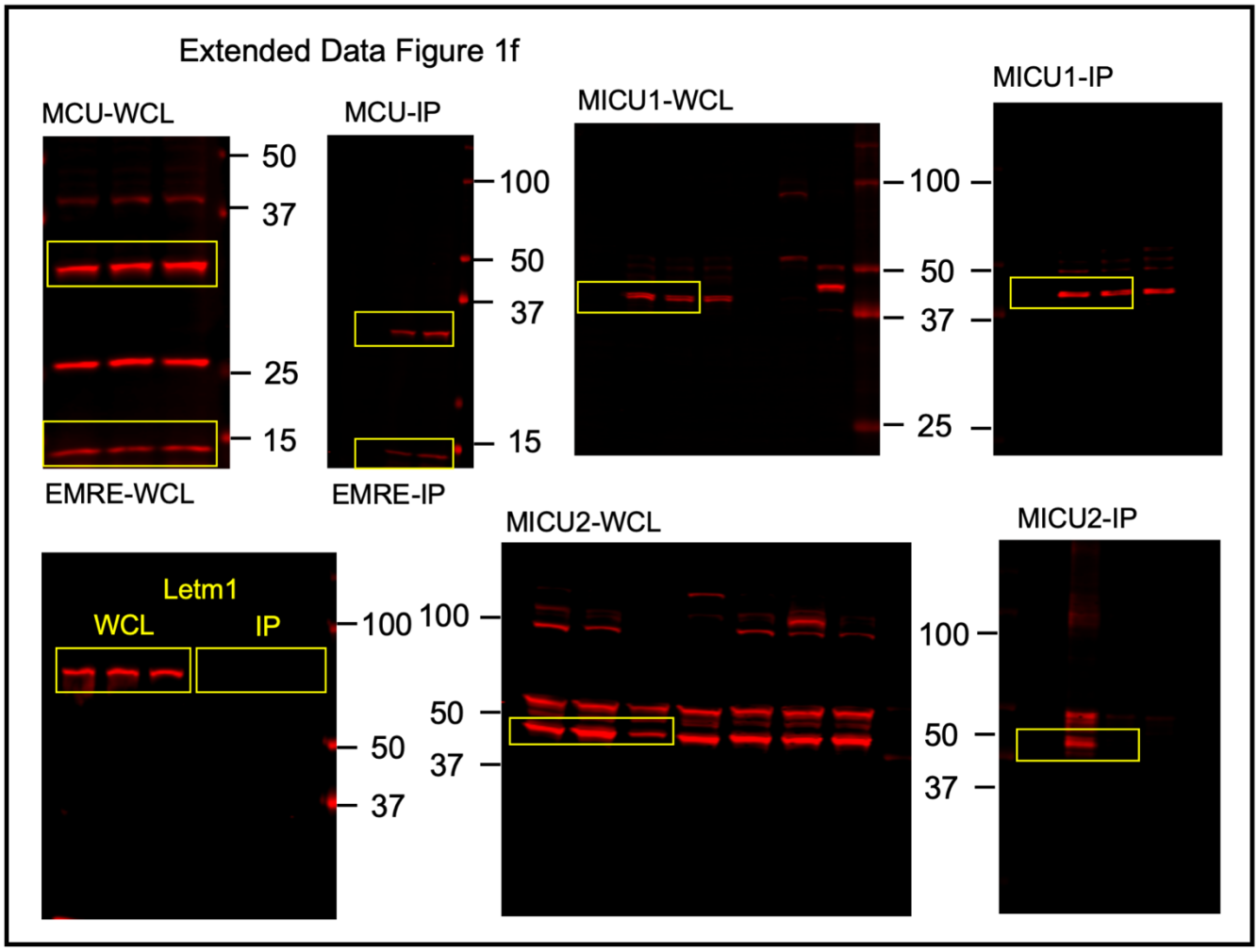

Extended Data Figure 1c (left panel, WT, K114A, K126E, and R129E)
MCU-WCL
MICU2-WCL
MCU-IP
MICU2-IP
MICU1 & Letm1-IP
MICU1 & Letm1-WCL

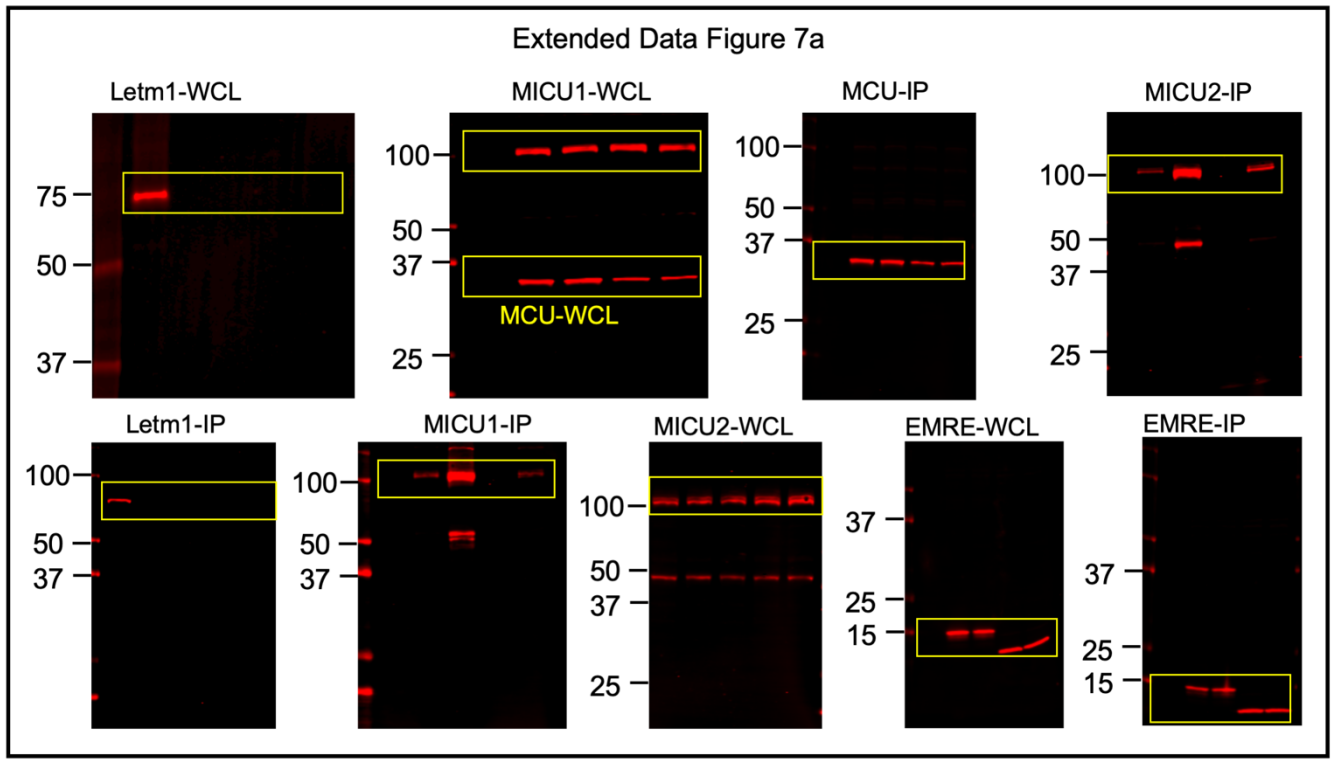

Extended Data Figure 1c (middle panel, WT vs. K126E/R129E)
MICU1-WCL
MCU-IP
MCU-WCL
MICU2-WCL
MICU2-IP
MICU1-IP
COX2-WCL
COX2-IP

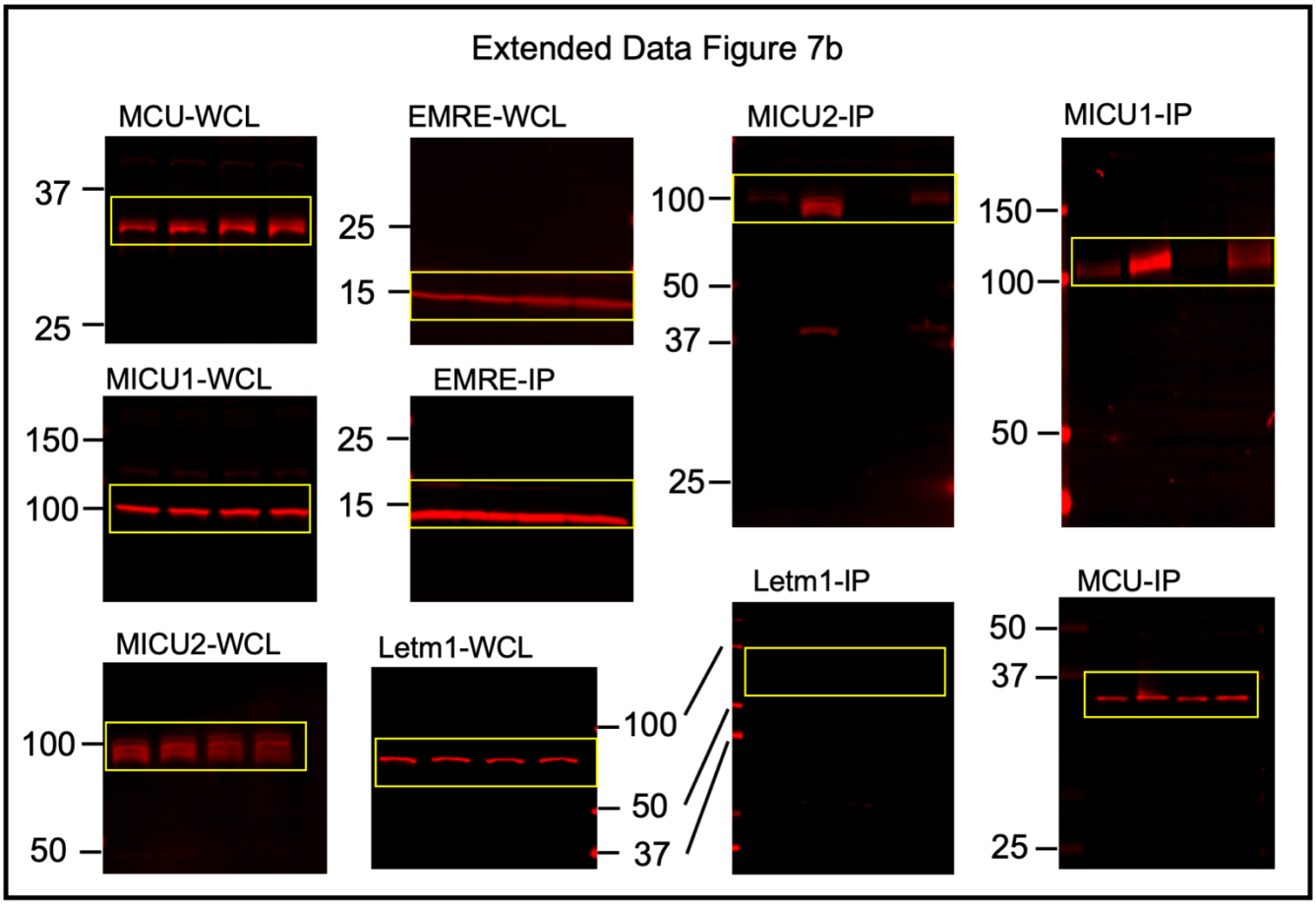

Extended Data Figure 1c (right panel)
MICU1-WCL
MCU-WCL
MICU2-IP
COX2-IP
MCU-IP
MICU2-WCL
MICU1-IP
COX2-WCL

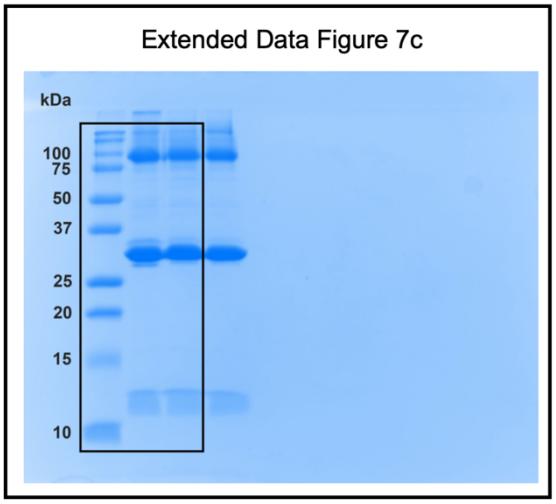

Extended Data Figure 1d
MICU1-WCL
MICU1-IP
MCU-WCL
MCU-IP
100
50
37
150
100
50
37
50
37
25
50
37
25
Tim23-WCL
Tim23-IP
MICU2-WCL
MICU2-IP
50
37
25
50
37
25
100
50
37
100
50
37

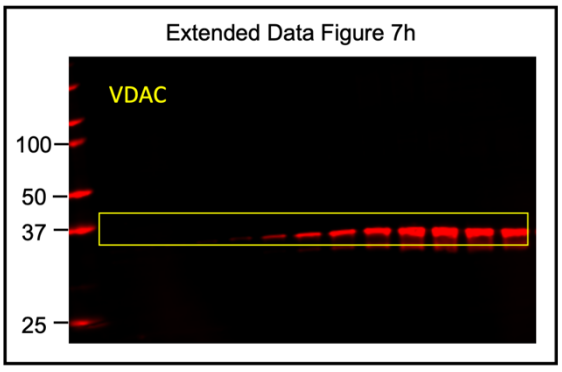

Extended Data Figure 1e
MICU2-WCL
MICU2-IP
MICU1-IP
COX2 & EMRE-IP
100
50
37
25
15
100
50
100
50
37
25
15
MCU-WCL
MICU1-WCL
MCU-IP
100
50
37
25
100
50
37
100
50
37
COX2 & EMRE
-WCL

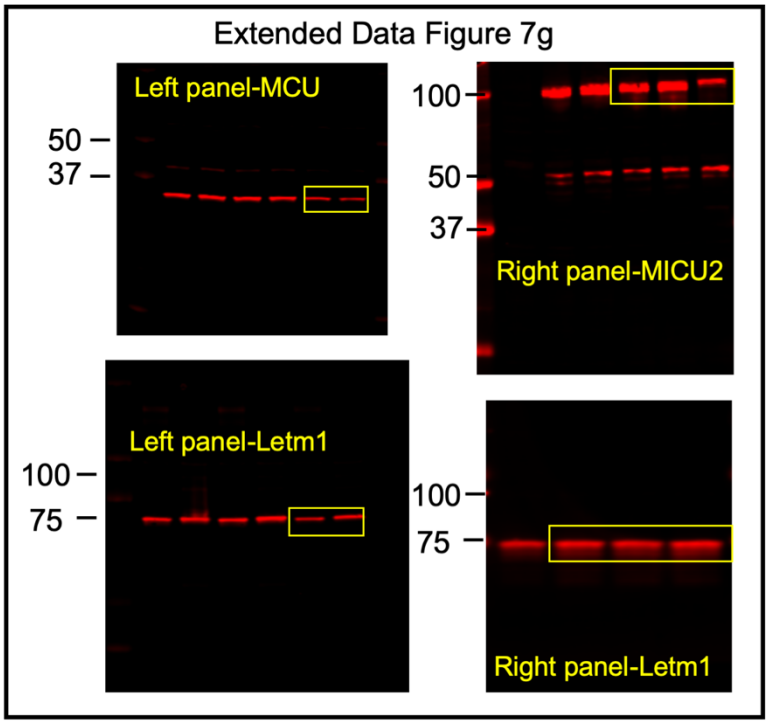

Extended Data Figure 1f
MCU-WCL
50
37
25
15
MCU-IP
100
50
37
15
MICU1-WCL
100
50
37
25
MICU1-IP
EMRE-WCL
Letm1
WCL
IP
100
50
37
EMRE-IP
MICU2-WCL
100
50
37
MICU2-IP
100
50
37


Extended Data Figure 7a
Letm1-WCL
75
50
37
MICU1-WCL
100
50
37
MCU-WCL
25
MCU-IP
100
50
37
25
MICU2-IP
100
50
37
25
Letm1-IP
100
50
37
MICU1-IP
100
50
37
MICU2-WCL
100
50
37
25
EMRE-WCL
37
25
15
EMRE-IP
37
25
15

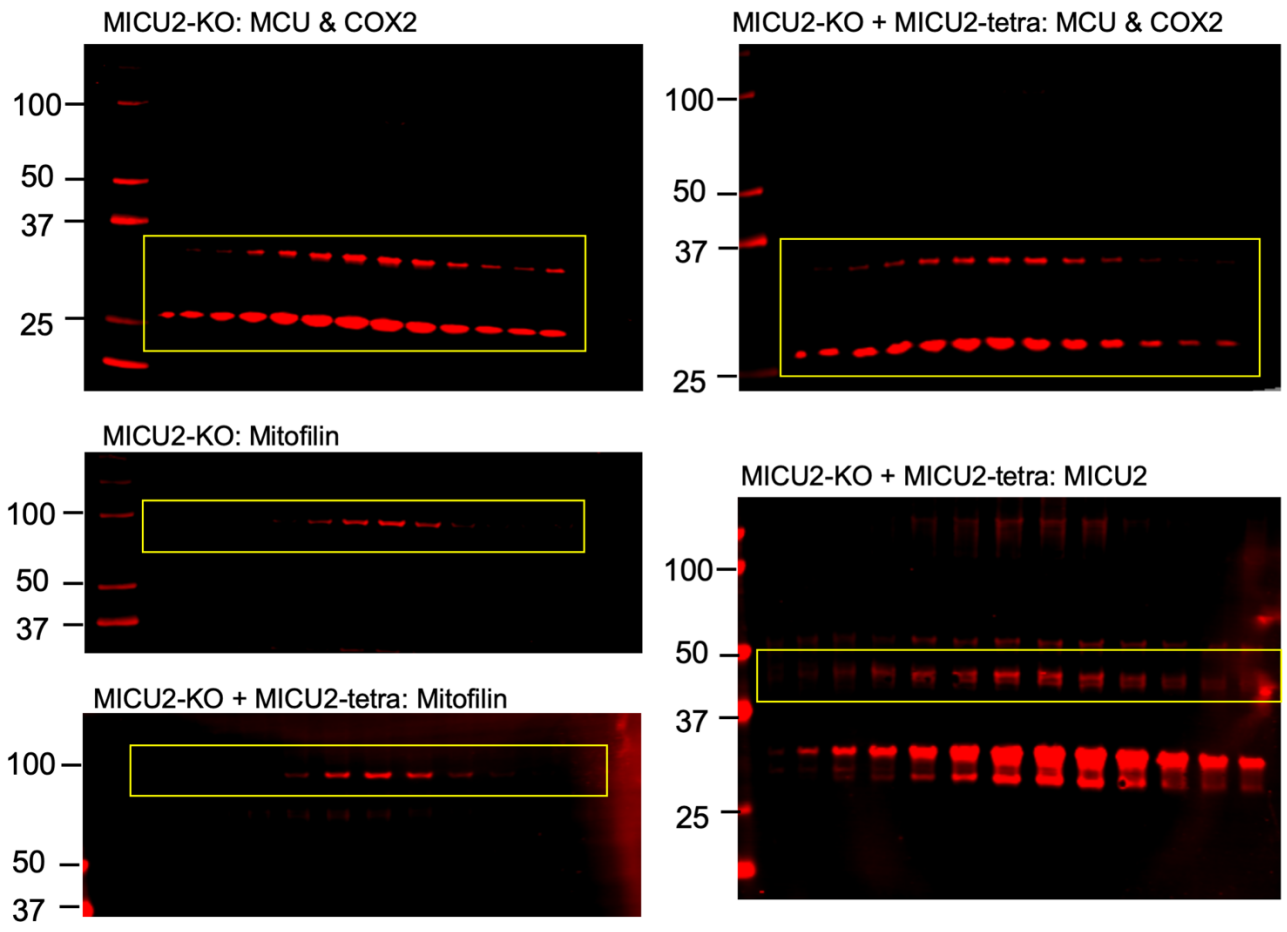
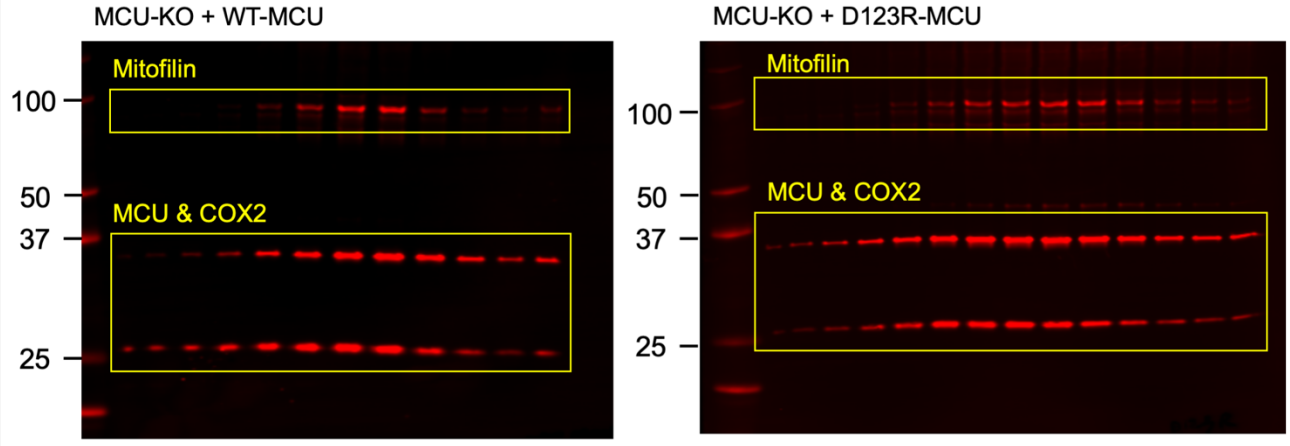

Extended Data Figure 7b
MCU-WCL
37
25
EMRE-WCL
25
15
MICU2-IP
100
50
37
25
MICU1-IP
150
100
50
MICU1-WCL
150
100
EMRE-IP
25
15
MICU2-WCL
100
50
Letm1-WCL
100
50
37
Letm1-IP
MCU-IP
50
37
25


Extended Data Figure 7c
kDa
100
75
50
37
25
20
15
10


Extended Data Figure 7h
VDAC
100
50
37
25


Extended Data Figure 7g
Left panel-MCU
50
37
100
50
37
Right panel-MICU2
Left panel-Letm1
100
75
100
75
Right panel-Letm1

Extended Data Figure 7j



Extended Data Figure 7l