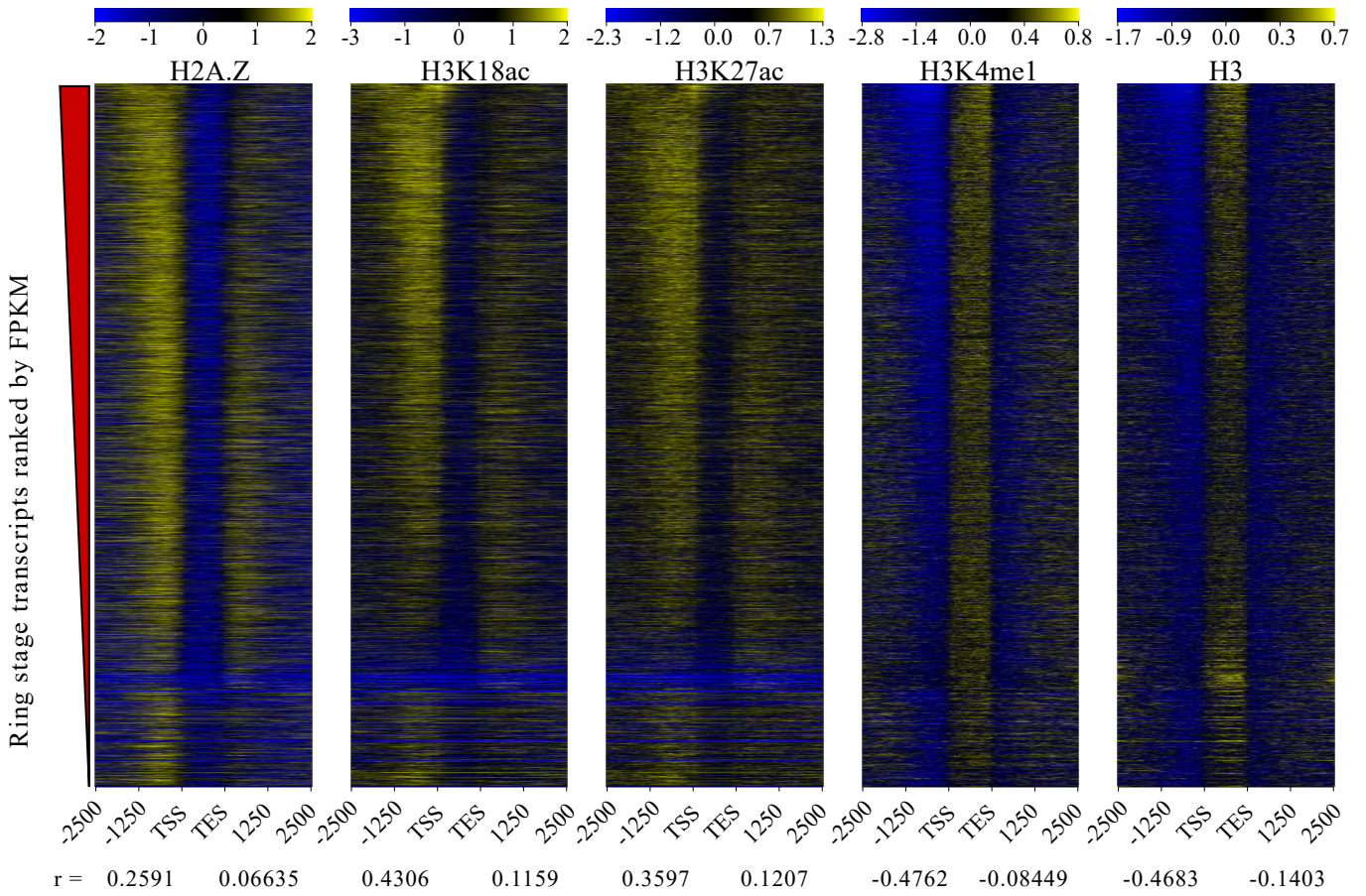Additional figure S5

Log2 (ChIP/input)