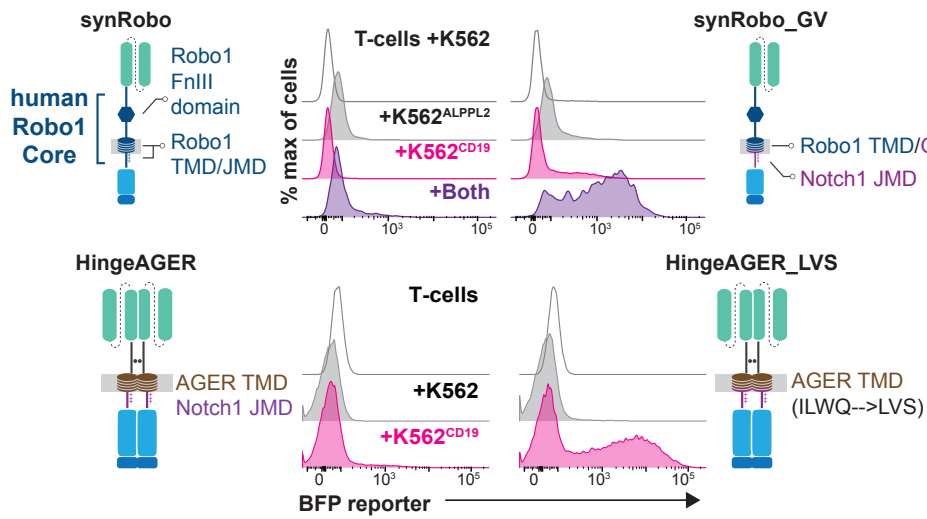
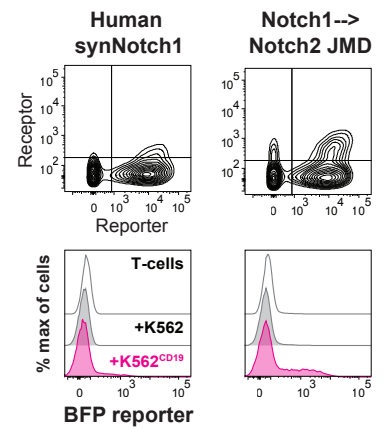


C Improve non-functional TMD through residue substitution



D Improve non-functional SNIPR through JMD swap



E Modular SNIPR assembly around a CLSTN2 core

