

Multimedia Appendix 1

Figure S1. The general architecture and technologies used in the system development.

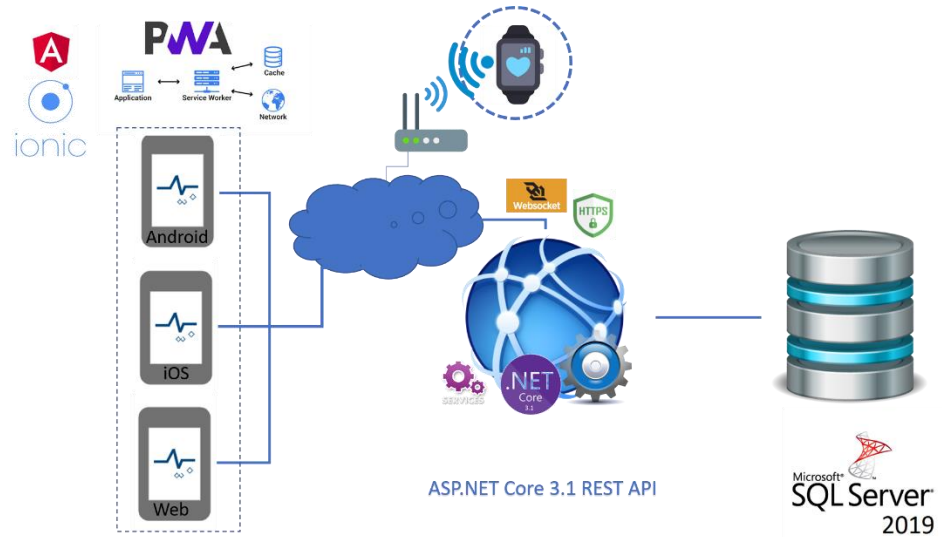
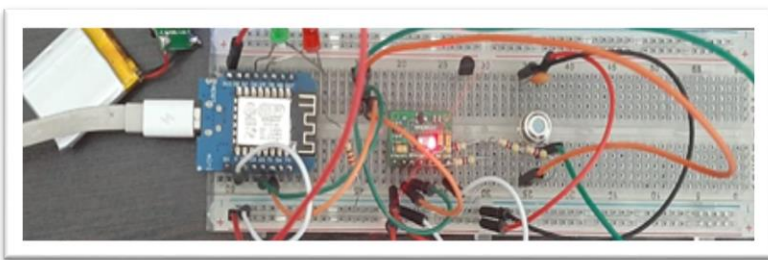
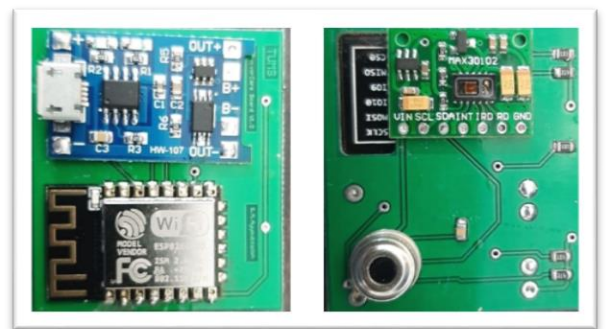


Figure S2. Process of hardware design. (A) Hardware designed on the bread board, (B) soldered parts on the board, and (C) assembled hardware.

(A)



(B)



(C)



Figure S3. The wristband. (A) Preliminary design of the wristband case and (B) the final wristband.

(A)

(B)

